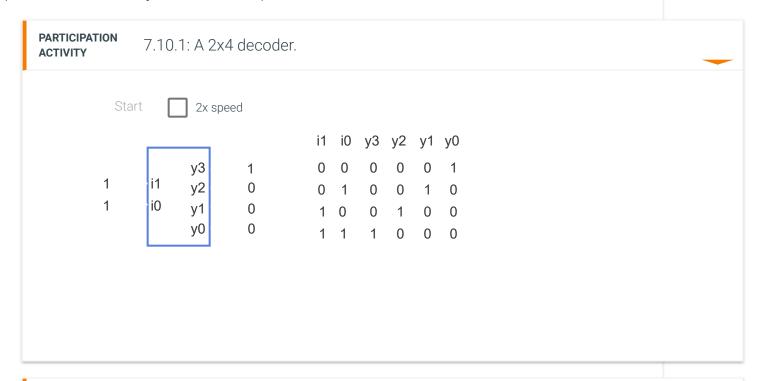
7.10 Decoders

Basics

A **decoder** is a combinational circuit that converts N inputs to a 1 on one of 2^N outputs. A **2x4 decoder**, spoken as "2 to 4 c converts two inputs to a 1 on exactly one of four outputs.



 PARTICIPATION ACTIVITY
 7.10.2: 2x4 decoder.

 Consider a 2x4 decoder.

 1) If i1i0 = 00, then y1 = _____.

Check Show answer

2) If i1i0 = 01, then y1= ____.

Check Show answer

3) i1i0 = ____ configures the decoder to output y0 = 0, y1 = 0, y2 = 0, and y3 = 1.

Check Show answer

4) How many outputs are set to 1 at any given time?

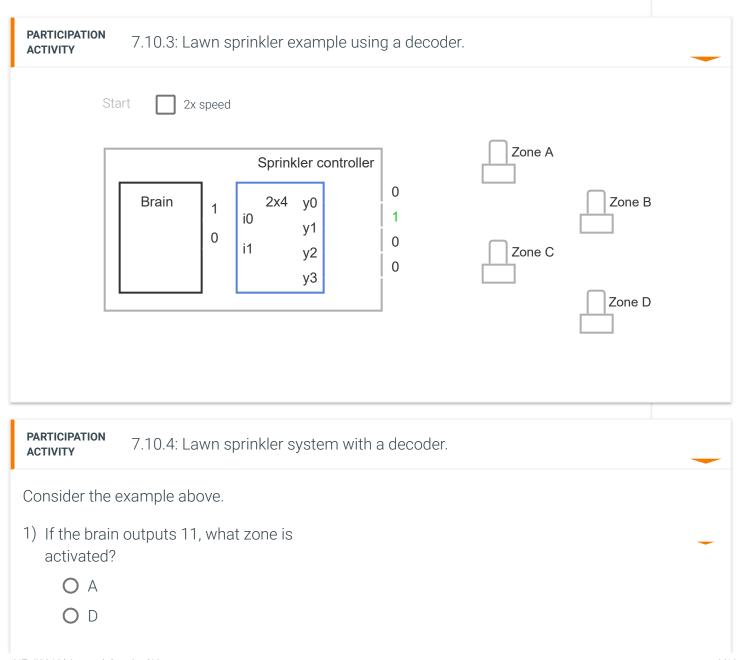
Type: 0, 1, 2, 3, or 4

Check Show answer

5) i1i0 = ____ configures the decoder to output y0 = 0, y1 = 0, y2 = 1, and y3 = 1. Type: 00, 01, 10, 11, or ** if not possible.

Check Show answer

A lawn sprinkler system may have multiple zones. A sprinkler controller activates only one zone at a time, due to limited inc. The brain of the controller, typically a small computer, may encode the active zone in binary on output pins, to save pins: If a zones, only 3 pins are needed, while 4 zones need only 2 pins. A decoder can convert the binary encoded zone into the active appropriate zone.



- 2) What brain output values will activate all zones at once?
 - O 11
 - O No such values
- 3) If a system has 32 zones instead of 4, how many outputs would the brain need?
 - 0 4
 - **O** 5
 - **O** 32

Decoder equation and circuit

A 2x4 decoder has four outputs. Each output's behavior is easily converted to an equation and then to a circuit.

7.10.5: Each decoder output is easily converted to an equation and circuit.

	y0
i0	y1
i1	у2
	уЗ

Start







y0

y2

уЗ

i1	i0	уЗ	y2	y1	y0

2x speed

1 1 1 0 0 0 y3 = i1i0

PARTICIPATION ACTIVITY	7.10.6: Decoder design.	_
1) How many have?	y inputs does a 2x4 decoder	~
Check	Show answer	
2) How many decoder ha	y outputs does a 2-input ave?	~
Check	Show answer	
3) How many decoder re	y AND gates does a 2x4 equire?	—
Check	Show answer	
4) How many decoder re	y OR gates does a 2x4 equire?	~
Check	Show answer	

Decoder sizes

Other size decoders can be designed similarly.

PARTICIPATION 7.10.7: Designing other sized decoders. **ACTIVITY** Start 2x speed y0 y0 у1 у1 y0 = i' y1 = i y0 y0 у1 i1 y1 y2 i0 i2 уЗ у2 y4 i1 у5 уЗ y6 y4 у7 i2 у5 y0 = i2'i1'i0'y1 = i2'i1'i0у6 y2 = i2'i1i0' y3 = i2'i1i0у7 y4 = i2i1'i0'

y5 = i2i1'i0 y6 = i2i1i0'y7 = i2i1i0

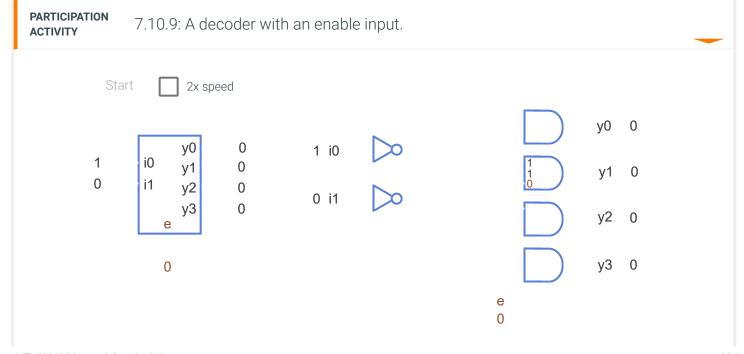
PARTICIPATION ACTIVITY	7.10.8: Various sized decoders.	_
	inputs are required for a th 8 outputs?	-
Check	Show answer	
Officer	Onow answer	
	inputs are required for a th 2 outputs?	•
Check	Show answer	
3) How many decoder ha	outputs does a 2-input eve?	-
Check	Show answer	
4) How many decoder ha	outputs does a 4-input ive?	•
Check	Show answer	
	AND gates does a 3x8 quire?	-

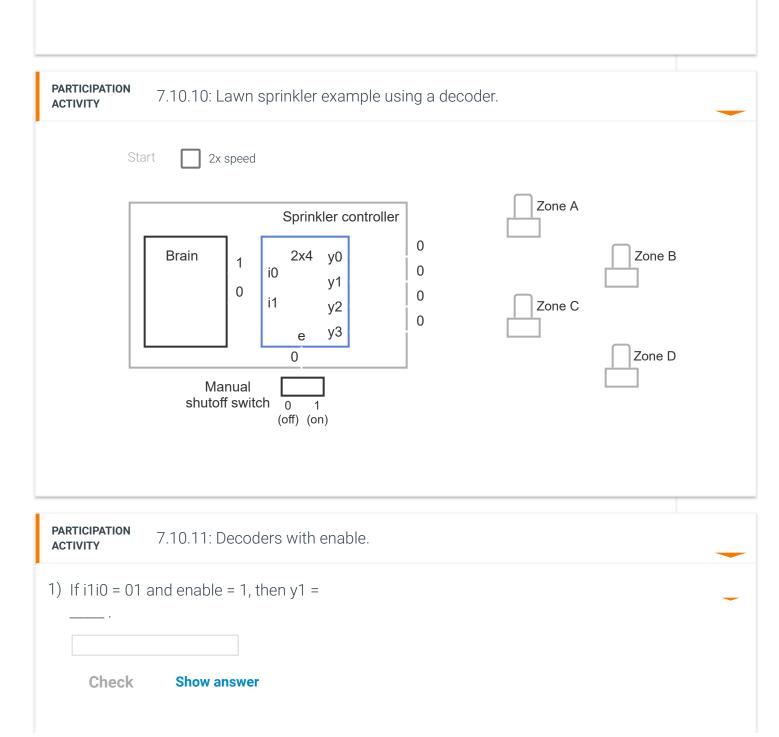
Check	Show answer		
6) How many decoder re-	OR gates does a 5x32 quire?		
Check	Show answer		

Decoder with enable

Some decoders have an additional input called an **enable input** that when 0 sets all outputs to 0s, and when 1 enables the common normal behavior.

A decoder's equations and circuit are easily extended for an enable input by including the enable in each AND.





Provide feedback on this section