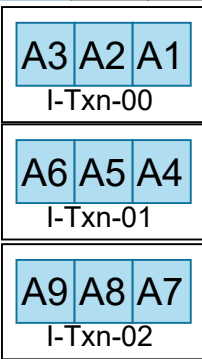
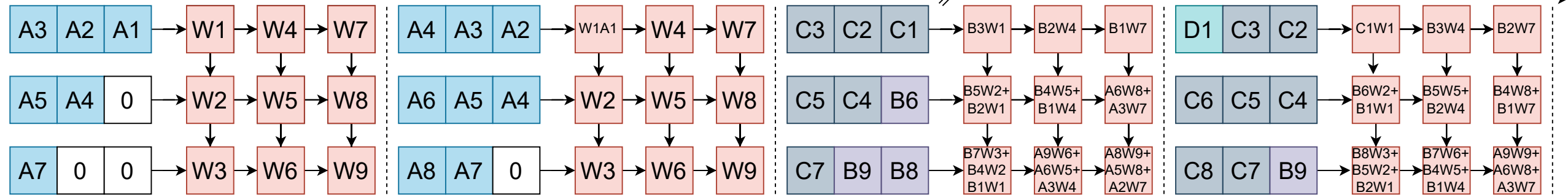


t = 0

t = 1

t = 6

t = 7



Placing I-Txn-Var in the SA Grid

$kernel_size = k \times k$

$SA_Size = \#MACs / kernel_size$

$Space_Placement \leftarrow \lfloor (Var/k) / SA_Size \rfloor$

$Time_Placement \leftarrow (Var \% k)$

Channels can be parallelly executed, and accumulated

