

# Curriculum Vitae/Resume

## Panagiotis Papanikolaou

[panagip@umich.edu](mailto:panagip@umich.edu)

Program: Computer Science & Engineering Ph.D

### RESEARCH INTERESTS

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#### Electronic Design Automation (EDA)

- Synthesis
- Verification

#### Architecture for Emerging Technologies

- Cryogenic electronics
- Quantum computing
- Flexible electronics

### EDUCATION

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#### University of Michigan

*PhD in Computer Science and Engineering*

Sep. 2024 – Present

*Ann Arbor, MI*

#### University of Patras

*BS/MS in Computer Engineering and Informatics*

Oct. 2018 – Jul. 2023

*Patras, GR*

- 2nd highest grades in graduating cohort.

### WORK EXPERIENCE

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#### Visiting Scholar

*CSE, University of Michigan*

Apr. 2024 – Aug 2024

*Ann Arbor, MI*

- Designed hardware for quantum error correction bandwidth management.
- Investigated superconducting memory architecture.

#### Research Assistant

*ECE/CEID, University of Patras*

Oct. 2023 – March 2024

*Patras, GR*

- Implemented high-performance peripheral interface modules on FPGA.

#### Intern ML engineer

*Irida Labs*

Mar. 2023 – May 2023

*Patras, GR*

- Trained vision systems for industrial control applications.

### PUBLICATIONS

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#### Synthesis of Resource-Efficient Superconducting Circuits with Clock-Free Alternating Logic

*\*J. Volk, \*P. Papanikolaou, G. Zervakis, G. Tzimpragos, DAC 2024 (\* Equal contribution)*

#### Evolutionary Approximation of Ternary Neurons for On-sensor Printed Neural Networks

*V. Mrazek, A. Kokkinis, P. Papanikolaou, Z. Vasicek, K. Siozios, G. Tzimpragos, M. Tahoori, G. Zervakis, ICCAD'24*

### SKILLS

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**Programming Languages:** C/C++, Haskell, Python, MATLAB, JavaScript, Bash, Verilog, VHDL

**Tools:** Microwind, Vivado/Vitis, Synopsys Design Compiler, Yosys/ABC, Microchip Studio

**Languages:** Greek, English