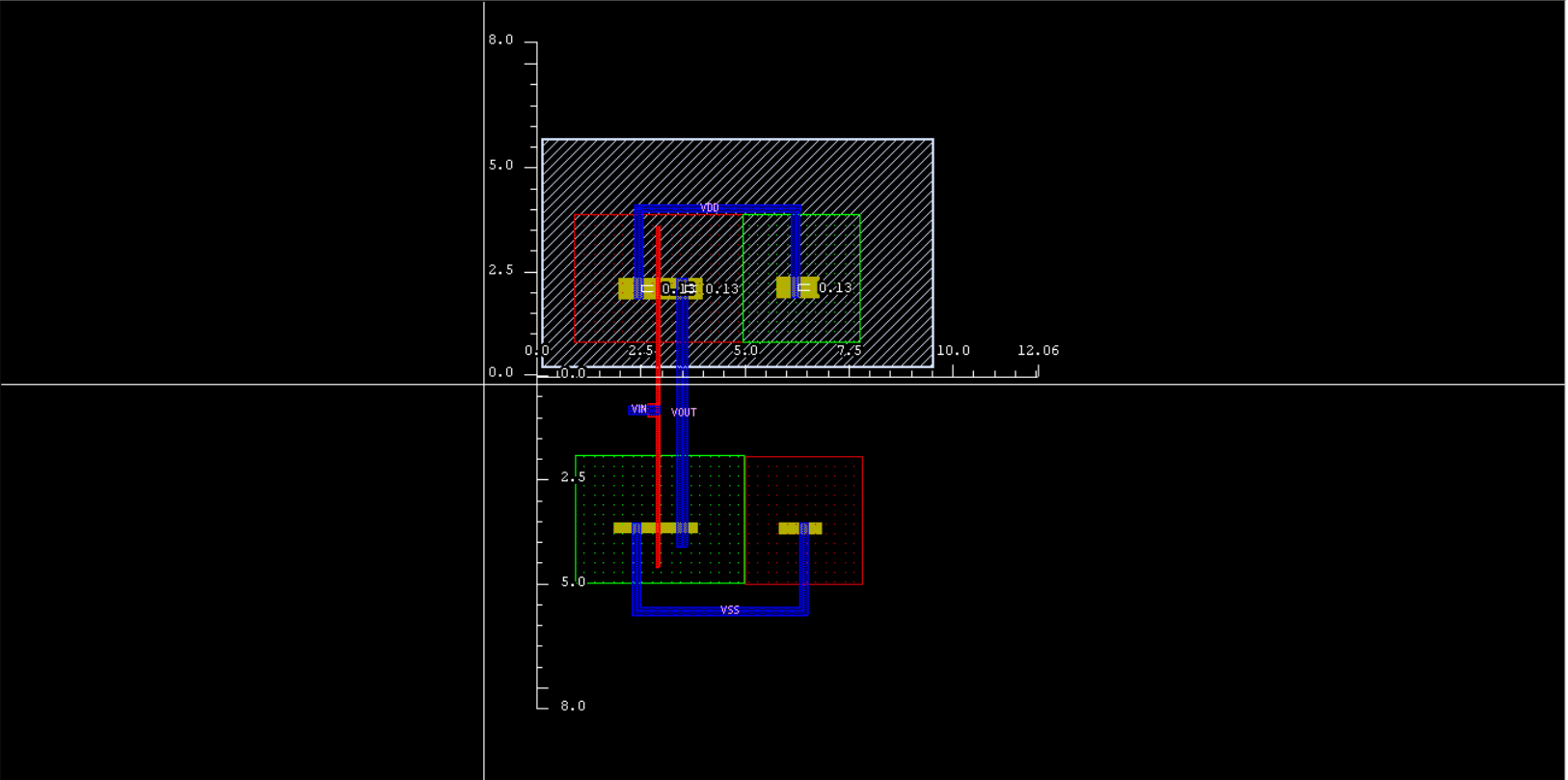
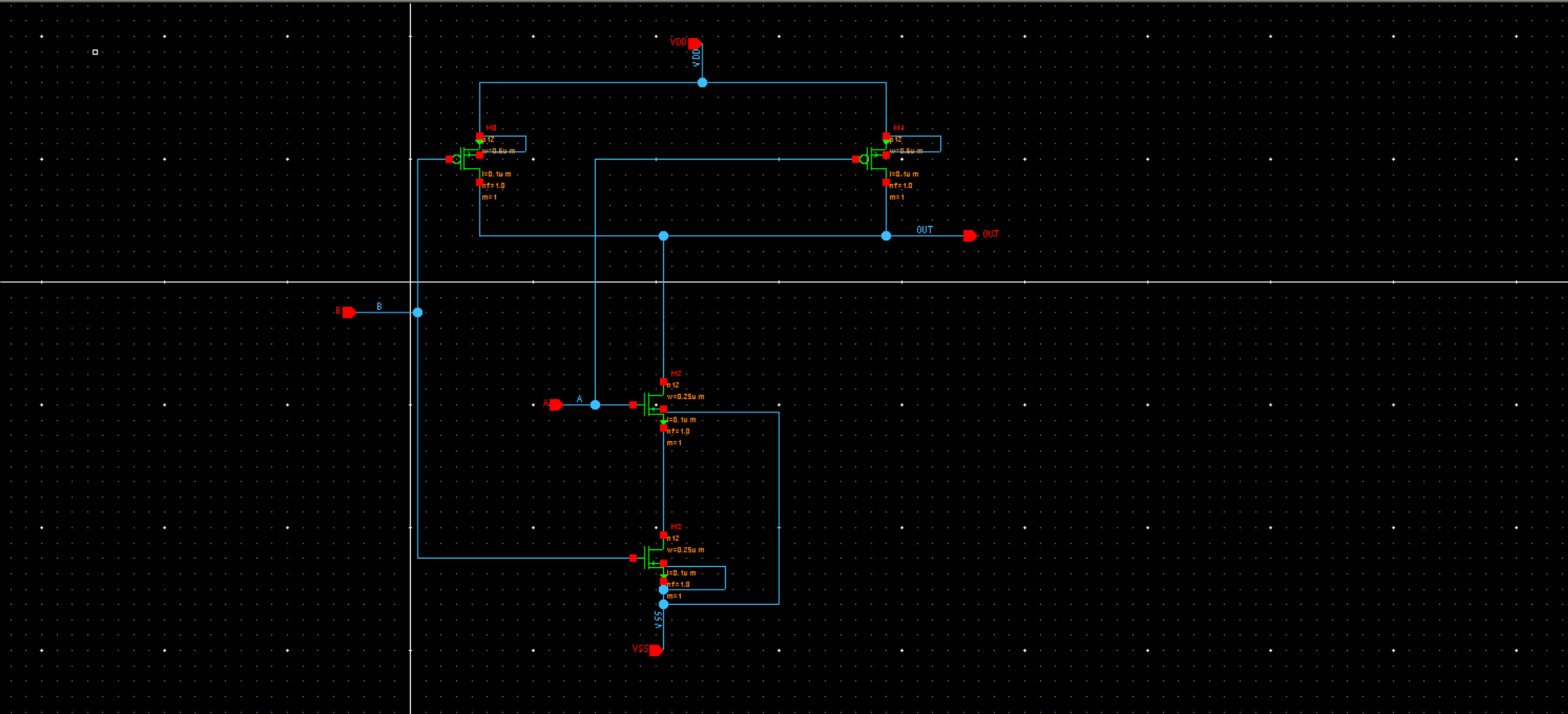
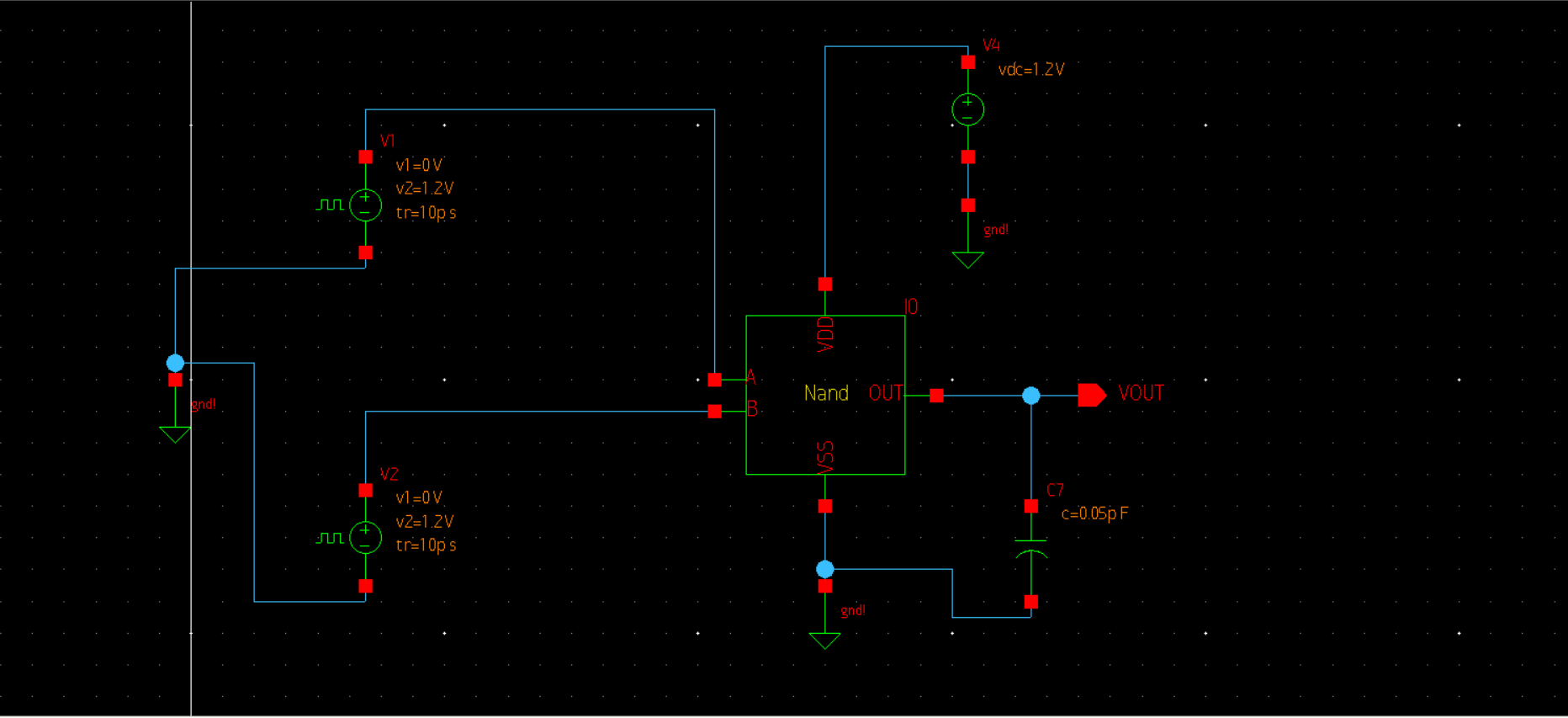
Inverter layout



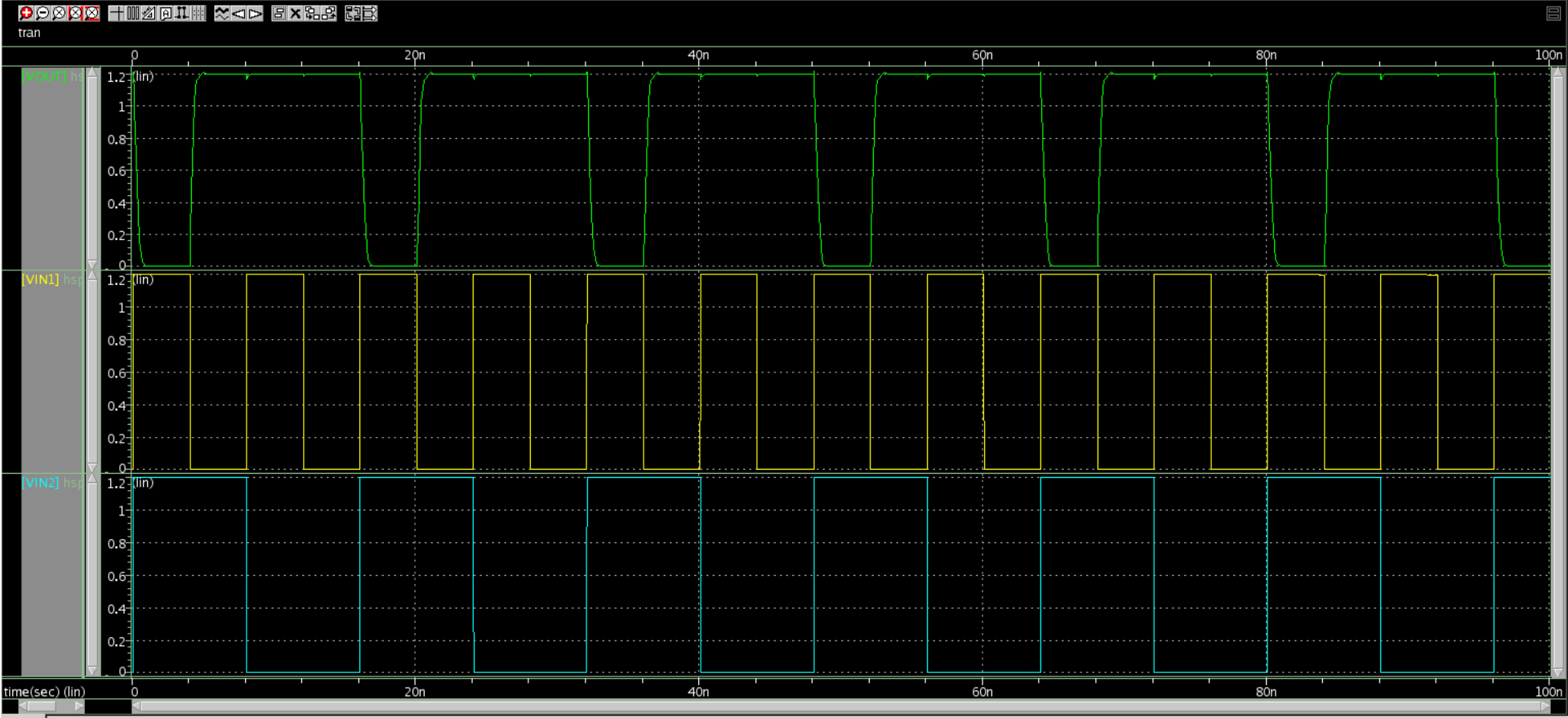
NAND schematic



NAND testbench



NAND transient



NAND layout

