

RIGHT_POTENTIOMETER

1

Pocke

A

VDD :

C

I2C2_S

I2C2_S

B

SERV

C

USB1

USB1

V

D

AGND

LEFT_POT

RIGHT_PO

E

Mechar



MGT1



MGT2



MC

1

[illegible]

The first diagram shows a capacitor model with a red circle representing the capacitor, connected between a green line labeled '3T3' and a red line labeled 'MGT4'. The bottom of the red line is connected to a red ground symbol labeled 'CGND'.

The second diagram shows an inductor model represented by a 2x3 grid of six red squares, each containing a red 'X'. The top row of squares is connected to a green line, and the bottom row is connected to a red line, which is then connected to a red ground symbol labeled 'CGND'.

The third diagram shows a resistor model with a red zigzag line labeled 'R5' and a value of '0.1'. It is connected between a green line labeled 'CGND' and a red line labeled 'GND'.

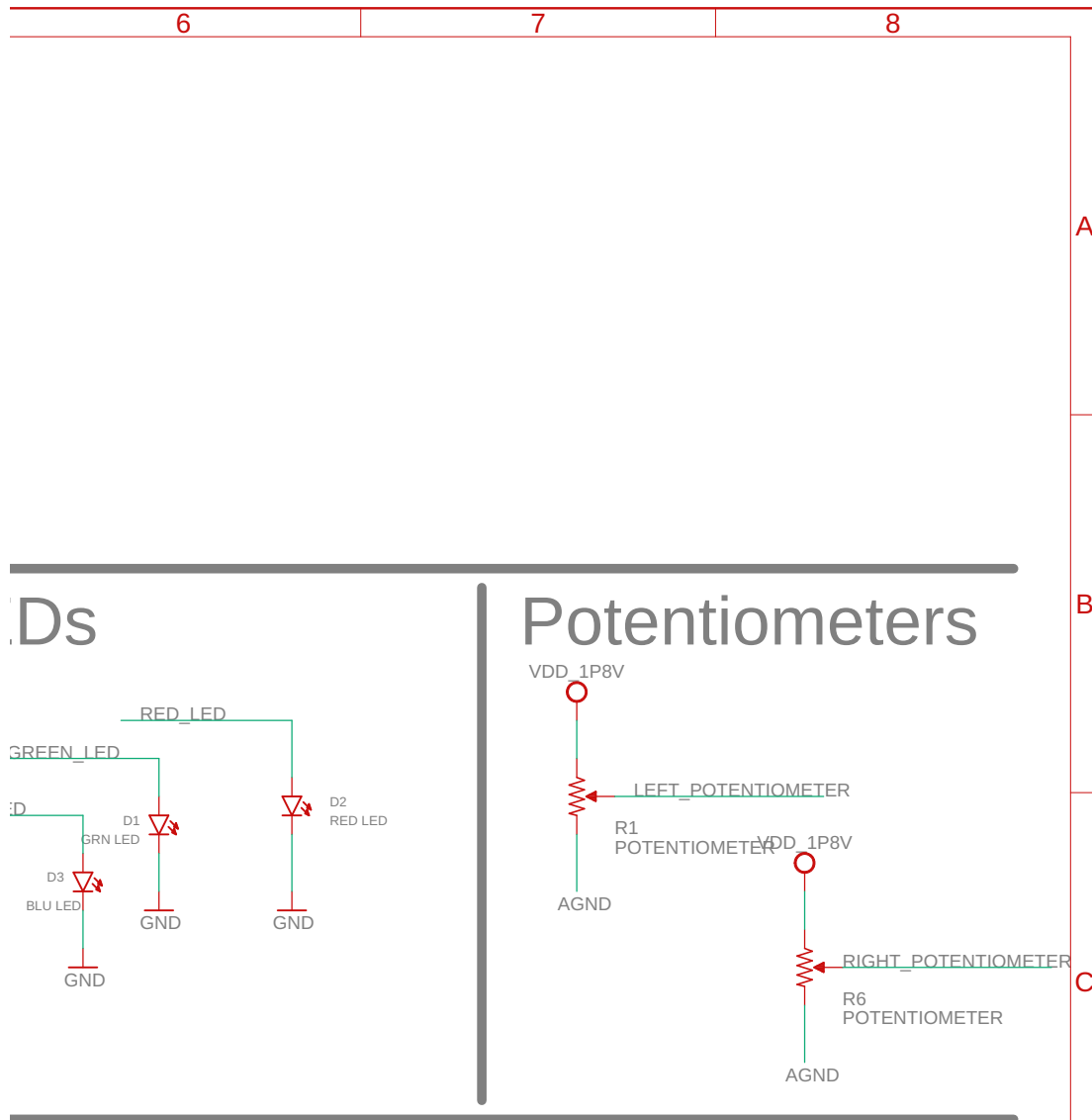
Button

The diagram illustrates a button interface circuit. It features two push buttons, SW1 and SW2. SW1 is connected to a 3.3V supply (VDD_3P3V) through a 1k resistor (R4) and its other terminal to the RESET_BUTTON signal line. SW2 is connected to the RIGHT_BUTTON signal line and its other terminal to ground (GND). Both buttons are shown in their open state.

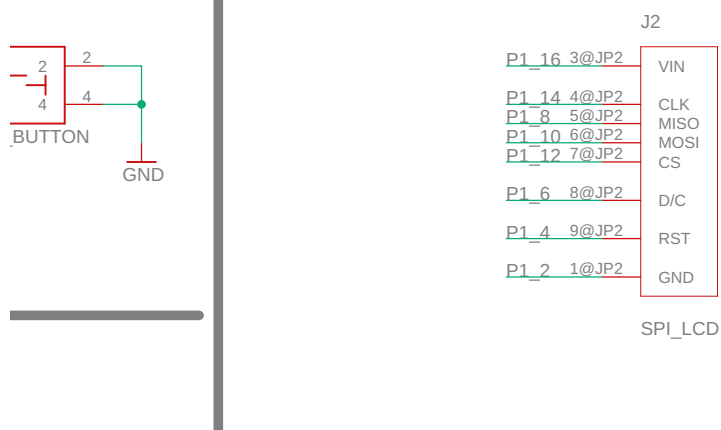
The schematic diagram illustrates the USB connector wiring for the SB module. The USB connector (J1) is shown with the following connections:

- Pin 1: VDD 5V
- Pin 2: VBUS
- Pin 3: DM
- Pin 4: DP
- Shield: SH1*2, connected to CGND

A capacitor C1 (220uF) is connected between VDD 5V and GND.



SPI LCD Display



ENGI 301 - Combination Lock

etch-a-sketch

12/10/22 8:39 AM

Sheet:

Rev 1