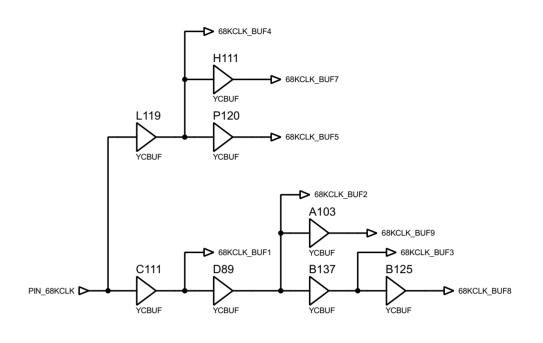
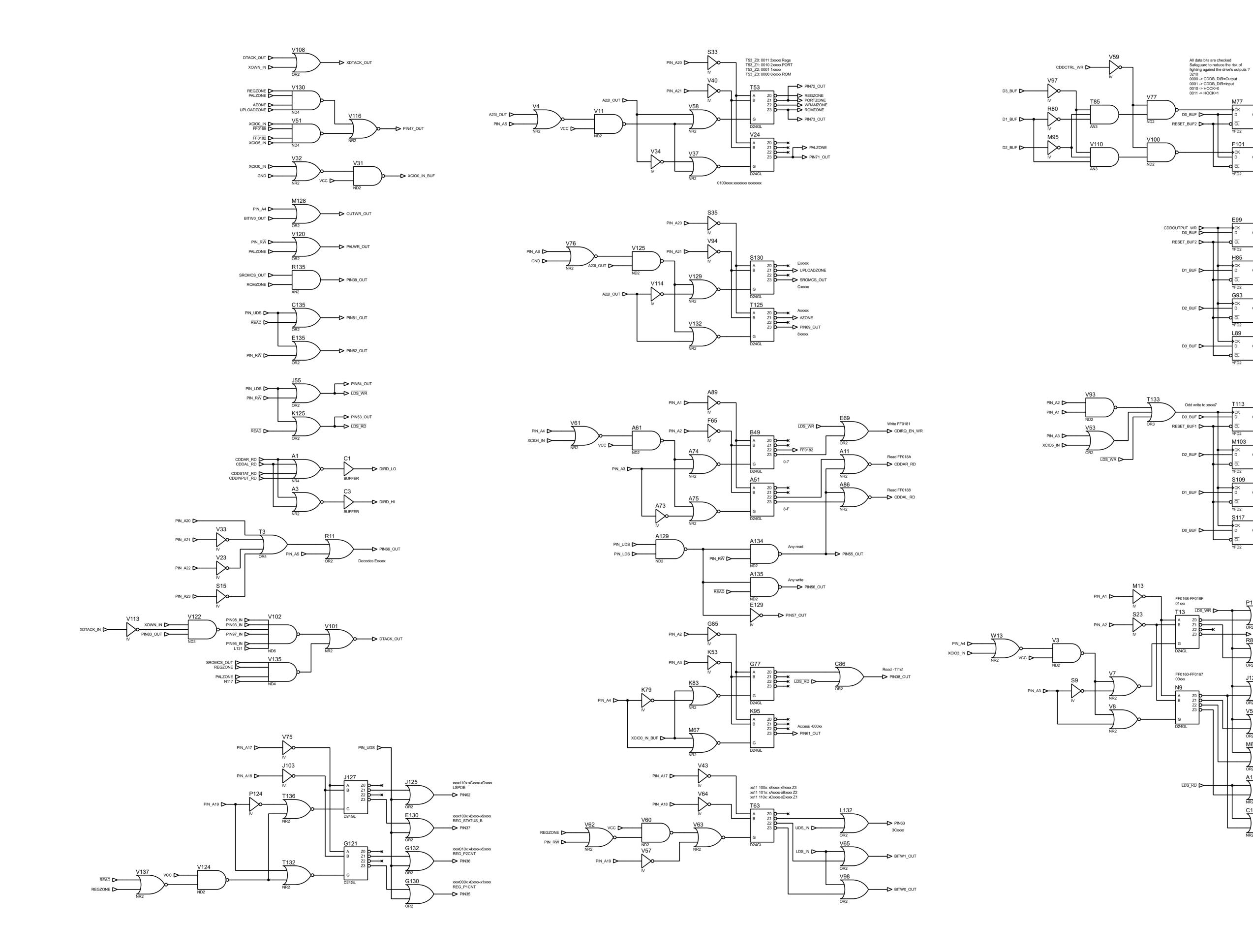


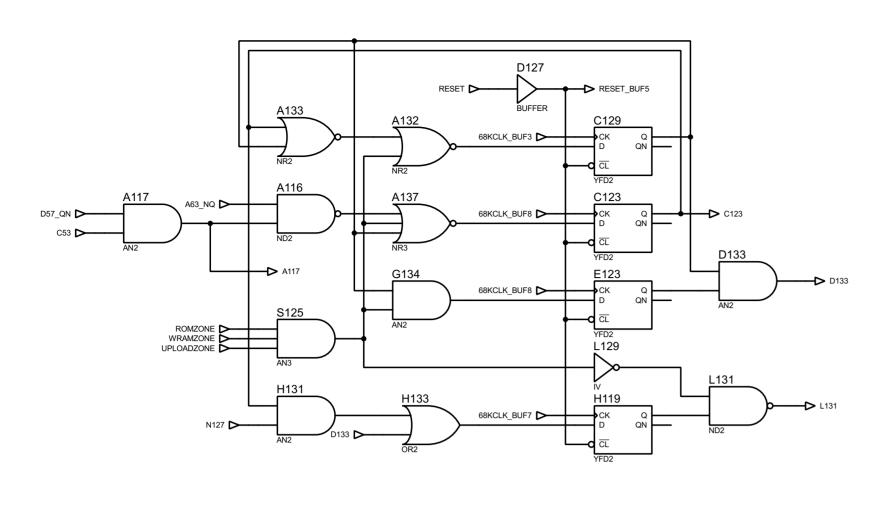
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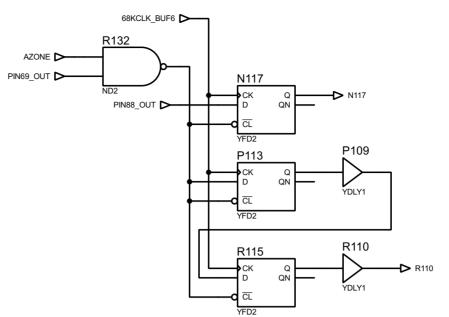


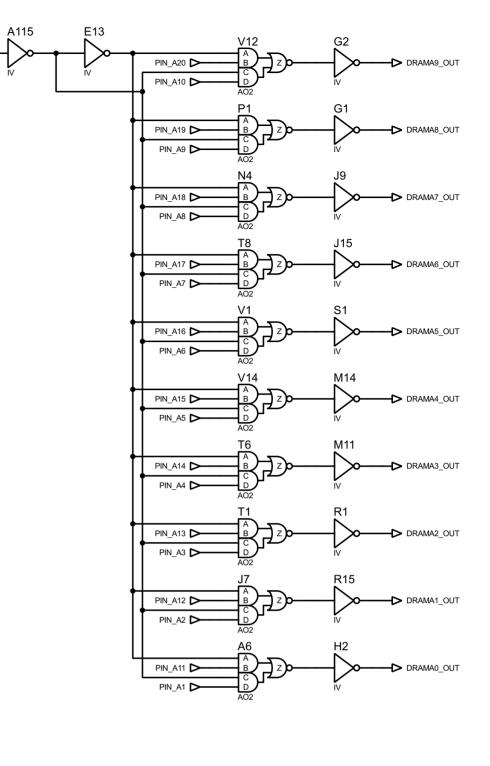
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C	DDA & BUS IO			PAGE:
BY:	Sean Gonsalves	REV:	Α	1/4

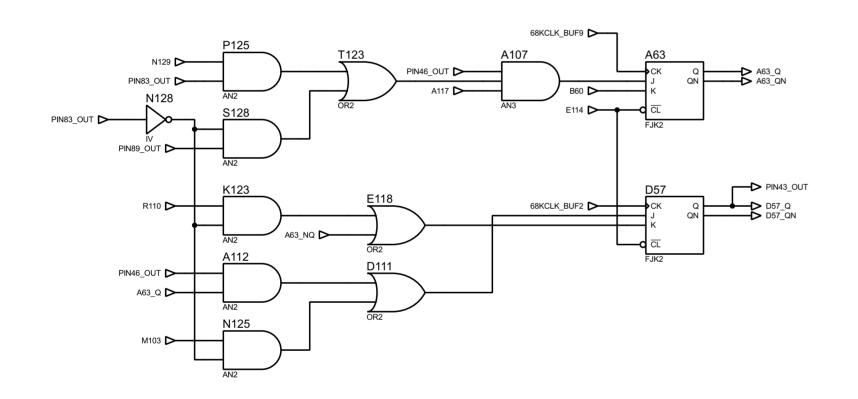


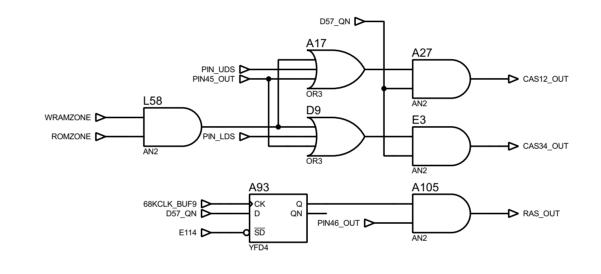
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DECODING & REGISTE	RS	PAGE:
BY: Sean Gonsalves	REV. A	2/4

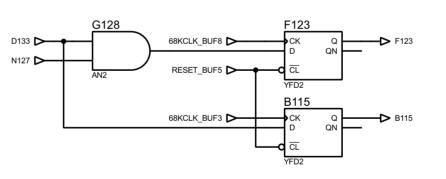


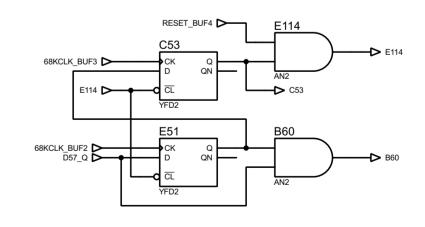








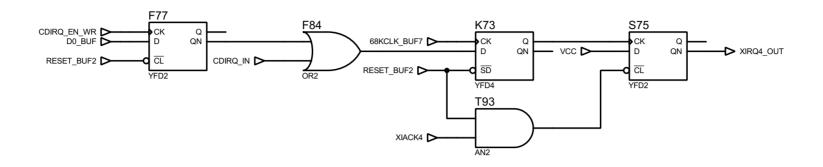


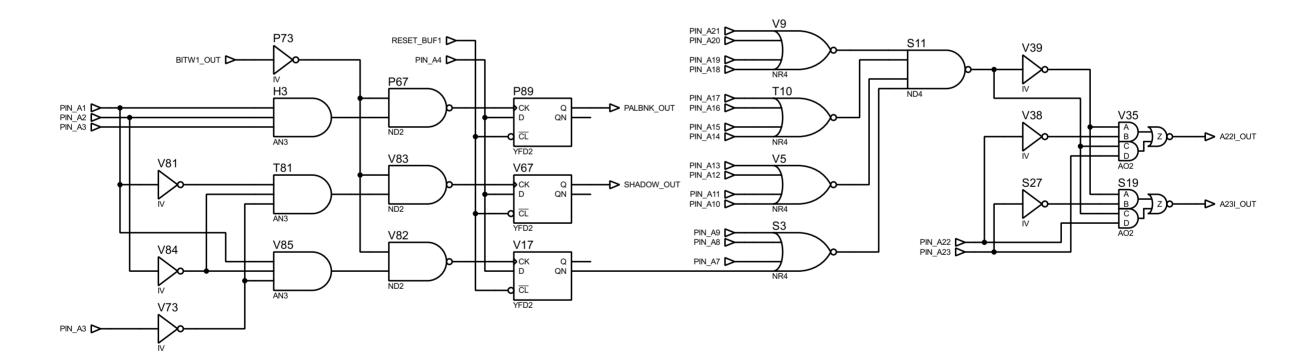


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D57_QN	A21 DRAMWR_OUT
D135 D133 D133 D133	A136 PIN45_OUT
C123 >	C114 PIN46_OUT
WRAMZONE POMZONE	N127

TITLE:		DATE:
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DRAM STUFF		PAGE:
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TITLE:		DATE:
NEO-MGA-T		09/10/2023
SYS REG & CD IRQ		PAGE:
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