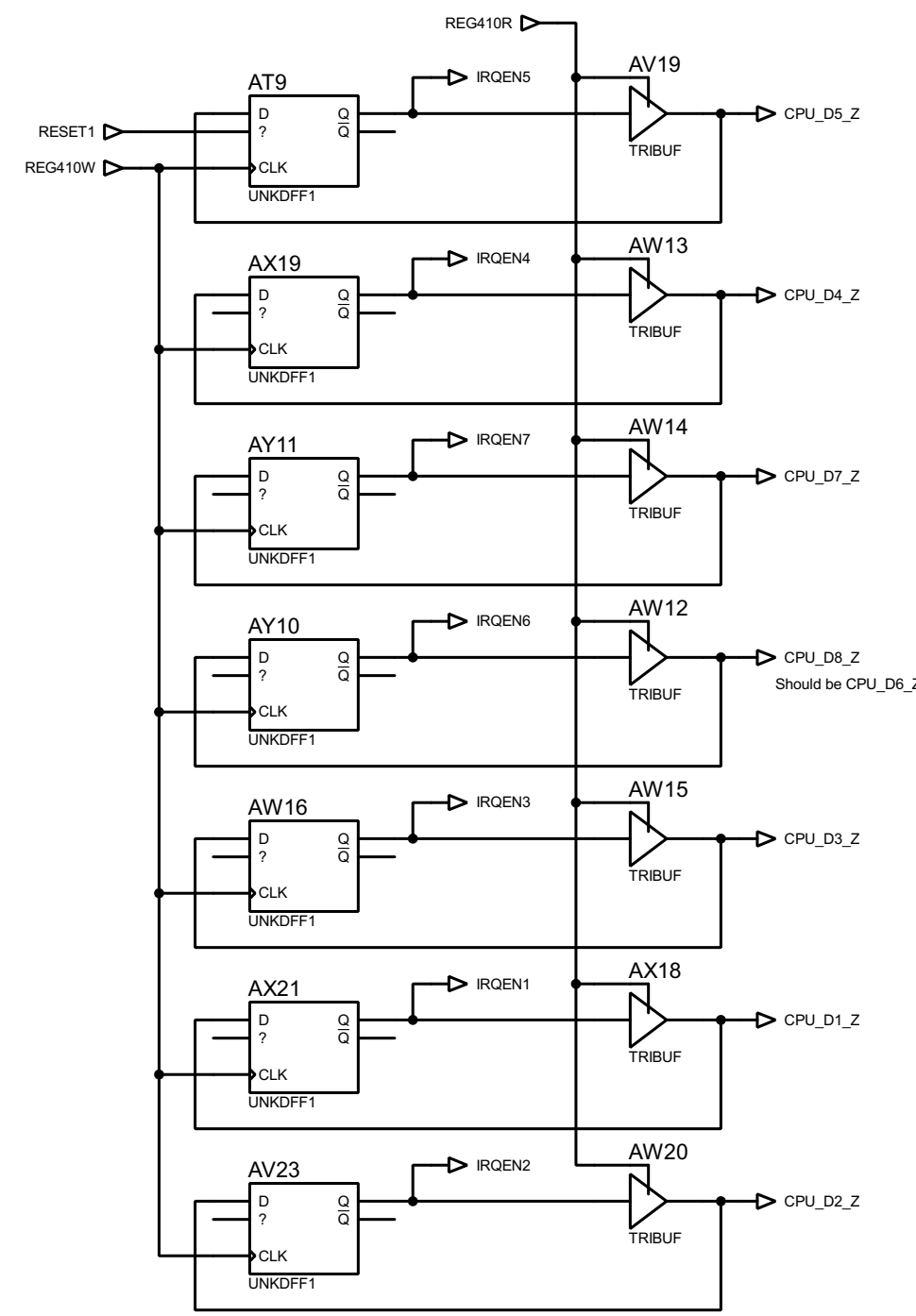
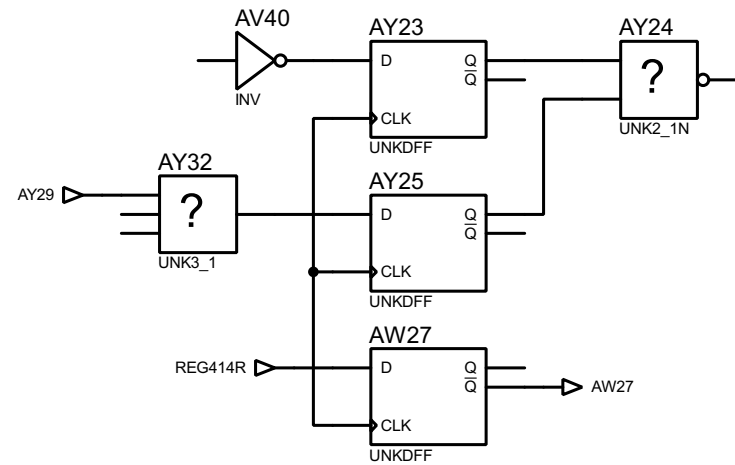
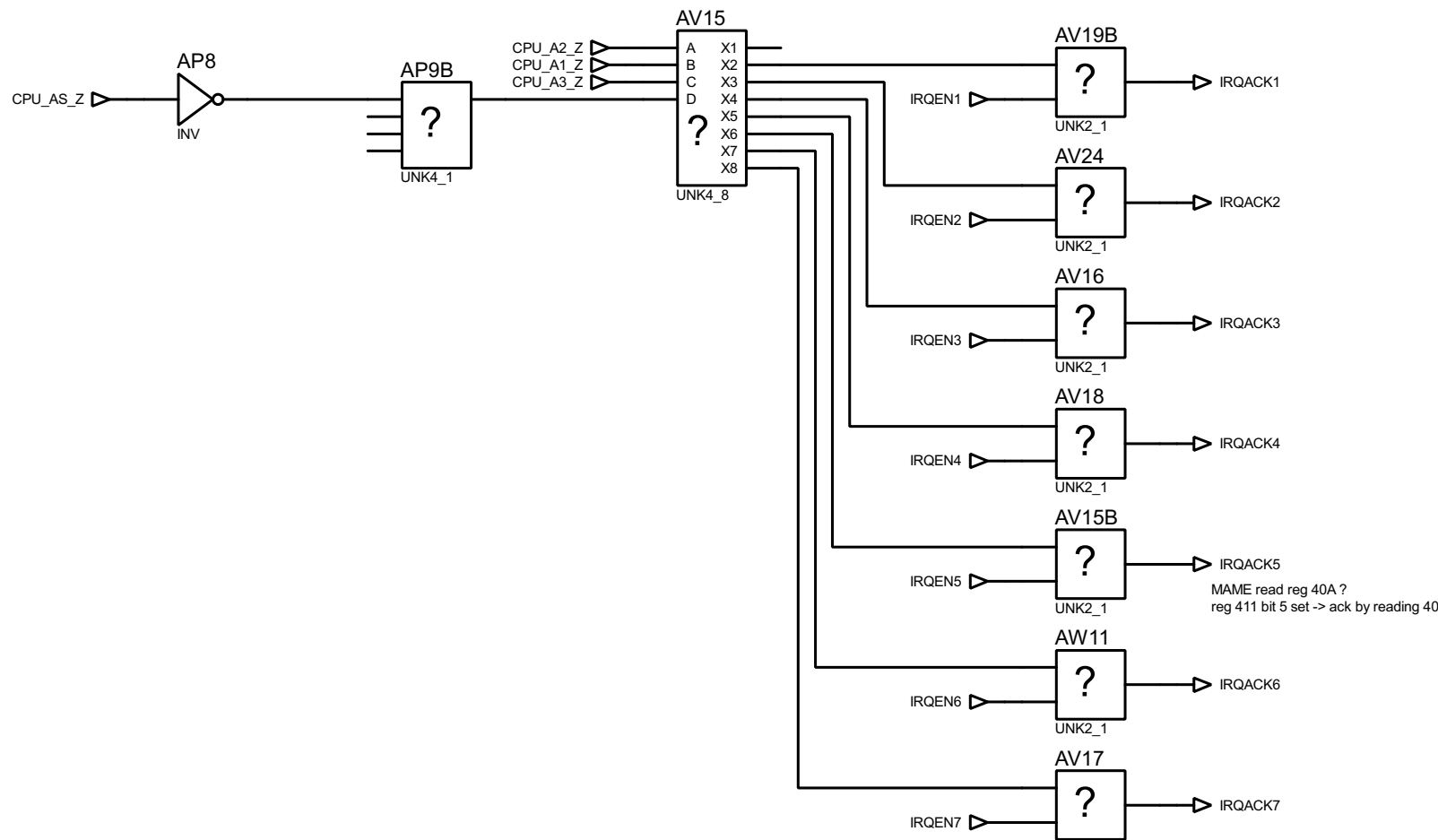


Reg 410: IRQ EN R/W



Ack IRQs

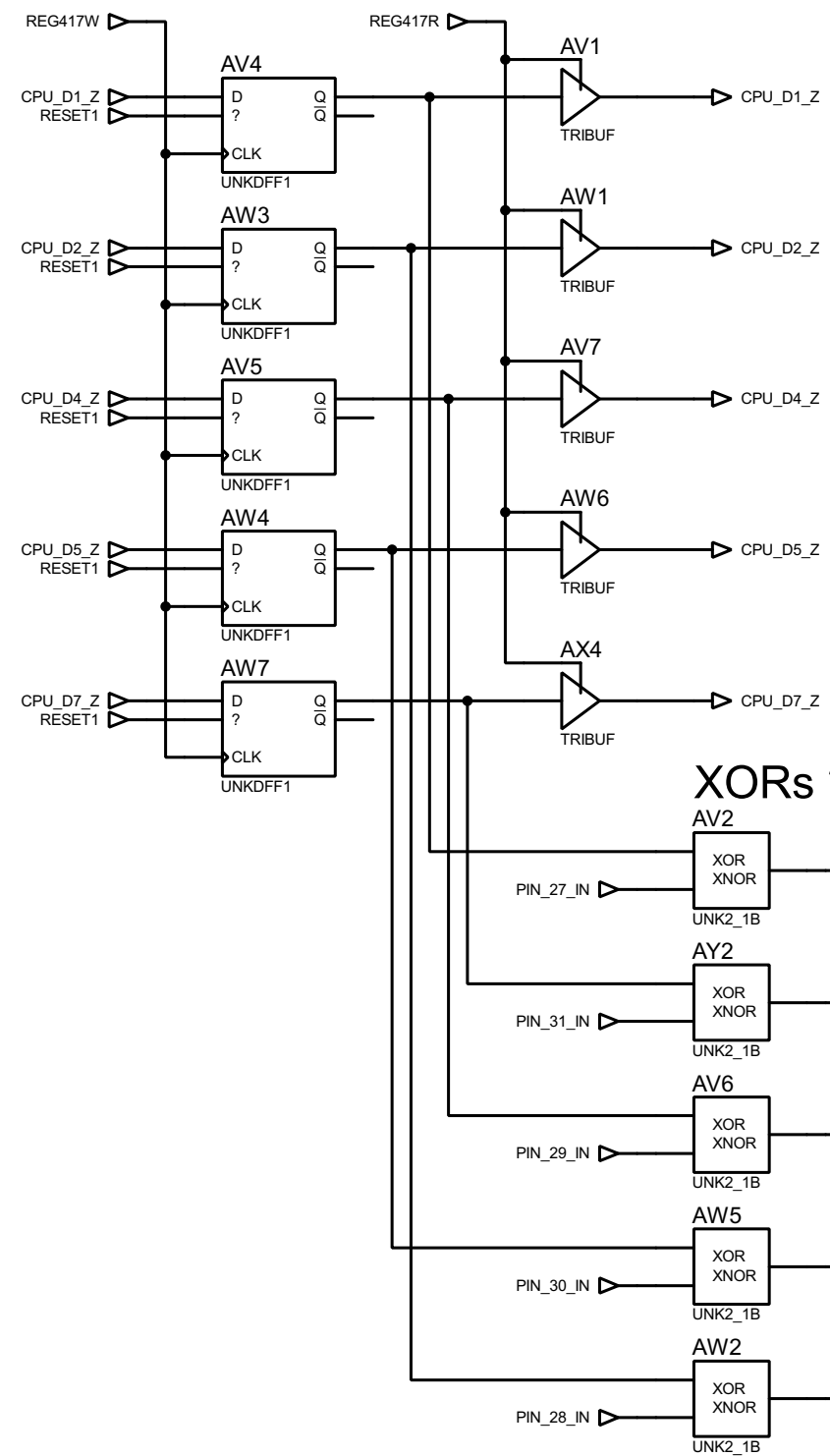


For 68k interrupts, MAME says:
Level 3 011: H-Blank
Level 5 101: Line match
Level 7 111: V-Blank

It seems there are 7 interrupt sources:
-5 from the 6618 PPU, which are set via 6619 pins 27 to 31.
-2 from the 6619 APU itself, which should be DMA and TIMER.

7: From pin 31 (PPU vblank)
6: ??? internal ?
5: From pin 30 (PPU line match)
4: From pin 29 (PPU)
3: ??? internal ?
2: From pin 28 (Cart)
1: From pin 27 (Exp)

Reg 417: R/W
Polarity for IRQ inputs from 6618



Reg 411: IRQ flags

