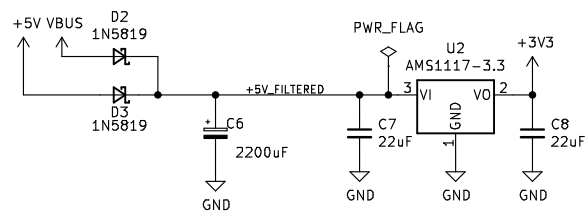


1	2	3	4	5	6		
A							
B	<div>C64 Ports</div> <div>File: c64_ports.kicad_sch</div>	<div>ESP32</div> <div>File: esp32.kicad_sch</div>	<div>USB Serial & Power</div> <div>File: usb_serial.kicad_sch</div>				
C							
D	<div><div></div><div><div><div>Ricardo Quesada</div><div>Sheet: /</div><div>File: unijoysticle2_c64.kicad_sch</div><div><div>Title: Unijoysticle 2 C64</div><div><div>Size: A4</div><div>Date: 2022-07-17</div><div>Rev: A</div></div><div>KiCad E.D.A. kicad (6.0.11)</div><div>Id: 1/4</div></div></div></div></div>						
1	2	3	4	5	6		

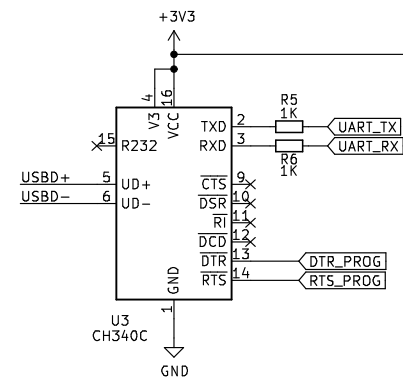
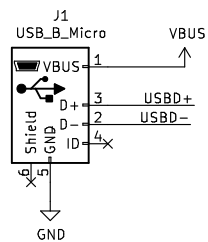
POWER



The 2200uF cap is too reduce the noise between the ESP32 and the C64.

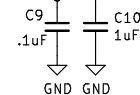
TODO: Add inductor for current filtering (???)

USB <--> UART



Either CH340B or CH340C.
They are pin compatible.
"B" has more features, but more expensive.

Bypass capacitors



Ricardo Quesada

Sheet: /USB Serial & Power/

File: usb_serial.kicad_sch

Title: Unijoysticle 2 C64

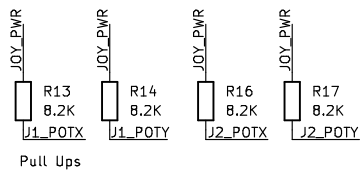
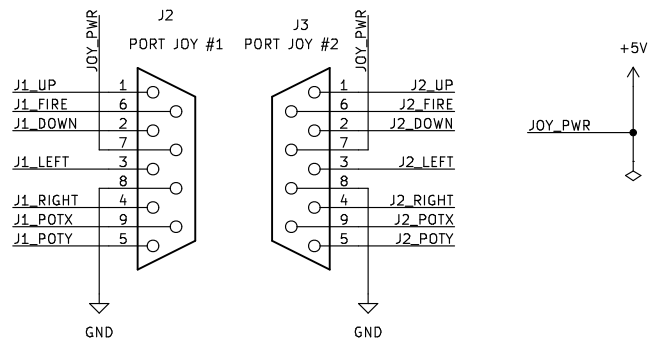
Size: A4 Date: 2022-07-17

KiCad E.D.A. kicad (6.0.11)

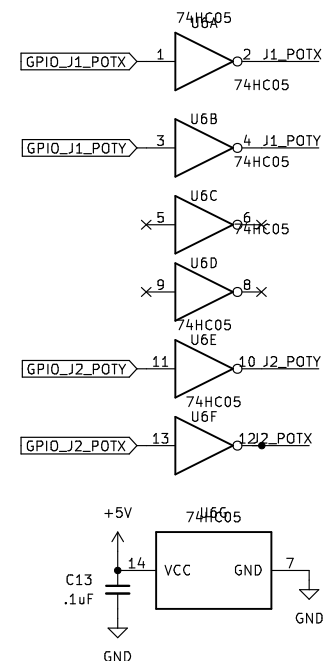
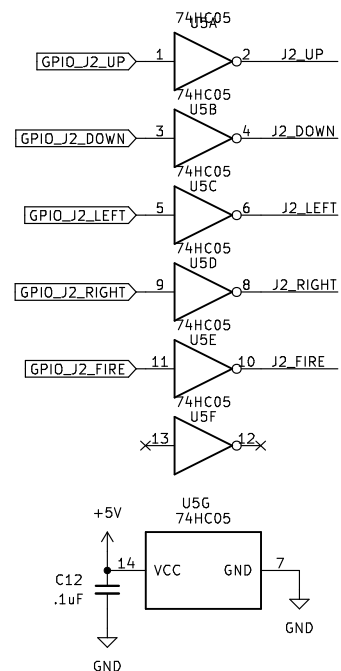
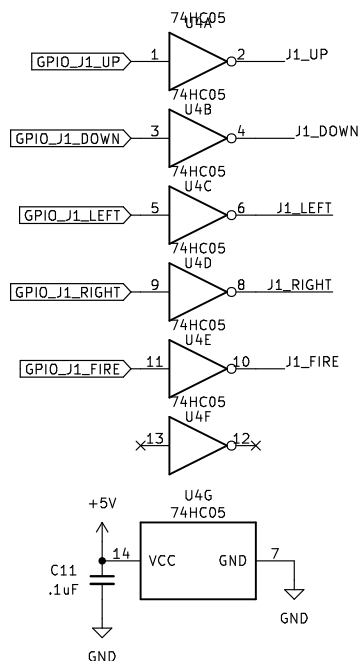
Rev: A

Id: 3/4

DB9 PORTS

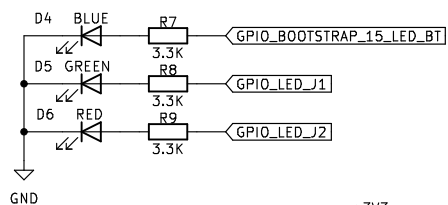


TTL LOGIC

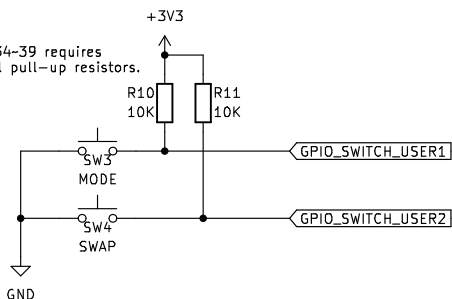


To prevent keyboard and/or other weird issues,
7405 must be connected to C64 power lines (VDD)

LEDS & SWITCHES

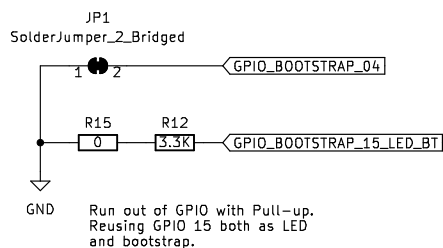


GPIOs 34~39 requires
external pull-up resistors.



MISC

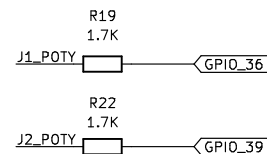
JP1 JP2 (R15)
Uni 2: open, open
Uni 2+: closed, open
A500: open, closed
C64: closed, closed



Run out of GPIO with Pull-up.
Reusing GPIO 15 both as LED
and bootstrap.

SYNC

Jx_POTY are already pulled-up.
Resistor as protection and to reduce voltage



Ricardo Quesada

Sheet: /C64 Ports/

File: c64_ports.kicad_sch

Title: Unijoysticle 2 C64

Size: A4 Date: 2023-02-07

KiCad E.D.A. kicad (6.0.11)

Rev: C

Id: 4/4