C64 Ports

File: c64_ports.kicad_sch

ESP32

File: esp32.kicad_sch

USB Serial & Power

File: usb_serial.kicad_sch

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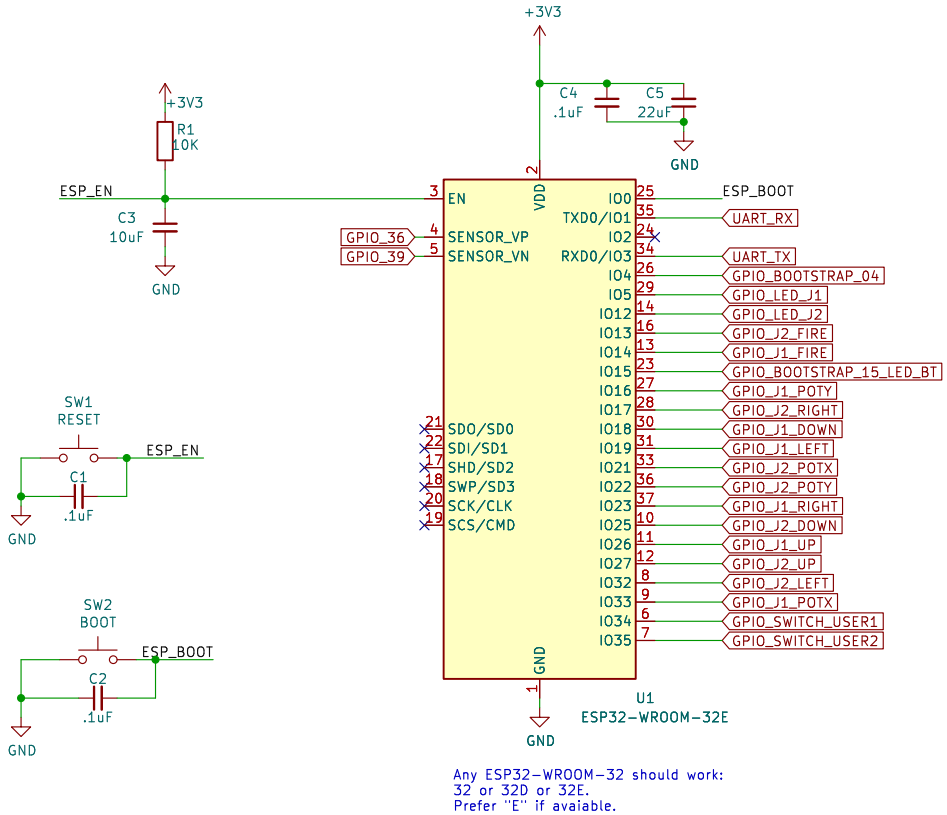
Sheet: /

File: unijoysticle2_c64.kicad_sch

Title: Unijoysticle 2 C64

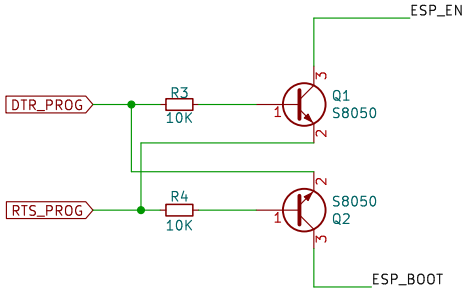
Size: A4	Date: 2022-07-17	**Rev: A**
KiCad E.D.A. kicad 7.0.1		Id: 1/4

ESP32

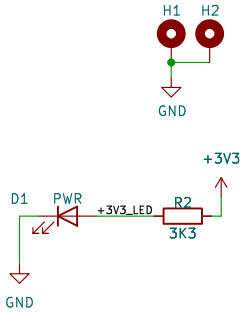


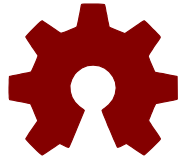
Any ESP32-WROOM-32 should work:
32 or 32D or 32E.
Prefer "E" if avaiable.

AUTO RESET



MISC





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Sheet: /ESP32/	
File: esp32.kicad_sch	
Title: Unijoysticle 2 C64	
Size: A4	Date: 2022-07-17
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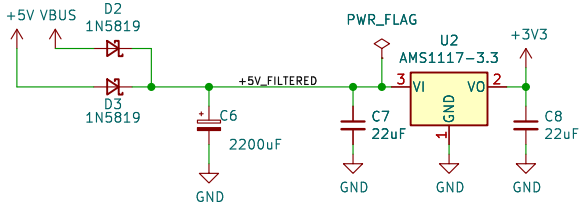
	Rev: A

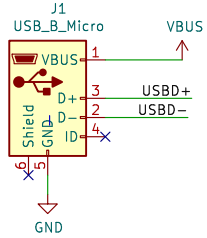
POWER

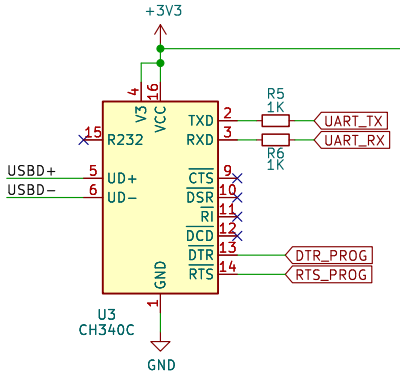


The 2200uF cap is too reduce the noise between the ESP32 and the C64.

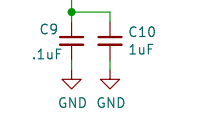
TODO: Add inductor for current filtering (???)

USB <-> UART





Either CH340B or CH340C.
They are pin compatible.
"B" has more features, but more expensive.



Bypass capacitors

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Sheet: /USB Serial & Power/		
File: usb_serial.kicad_sch		
Title: Unijoysticle 2 C64		
Size: A4	Date: 2022-07-17	**Rev: A**
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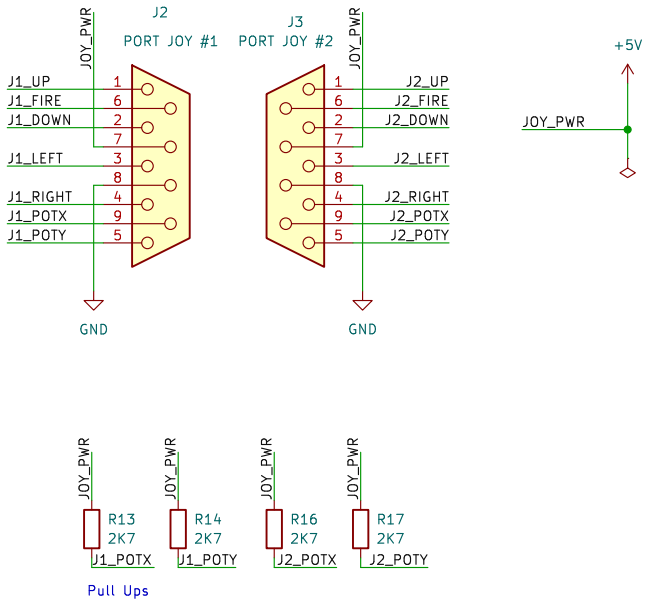
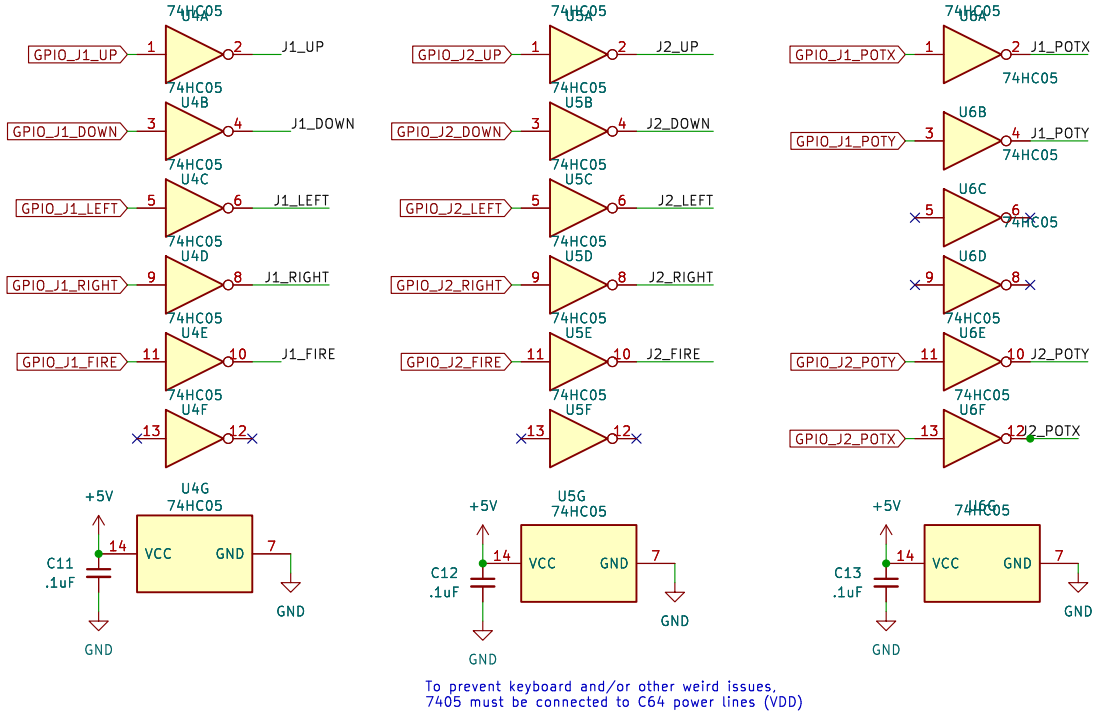
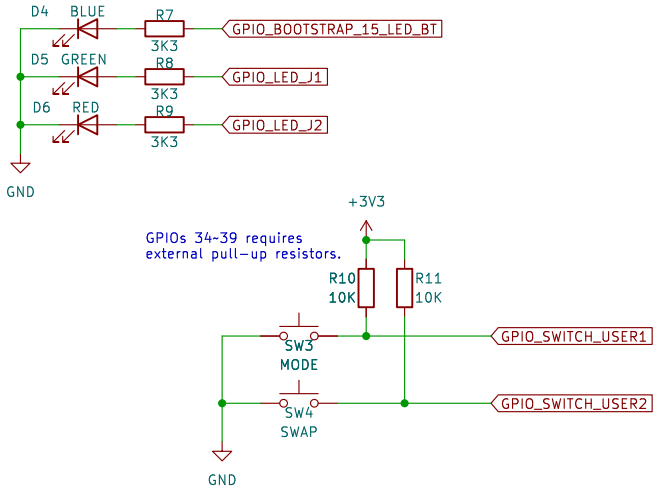
DB9 PORTS



TTL LOGIC

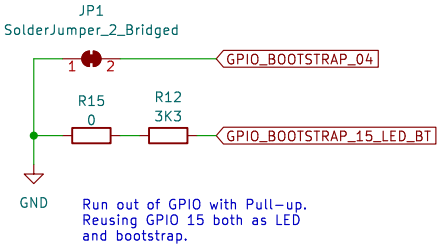


LEDS & SWITCHES



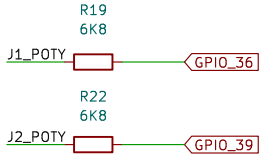
MISC

	JP1	JP2 (R15)
Uni 2:	open,	open
Uni 2+:	closed,	open
A500:	open,	closed
C64:	closed,	closed



SYNC

Jx_POTY are already pulled-up.
Resitor as protection and to reduce voltage



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Sheet: /C64 Ports/
File: c64_ports.kicad_sch

Title: Unijoysticle 2 C64

Size: A4	Date: 2023-02-07	**Rev: C**
KiCad E.D.A. kicad 7.0.1		Id: 4/4