Evaluation board :

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **VHDL** **name** | **IO port name** | | **Package pin** | |
| clk\_i | IO\_L13P\_T2\_MRCC\_35 | H16 | |
| rst\_i | IO\_L4N\_T0\_35 | D20 | |
| hdmi\_in\_clk\_n | IO\_L13N\_T2\_MRCC\_34 | P19 | |
| hdmi\_in\_clk\_p | IO\_L13P\_T2\_MRCC\_34 | N18 | |
| hdmi\_in\_data\_2\_n | IO\_L16N\_T2\_34 | W20 | |
| hdmi\_in\_data\_2\_p | IO\_L16P\_T2\_34 | V20 | |
| hdmi\_in\_data\_3\_n | IO\_L15N\_T2\_DQS\_34 | U20 | |
| hdmi\_in\_data\_3\_p | IO\_L15P\_T2\_DQS\_34 | T20 | |
| hdmi\_in\_data\_4\_n | IO\_L14N\_T2\_SRCC\_34 | P20 | |
| hdmi\_in\_data\_4\_p | IO\_L14P\_T2\_SRCC\_34 | N20 | |
| start\_i | IO\_25\_34 | T19 | |
| hdmi\_in\_ddc\_scl\_io | IO\_L11P\_T1\_SRCC\_34 | U14 | |
| hdmi\_in\_ddc\_sda\_io | IO\_L11N\_T1\_SRCC\_34 | U15 | |
| hdmi\_out\_clk\_n | IO\_L11N\_T1\_SRCC\_35 | L17 | |
| hdmi\_out\_clk\_p | IO\_L11P\_T1\_SRCC\_35 | L16 | |
| spi\_ss\_io | IO\_L12P\_T1\_MRCC\_35 | K18 | |
| spi\_io0\_io | IO\_L12P\_T1\_MRCC\_35 | K17 | |
| spi\_sck\_io | IO\_L10N\_T1\_AD11N\_35 | J19 | |
| cal\_mode | IO\_L10P\_T1\_AD11P\_35 | K19 | |
| hdmi\_in\_data\_1\_n | IO\_L14N\_T2\_AD4N\_SRCC\_35 | H18 | |
| hdmi\_in\_data\_1\_p | IO\_L14P\_T2\_AD4P\_SRCC\_35 | J18 | |

GULF simulation board:

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **VHDL** **name** | **IO port name** | | **Package pin** | |
| en\_i | IO\_L4P\_T0\_35 | D19 | |
| rst\_i | IO\_L4N\_T0\_35 | D20 | |
| sysclk\_in\_n | IO\_L13N\_T2\_MRCC\_34 | P19 | |
| sysclk\_in\_p | IO\_L13P\_T2\_MRCC\_34 | N18 | |
| data\_out\_n | IO\_L14N\_T2\_SRCC\_34 | P20 | |
| data\_out\_p | IO\_L14P\_T2\_SRCC\_34 | N20 | |
| start\_o | IO\_0\_34 | R19 | |