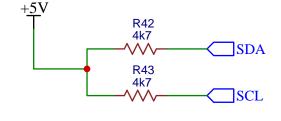


are: A16-A23 are instead assigned to GPB0-7, n41-n48 are instead assigned to GPA0-7, pins 60 & 61 are used for detection, and USER pins are assigned to extra power pins and(much like RC80) I2C bus, and interrupt cascade. This creates the potential danger when using cards designed for RC2014 and the like: If the add-on doesn't expect power on the USER pins (for example), you could fry the

board or worse. Any add-on card that just uses the traditional power pins, address bus, data bus, and control signals should work fine and, of course, any boards specifically designed for the BP-80E bus should also be fine. In a future version I may change this to an entirely different slot type to avoid potential problems.



12C Bus Term On Slot 3

TITLE: CyBorg - Expansion Bus		REV: 1.3
⇔ EasyEDA	Company: CyrusBuilt	Sheet: 6/6
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2024-11-05 Drawn By: Chris Brunner