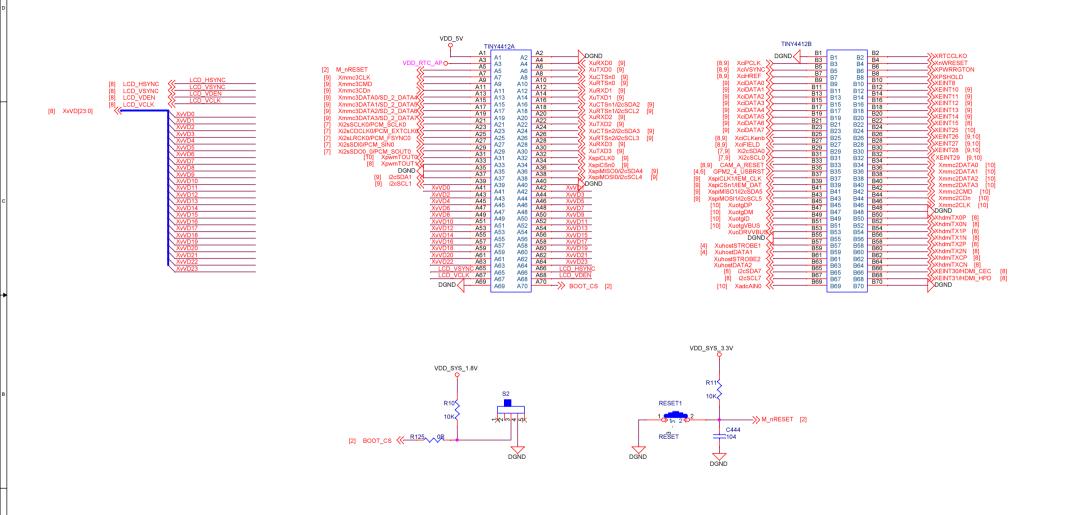
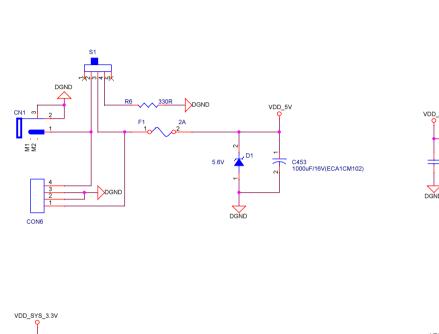
Tiny4412SDK 1312

FriendlyARM http://www.arm9.net



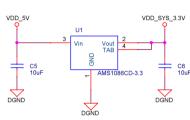


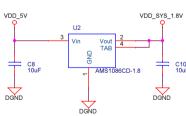
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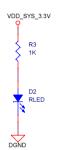
DN3 BAT54C

3.3V BATTERY

DGND



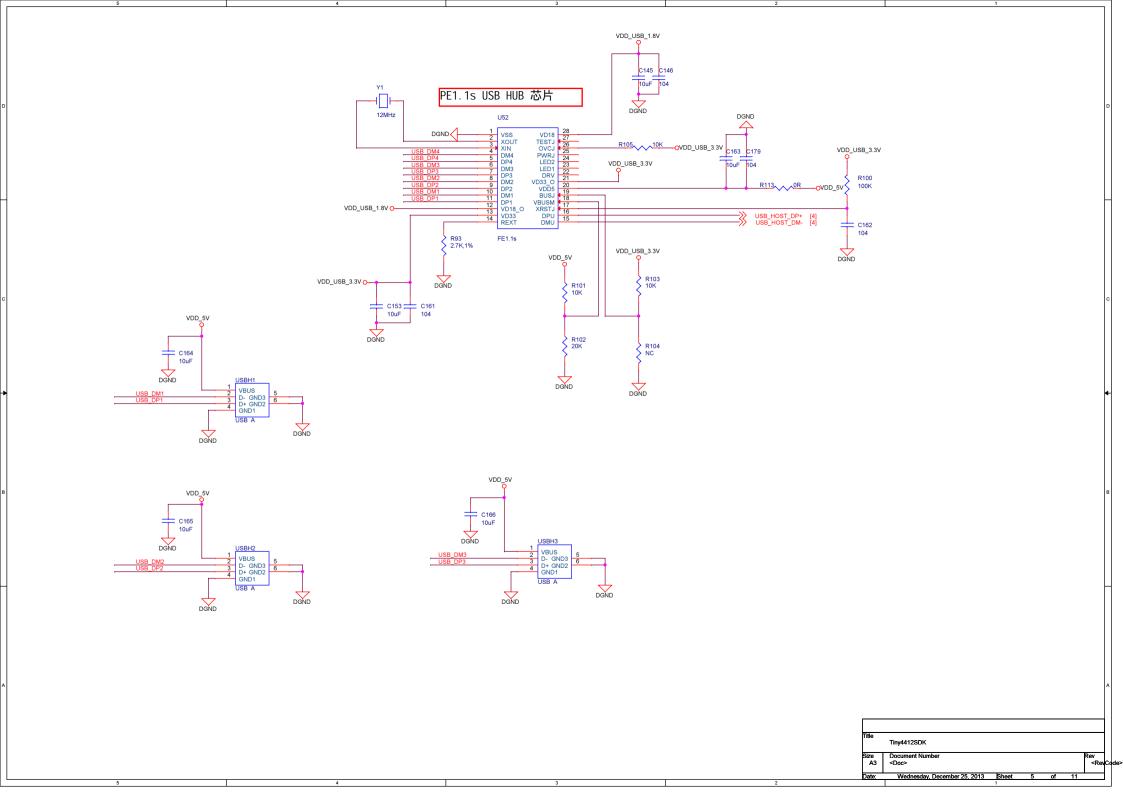




1312						
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HSIC 1 U35A 0 TP71 **HSIC Interface** USB4640 <Device> 10 Differential pairs **⇔** PRTCTL2 [2] XuhostDATA1 HSIC DATA R302 OR USB_HOST_ETHNET_DP1+ [6] USBDN_DP2 [2] XuhostSTROBE1 HSIC_STROBE USBDN DM2 TP72 Q TP70 ◆ PRTCTL3 VDD_HUB_3.3V 0 R465 100K HSIC_IMP USBDN DP3 R464 USBDN_DM3 USB_HOST_DM- [5] 100K/NC EEPROM VDD_HUB_3.3V DGND R466 100K Misc. RESET# 38 RESET R469 OR [2,6] GPM2_4_USBRST >> R468 DGND TEST ↔ C445 47 RBIAS R467 12K,1% DGND 45 XTAL1/CLKIN V_1.2 VDD_HUB_3.3V GPIO10/CARD_PWR 35 R463< U30 VDD_HUB_1V2 0 VIN VOUT 24MHz GPIO1/LED1/TXD

→ X C225 GND C333 15p NC C228 C227 DGND VDD_HUB_1V2 DGND 10K XC6209-1.2V XTAL2 GPIO2/RXD DGND 10u PLLFILT VDD12 C334 DGND C370 104 104 VDD_HUB_3.3V DGND VDD33 5 VDD33 C339 C378 C375 104 104 VDD_SYS_3.3V VDD_HUB_3.3V P R477 OR VDD_HUB_3.3V CRFILT C377 C369 VDD33 VDD33 VDD33 VDD33 104 C374 C373 C372 C371 C376 VSS(FLAG) 104 DGND 10u DGND U35B USB4640 640 <Device>
Media I/F: xD/MS/SD ⊕ GPIO12/MS_INS GPIO14/xD_CD GPIO15/SD_nCD Tiny4412 Size A3 Document Number Wednesday, December 25, 2013 Sheet



USB ETHNET NO USE DGND 104 10u VDD_SYS_3.3V VDD_ETHNET_1.8 VDD_SYS_3.3V VDD_5V VDD_SYS_3.3V R127 49.9R,1% 49.9R,1% C229 C390 C391 C394 C398 R479 10K C231 10u DGND R478 47K R131 R130 330R 330R U66 加上RESET控制 VDD_SYS_3.3V DGND R486 6.98K TEST1 TXD0 TXD1 TEST2 RREF BGRES R487 LEDG+ LEDG-TD+ CT2 TD-LINKLED [2,4] GPM2_4_USBRST >> 34 RSTB VBUS_IN LEDY+ LEDY-0R LANLED TX+ TX-RX+ RX-[4] USB_HOST_ETHNET_DP1+ [4] USB_HOST_ETHNET_DM1-3 5 CT1 RX-CHS_GND R480 75K M1 M2 M2 M2 20 21 EEDIO EECK EECS NC G1 G2 VDD_ETHNET_1.8 49.9R, 1% R132 TXVDDOUT RXVDDOUT C185 C193 C194 104 104 DGND 25 × 26 × 47 SMI_D SMI_CK X1 LNK_LED 29 LI 28 L USB_LED X C147 C190 104 Y2 25MHz G1 DGND R137 C149 20p DGND DGND Tiny4412SDK Size A3 Document Number <Doc> Wednesday, December 25, 2013 Sheet

