

# Christos Zarkos

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## Education

<b>Massachusetts Institute of Technology</b>	Cambridge, MA, 2023-2028 (expected)
PhD in Electrical Engineering and Computer Science, advised by Prof. Christina Delimitrou	<b>GPA(Current): 5.0/5.0</b>
<b>University of Crete</b>	Heraklion, Greece, Sept. 2019 - June 2023
Bachelor's in Computer Science	<b>GPA: 9.62/10.0 (Valedictorian)</b>
<b>Universitat Polytecnic de Catalunya</b>	Barcelona, Spain, Sept. 2022 - Jan. 2023
Erasmus+ exchange program, studying Computer Science	

## Experience

<b>Microsoft Research Redmond</b>	May 2024 - August 2024
<b>Research Intern</b>	
<ul style="list-style-type: none"><li>Worked on AI-driven hardware design</li></ul>	
<b>University of Crete</b>	Feb. 2023 - June 2023
<b>Teaching Assistant for the class CS255 Systems Programming Lab</b>	
<ul style="list-style-type: none"><li>Held recitations, weekly office hours and graded assignments, midterm and final exams</li></ul>	
<b>Barcelona Supercomputing Center</b>	Sept. 2022 - Jan 2023
<b>Visiting Junior Researcher</b>	
<ul style="list-style-type: none"><li>Implemented part of my bachelor's thesis as a member of the CAOS group, advised by Dr. Leonidas Kosmidis</li></ul>	

## Projects

<b>Ka-chow</b>	
<b>[6.5930] Hardware Architecture for Deep Learning Final Project</b>	Jupyter Notebook, Python
<ul style="list-style-type: none"><li>3-member group project</li><li>Implemented a simulation of the performance of a photonics accelerator on CiMLoop</li></ul>	
<b>SPARROW-SV (Bachelor's thesis)</b>	
<b>Implementation of a SIMD Unit for AI Acceleration for a RISC-V Processor</b>	SystemVerilog, C
<ul style="list-style-type: none"><li>Implemented a SIMD unit in SystemVerilog and integrated it into an industry proven RISC-V processor (Veer EH1 RISC-V Core by Western Digital)</li><li>Operated Logical Synthesis, area and timing analysis of the design with EDA tools (DesignCompiler)</li></ul>	
<b>AlphaCompiler</b>	
<b>Compiler's class University project</b>	C
<ul style="list-style-type: none"><li>3-member group project, apart from implementation, in charge of coordinating the tasks assigned to each member</li><li>Implemented a Compiler and a Virtual Machine for a javascript-like language (written in C)</li></ul>	

## Awards and Honors

<b>Onassis Foundation PhD Fellowship</b>	Sept. 2024 - Aug. 2027
For PhD studies at MIT	
<b>Greek Institute of National Scholarships Excellence Scholarship</b>	Dec. 2023
Awarded from the Greek Institute of National Scholarships for being the valedictorian of the class of 2023 for the Computer Science Department of the University of Crete	
<b>Paris Kanellakis Fellowship</b>	Sept. 2023
For the first year of PhD studies at MIT	
<b>Elisavet Karamintzou Scholarship</b>	July 2023
Awarded from the University of Crete for being the valedictorian of the class of 2023 for the Computer Science Department	
<b>Stelios Orphanoudakis Scholarship</b>	Sept. of 2020, 2021, and 2022
Awarded from ICS-FORTH all three possible years during my Bachelor studies in the Computer Science Department of UoC (2019-2020, 2020-2021 and 2021-2022) for being among the top3 students of the class (first all 3 years)	

## Service

DATE 23 Conference, D9 track

Fall 2022

*Paper Sub-reviewer*

## Skills

**Programming Languages, Technologies & Tools** : SystemVerilog, C, C++, VHDL, Python, Java, R, OpenMP, Vivado, EDA, Git, LaTeX, Linux

**Languages** : Greek (Native Language), English (C2), Spanish (B2)

## Areas of interest

Computer Architecture, Computer Systems, Cloud Computing, Hardware/Software Codesign, FPGAs, Embedded Systems