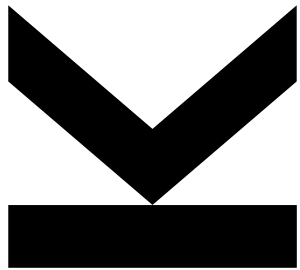


Assembly Introduction



Systems Programming



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Programming environment

■ Some Linux distribution

- ☐ CentOS: <https://www.centos.org/>
- ☐ Ubuntu: <http://www.ubuntu.com/>
- ☐ Debian: <https://www.debian.org/>
- ☐ ...

■ GNU Binutils

- ☐ Collection of binary creation/analysis/manipulation tools
- ☐ <http://www.gnu.org/software/binutils/>
- ☐ **GNU Assembler (as)**
- ☐ **GNU Linker (ld)**

➔ Technically the above is correct and possible.

BUT: We strongly advise you to use the VM that we provided as our ready-made development environment!

Terminology

■ Assembly language

- ☐ Low-level programming language
- ☐ Symbolic representation of machine code (*mnemonics*)

■ Machine code

- ☐ Actual numbers (bits/bytes) a CPU interprets as commands
- ☐ We are not concerned with this – Disassemblers are (very) reliable

■ Assembler / to assemble

- ☐ Software utility translating programs written in assembly language into machine code (object code)

■ Linker / to link

- ☐ Software combining modules (object code) into an executable
- ☐ Static linking: Linking at compile time
- ☐ Dynamic linking: Linking at run time
 - Needs Dynamic Link Library/Shared Object (DLL / .so) at runtime

Example: Shortest program (=“Suicide”)

exit.s

```
#PURPOSE: Simple program that exits and returns a
#          status code back to the Linux kernel
#INPUT:   none
#OUTPUT:  returns a status code. This can be viewed
#          by typing
#          echo $?
#          after running the program
#VARIABLES:
#          %rax holds the system call number
#          %rdi holds the return status
```

```
.section .data
.section .text
.globl _start
```

_start:

```
movq $60, %rax    # this is the linux kernel command
                  # number (system call) for exiting
                  # a program
movq $0, %rdi      # this is the status number we will
                  # return to the operating system.
                  # Change this around and it will
                  # return different things to
                  # echo $?
syscall           # this wakes up the kernel to run
                  # the exit command
```

Assemble, link & run

■ First step: **assembling**

- ☐ `as exit.s -o exit.o --gstabs+`

■ Second step: **linking**

- ☐ `ld exit.o -o exit`

■ Third step: **running**

- ☐ `./exit`

■ Fourth step: view **exit code**

- ☐ `echo $?`

- ☐ Prints the return value of the last executed program

■ “--gstabs+”: Add debug information

■ Note: 32 Bit programs run in a 64 Bit environment need “tweaking”

- ☐ “--32” / “-m elf_i386” must be added for assembler / linker

- ☐ Otherwise it will crash almost immediately!

- ☐ All our programs are 64 Bit, however

Syntax (1)

■ Assembler directives

- ☐ Begin with “.”
- ☐ Directives are **not** translated to code
 - Will end up in the executable file as a specification for the OS what to do with the following bytes/where to load them/...
- ☐ **.section .data**
 - Tells the assembler that the memory storage section begins
- ☐ **.section .text**
 - Tells the assembler that the program instruction section begins
- ☐ **.globl**
 - Defines global symbol needed by the linker
- ☐ **.quad** (64 bits), **.long** (32 bits), **.int** (16 bits!), **.byte** (8 bits) and **.ascii** (8 bits) for specifying the size of data items

Syntax (2)

■ Labels

- ☐ End with “:”
- ☐ Symbolic locations: Provide a name for an address
 - We don’t know in advance which specific address. The linker will later on assign it one and replace all occurrences with the selected one
- ☐ `_start:`
 - Tells the assembler where the program starts
 - This name is not changeable! It must **always** be **exactly** like this!

■ Registers

- ☐ Prefixed with “%”

■ Immediate values

- ☐ Prefixed with “\$”

Syntax (3)

■ Instruction suffix

- ☐ Suffix determines the amount of data
- ☐ “b” → byte (8 bits)
- ☐ “w” → word (16 bits)
- ☐ “l” → long int (32 bits)
- ☐ “q” → quadword (64 bits)
- ☐ For example “**mov****l** \$1, %**eax**” copies the number 1 as a **long** into register EAX
 - Same instruction, different size of operands:
mov**b** \$1, %**al** - **mov****w** \$1, %**ax** - **mov****q** \$1, %**rax**
 - Note: Suffix **must match** size of register used (if any)!

■ Endianness: High-byte at higher or lower address?

- ☐ IA-32, X86-64:

Little endian → High-byte at high-address

1001	1002	1003	1004
0x0A	0x0B	0x0C	0x0D

■ Comments

- ☐ Lines beginning with “#”

1001: 0x0D0C0B0A (l)

1001: 0x0B0A (w)

100**2**: 0x0C0B (w)

Example: Find maximum in list of numbers (1)

- **Given:** list of integer numbers
- **Task:** find largest number in list and return it
- **Storage requirements:** How much/which memory do we need?
 - ☐ List of numbers, terminated by special value (**End Of List**)
 - ☐ Index variable: position of currently examined element
 - ☐ Current value
 - ☐ Maximum value found so far
- **Algorithm**
 1. Initialize index with 0
 2. Load number in list at current index as current value
 3. Save current value as maximum value
 4. While current value is not EOL
 1. Increase index
 2. Load number in list at current index as current value
 3. If current value is larger than maximum, save current value as new maximum
 5. Return maximum value and exit

Example: Find maximum in list of numbers (2)

- Linear execution of program code; loop using branches & goto (comparison & jump instructions)

- **Pseudo code**

1. index = 0
2. curVal = numList[index]
3. maxVal = curVal
4. IF curVal == 0 THEN GOTO 10
5. index = index + 1
6. curVal = numList[index]
7. IF curVal <= maxVal THEN GOTO 4
8. maxVal = curVal
9. GOTO 4
10. RETURN maxVal
11. EXIT

Example: Find maximum in list of numbers (3)

maximum.s

```
#PURPOSE:  This program finds the maximum number of a
#           set of data items.
#
#VARIABLES: The registers have the following uses:
# %rdx - Holds the index of the data item being examined
# %rdi - Largest data item found
# %rax - Current data item
#
# The following memory locations are used:
# data_items - contains the item data.  A 0 is used
#              to terminate the data
```

```
.section .data
```

```
data_items:                                #These are the data items
.quad 3,67,34,222,45,75,54,34,44,33,22,11,66,0
```

```
.section .text
```

```
.globl _start
```

```
_start:
```

```
1.  movq $0, %rdx                          # move 0 into the index register
2.  movq data_items(,%rdx,8), %rax          # load the first byte of data
3.  movq %rax, %rdi                        # since this is the first item, %rax is
                                           # the biggest
```

Example: Find maximum in list of numbers (4)

```
start_loop:                                # start loop
4.      cmpq $0, %rax                       # check to see if we've hit the
end
5.      je loop_exit
6.      incq %rdx                           # load next value
7.      movq data_items(,%rdx,8), %rax
      cmpq %rdi, %rax                       # compare values
      jle start_loop                       # jump to loop beginning if the
8. new
9.
      movq %rax, %rdi                       # one isn't bigger
      jmp start_loop                       # move the value as the largest
                                           # jump to loop beginning
10.
loop_exit:
11.      # %rdi is the status code for the exit system call
      # and it already contains the maximum number
      movq $60, %rax                       # 60 is the exit() syscall
      syscall
```

Data access methods - Details

■ Immediate

- ☐ `movq $12, %rax`
- ☐ Load the number 12 into RAX
 - Note: limited to 32 Bit; sign-extension used for 64 Bit registers!

■ Register

- ☐ `movq %rbx, %rax`
- ☐ Copy the value of RBX into RAX

■ Direct

- ☐ `movq 12, %rax`
- ☐ Load value from memory address 12 into RAX
 - If you want to load the **address itself** and not the **value at it**, then use LEAQ (see later – allows memory calculation – or general arithmetic 😊)

■ Indirect

- ☐ `movq (%rbx), %rax`
- ☐ Load the value from memory address in RBX into RAX

Data access methods - Details

■ Base Pointer

- `movq 4(%rbx), %rax`
- Similar to the indirect example, but add the constant offset 4 to the address stored in RBX, before accessing the memory
- 64 Bit specialty: `32-Bit-displacement(%rip)` also possible

■ Indexed

- `movq data_items(,%rdi,8), %rax`
- Start at address `data_items`, add $8 * \text{value of RDI}$ to that address, and finally load the memory content of this address into the register RAX
- With additional offset
 - `movq data_items(%rbx,%rdi,8), %rax`
 - start at address `data_items + RBX`, add $8 * \text{value of RDI}$ to that address, and finally load the value into RAX
- Attention: Size-suffix (“q”) should generally match multiplier (“8”)
 - It need not; but this is then very likely a programming error!

Data access – Generalized version

■ General form of indexed memory address references

□ **DISPLACEMENT (%BASE, %INDEX, SCALE)**

$$\text{FINAL ADDRESS} = \text{DISPLACEMENT} + \% \text{BASE} + (\% \text{INDEX} * \text{SCALE})$$

■ Most general form

□ Others are specializations of it

- Direct: Displacement only
- Indirect: Base only
- Base pointer: Displacement and Base

■ Elements

□ Displacement: Fixed (**at assembly time!**) number

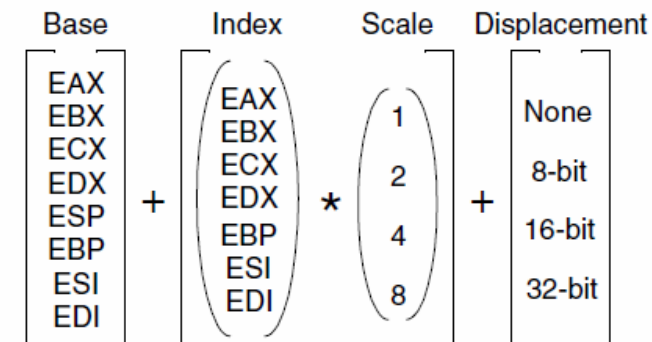
None, 1 Byte, 2 Bytes, or 4 Bytes long (**not** 8 Bytes!)

□ Note: You can specify multiple numbers; assembler will add them up for you so they **MUST** be fixed (=at compile time) numbers!

□ Base: one of RAX, RBX, RCX, RDX, RSP, RBP, RSI, and RDI, R8-R15

□ Index: one of RAX, RBX, RCX, RDX, RBP, RSI, and RDI, R8-R15

□ Scale: (1), 2, 4, or 8



$$\text{Offset} = \text{Base} + (\text{Index} * \text{Scale}) + \text{Displacement}$$

AT&T syntax vs. Intel Syntax

- In this course, we use **AT&T syntax**
- These are the most important differences:

	AT&T	Intel
Parameter order	Source before the destination. <pre>mov \$5, %eax</pre>	Destination before source. <pre>mov eax, 5</pre>
Parameter size	Mnemonics are suffixed with a letter indicating the size of the operands: <i>q</i> for qword, <i>l</i> for long (dword), <i>w</i> for word, and <i>b</i> for byte. ^[1] <pre>addl \$4, %esp</pre>	Derived from the name of the register that is used (e.g. <i>rax</i> , <i>eax</i> , <i>ax</i> , <i>al</i> imply <i>q</i> , <i>l</i> , <i>w</i> , <i>b</i> , respectively). <pre>add esp, 4</pre>
Sigils	Immediate values prefixed with a "\$", registers prefixed with a "%". ^[1]	The assembler automatically detects the type of symbols; i.e., whether they are registers, constants or something else.
Effective addresses	General syntax of <i>DISP(BASE,INDEX,SCALE)</i> . Example: <pre>movl mem_location(%ebx,%ecx,4), %eax</pre>	Arithmetic expressions in square brackets; additionally, size keywords like <i>byte</i> , <i>word</i> , or <i>dword</i> have to be used if the size cannot be determined from the operands. ^[1] Example: <pre>mov eax, [ebx + ecx*4 + mem_location]</pre>

https://en.wikipedia.org/wiki/X86_assembly_language

THANK YOU FOR YOUR ATTENTION!

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