CPLD-Flashing:

1. make sure you have connected usbblaster JTAG header on platform and other end to hostmachine.

2. make sure you install qaurtus application C:\intelFPGA_pro\18.1\qprogrammer\bin64

3. make sure you give proper config path of the qaurtus application. Refer cpld_sample_configuration.xml available in dtaf core data folder.

4.refer test_usbblaster_cpld_provider.py for usage examples.

5.make sure during cpld main & secondary flashing you need to turn on the platform power.