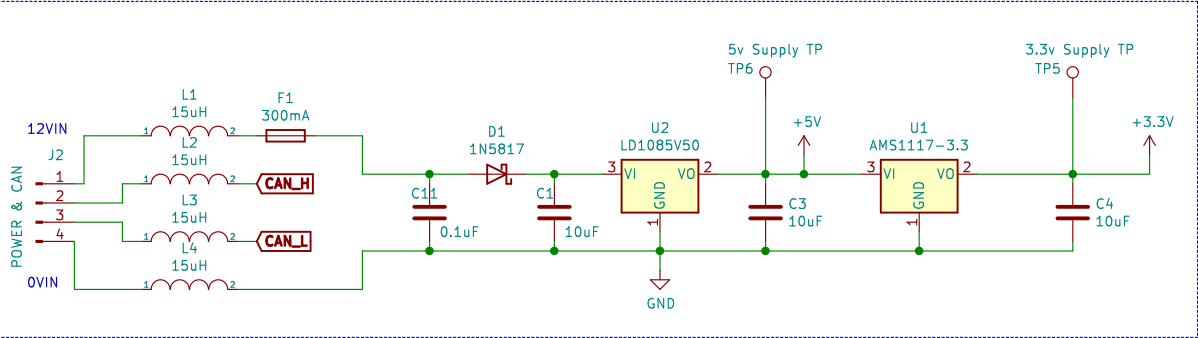
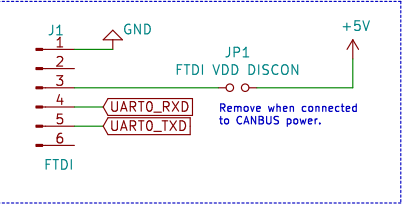


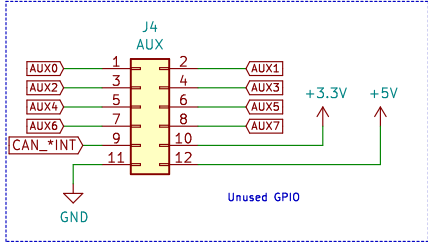
POWER



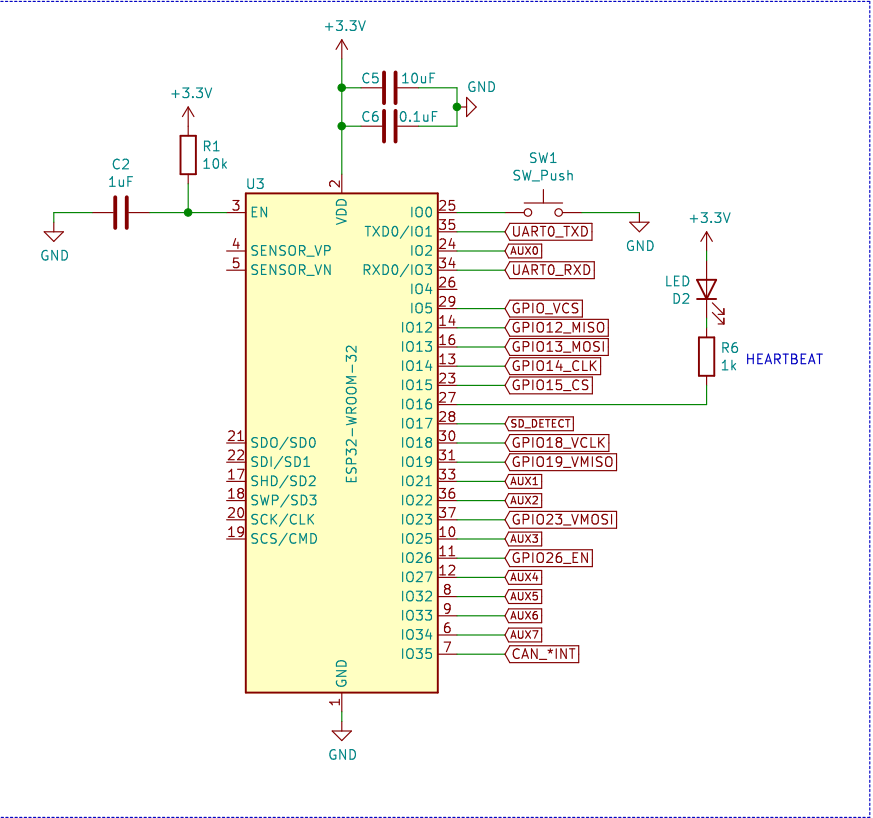
FTDI / PROGRAMMING



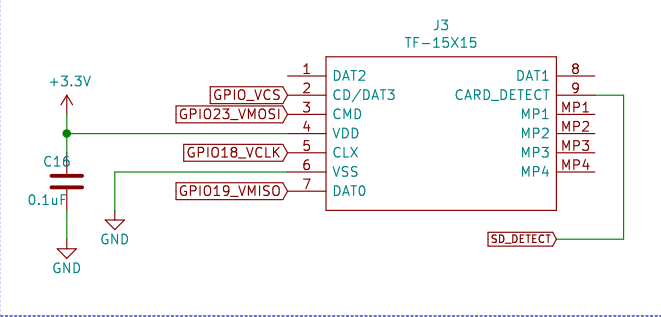
AUX CONNECTOR (Option)



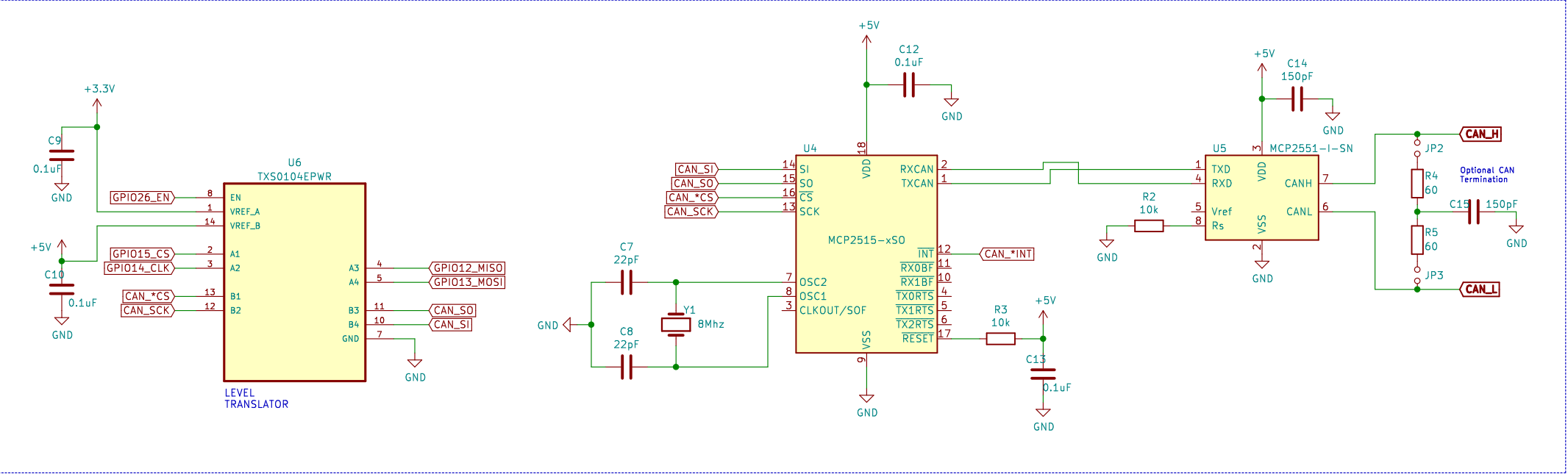
MICROPROCESSOR



OPTIONAL MICRO-SD SLOT



CANBUS INTERFACE



- SLKCAN01
- 20210303/CZM - Moved CAN SPI to HSPI
- 20210303/CZM - Added LSF Level Translator for CAN SPI
- 20210303/CZM - Moved 3.3V reg downstream of 5v reg.
- 20210303/CZM - Added optional MicroSD card slot. VPSI.
- 20210303/CZM - Added option CAN termination.
- 20210303/CZM - Added Decoupling caps to MCP chips.
- 20210303/CZM - Moved FTDI pin 3 from 3.3v to 5V. jumper.
- 20210303/CZM - Added heartbeat LED.
- 20210303/CZM - Added MCP2551 Rs pull down.
- 20210307/CZM - Added extra pins and power to AUX connector
- 20210307/CZM - Missing note: Fixed RX pin from FTDI to ESP32
- 20210313/CZM - Replaced the AMS 5v reg with a more beefy regulator.
- 20210313/CZM - 15uF power inductors on all input lines. SLK bus is very noisy
- SLKCAN02
- 20210615/CZM - Disconnected IO4 from CAN\_\*INT
- 20210615/CZM - AUX8 replaced with CAN\_\*INT
- 20210615/CZM - U6 changed to TXS0104EPWR

PRE-RELEASE

Experimental CANBUS Interface, ESP32

Author: CZ Morris

Sheet: /

File: SLKCAN.sch

Title: SLKCAN

Size: USLedger Date: 2021-06-15

Rev: 02

KiCad E.D.A. eeschema (5.1.9)-1

Id: 1/1