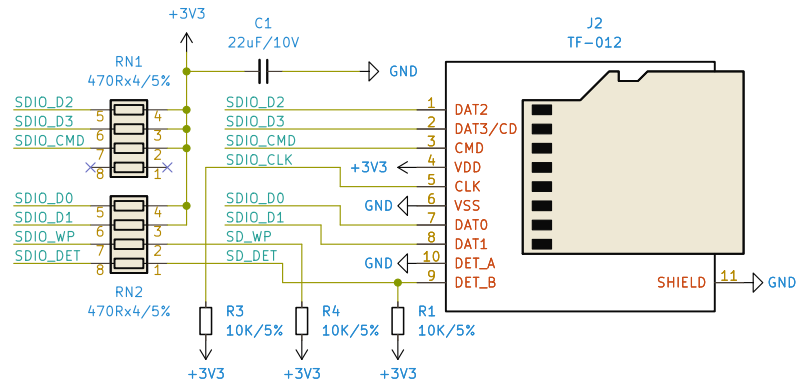
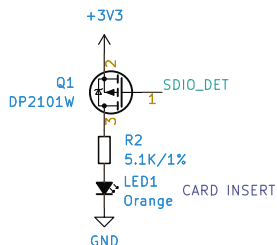


| PMOD PIN MAPPING | | | | | | | |
|------------------|------|----|------------------|----------|-----------------|----|------------------|
| SDIO MODE | | | | SPI MODE | | | |
| 1 | DAT3 | 7 | DAT1 | 1 | \overline{CS} | 7 | NC |
| 2 | CMD | 8 | DAT2 | 2 | MOSI | 8 | NC |
| 3 | DAT0 | 9 | \overline{DET} | 3 | MISO | 9 | \overline{DET} |
| 4 | CLK | 10 | WP | 4 | SSCK | 10 | WP |
| 5 | GNL | 11 | GND | 5 | GND | 11 | GND |
| 6 | 3V3 | 12 | 3V3 | 6 | 3V3 | 12 | 3V3 |



Notes:
Any external power applied to the Pmod MicroSD must be within the specifications of the inserted microSD card. As determined by the SD card association, this voltage range must be within 2.7 to 3.6 volts. The 3.3 V operating voltage nicely complies with this standard.



This design complies with Digilent Pmod (TM) Interface Specification 1.2.0.

Shenzhen Sipeed Technology Co, Ltd.

Sheet: /

File: PMOD_TF.kicad_sch

Title: PMOD_TF-CAARD

Size: A5

Date: 2023-12-20

| | |
|--------------|---------------|
| KiCad E.D.A. | kicad (6.0.8) |
|--------------|---------------|

Rev: 1.1

Id: 1/1