Port		AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF14	AF15
		SPI2/3/ USART1/ 2/3/UART 5/SPDIFR X	SAI/ USART6/ UART4/5/ SPDIFRX	CAN1/2 TIM12/13/ 14/ QUADSPI	SAI2/ QUADSPI/ OTG2_HS/ OTG1_FS	OTG1_FS	FMC/ SDIO/ OTG2_FS	DCMI	<b>.</b>	sys
	PA0	USART2_ CTS	UART4_ TX	1150	50	250		F	Ж	EVENT OUT
Port A	PA1	USART2_ RTS	UART4_ RX	QUADSPI_ BK1_IO3	SAI2_ MCLK_B	19-1	-	-	÷	EVENT
	PA2	USART2_ TX	SAI2_ SCK_B	1870	50	157	150	TA .	须	EVENT
	PA3	USART2_ RX	T.E	6 <del>5</del> 6	OTG_HS_ ULPI_D0	45%	17.0	TH 20	XT.	EVENT OUT
	PA4	USART2_ CK	7	9 <b>7</b> 5	<b>37</b> 1	<b>设</b> 面间	OTG_HS_ SOF	DCMI_ HSYNC	25	EVENT OUT
	PA5	-	Ð	9 <del>7</del> 5	OTG_HS_ ULPI_CK	\$E	1-3	5:	25	EVEN OUT
	PA6	ē	3	TIM13_CH1	<b>5</b> 0	\$750	=	DCMI_ PIXCLK	27	EVEN OUT
	PA7	=	\$	TIM14_CH1	<b>3</b> 71	<b>设</b> 面制	FMC_ SDNWE	<u>.</u>	ā	EVEN'
	PA8	USART1_ CK	Ð	3 T.	OTG_FS_ SOF	15	-	5:	27	EVEN OUT
	PA9	USART1_ TX	3	유판하	81)	\$50	250	DCMI_D0	27	EVEN OUT
	PA10	USART1_ RX	9	유판의	OTG_FS_ ID	\$75°	-	DCMI_D1	25	EVEN
	PA11	USART1_ CTS	<b>7</b> 1	CAN1_RX	OTG_FS_ DM	120	370	=	(in	EVEN OUT
	PA12	USART1_ RTS	SAI2_ FS_B	CAN1_TX	OTG_FS_ DP	820	(12)	=	6	EVEN'
	PA13	=	*)	æ	8 <del>4</del> .8	100	-	-	æ	EVEN' OUT
	PA14	ē.	-	360	8 <del>2</del> E	(17)	-	-	65	EVEN OUT
	PA15	=	UART4_RT S	aec	8 <del>2</del> .2	181	-	-	8	EVEN OUT

Table 4-6: Alternate Functions (AF) Pin Multiplexing for Port A in STM32F446 Arm. See Appendix B for other Ports. (See AF table for your device)

## Example 4-6

In a given ST Miro Arm trainer USART2 port is used for serial ports. Find the values for registers GPIO\_MODER and GPIO\_AFRL if pins PA2 and PA3 are used for TxD and RxD.