

Reference

Resistor

Naming rule 1 (single resistor): value + R + size + tolerance		
Naming rule 2 (resistor network): value + R + size + tolerance + count + circuit type		
Example 1: 62D7R2F 62D7R #NAME? 62.7 Ohm 2 #NAME? 0402 F #NAME? ±1% tolerance	Example 2: 220R2J4X 220R #NAME? 220 Ohm 2 #NAME? 0402 J #NAME? ±5% tolerance 4X #NAME? 4 isolated resistors	
Tolerance	Size	Circuit type
B: ±0.1%	1: 0201	X: isolated
C: ±0.25%	2: 0402	B: bussed
D: ±0.5%	3: 0603	
F: ±1%	5: 0805	
G: ±2%	6: 1206	
J: ±5%	0: 1210	
K: ±10%		
M: ±20%		

Capacitor

Naming rule 1 (ceramic caps): type + value + voltage rating + size + tolerance + dielectric			
Naming rule 2 (electrolytic caps): type + value + voltage rating + size + tolerance + ESR			
Example 1: SCD1U10V2MX SC #NAME? SMT Ceramic D1U #NAME? 0.1uF 10V #NAME? 10V voltage rating 2 #NAME? 0402 M #NAME? ±20% tolerance X #NAME? X7R/X5R	Example 2: ST330U6VD3M9 ST #NAME? SMT Tantalum 330U #NAME? 330uF 6V #NAME? 6.3V voltage rating D3 #NAME? D3 package M #NAME? ±20% tolerance 9 #NAME? 9 mOhm ESR		
Tolerance	Size	Type	Dielectric
B: ±0.1pF	1: 0201	SC: SMT Ceramic	X: X5R/X7R
C: ±0.25pF	2: 0402	ST: SMT Tantalum	N: NP0/C0G
D: ±0.5pF	3: 0603	SA: SMT Aluminium Electrolytic	
F: ±1%	5: 0805	SP: SMT Aluminium Polymer	
G: ±2%	6: 1206		
J: ±5%	0: 1210		
K: ±10%			
M: ±20%			

DY : No stuff

1

2

3

4

A

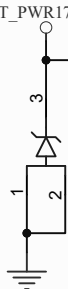
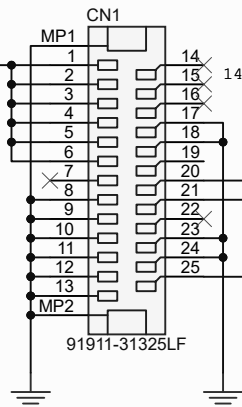
B

C

D

Power output to MB

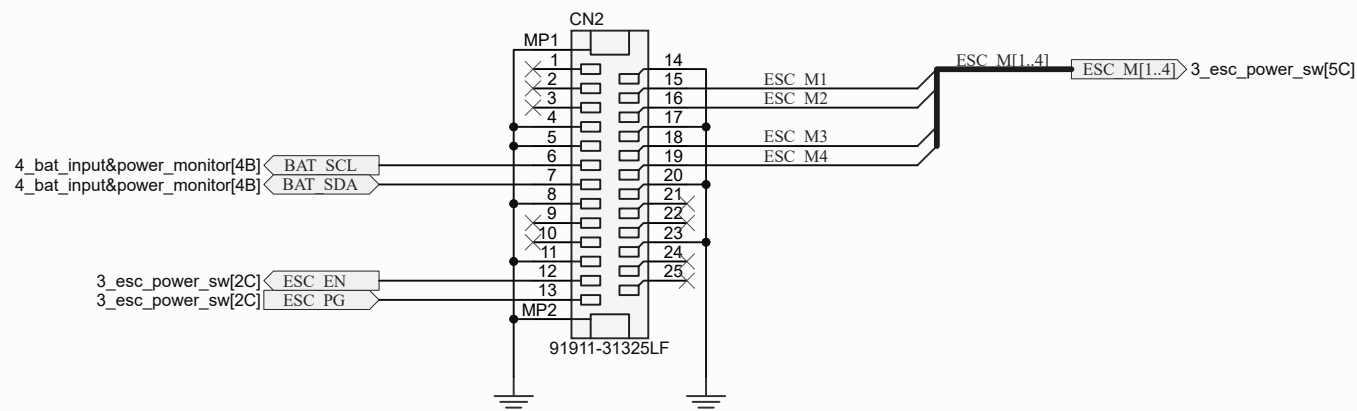
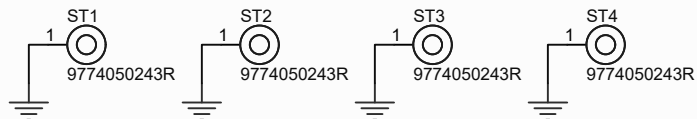
BAT_PWR17_PDB

D1
ESDA24P140-1U3M**PDB_A conn**

VCC3M

VCC3SW

14, 15: VCC5M

PDB_B conn**Standoffs for MB**Title **MB connectors**

Size: A4

Number:*

Revision:

Date: 2/22/2025

Time: 6:36:59 PM

Sheet 4 of 4

File: C:\Users\Public\Documents\Altium\Sample - Kame PDB\2 mb conn.SchDoc



