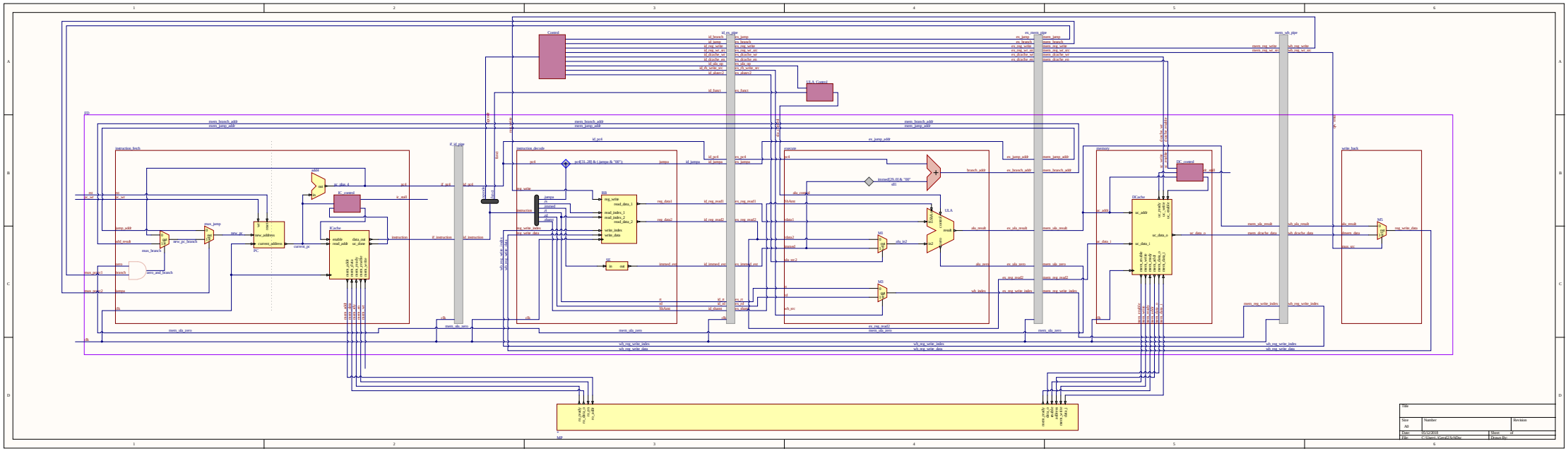
Control
U.A. Control
ID
instruction_fetch
instruction_decode
execute
memory
write_back
IC_control
DC_control
ICache
DCache
RF
U.A
M0
M1
M2
M3
M4
M5
PC
mux_jump
mux_branch
new_pc_branch
new_address
current_address
new_pc
current_pc
mem_and_branch
mem_alu_zero
mem_branch_addr
mem_jump_addr
if_id_pipe
id_ex_pipe
ex_mem_pipe
mem_wb_pipe
reg_write
reg_write_index
reg_write_data
wb_reg_write_index
wb_reg_write_data
Title
Size
A0
Number
Revision
Date:
05/12/2018
Sheet
of
File:
C:\Users\Gerard\Schdoc
Drawn By: