

Planned Project Activities

Please list the activities that you intend taking place during your laboratory time, and indicate when they should occur, and who will do them. The 'Initials' column must specify only one person. If two people are working on the same subsystem or task, you should list this as two separate activities, and be clear about what everyone is contributing to it.

Activity	Initials	Fri am	Fri pm	Mon am	Mon pm	Tue	Wed	Thu	Fri am	Fri pm	Mon am	Mon pm
Confirming sample rate of the ADC	KP	X	X									
Construct development board	KP			X	X							
Testing design	KP			X	X	X						
Construct second final board	KP						X	X	X	X		
Testing Final board	KP								X	X		
Finalise layout of hardware within the case	WCW	X	X									
Finalise the schematic	WCW	X	X									
Construct development board	WCW			X	X							
Testing design	WCW			X	X	X						
Construct second final board	WCW						X	X	X	X		
Testing Final board	WCW								X	X		
LED ring and calibration button code	MPW	X	X									
Case design	MPW			X	X	X						
Case printing and construction	MPW						X					

LED ring and calibration button construction	MPW							X				
Review of embedded code	MPW								X			
LED ring and calibration button debugging	MPW									X		
Code for output interface	FEW	X	X									
Generating test data with a script	FEW	X	X									
Tweaking and debugging signal processing	FEW			X	X	X	X		X	X		
Hardware construction	FEW							X	X			
Implement the FFT	YFL	X	X									
Implement the cross-correlation function	YFL	X	X									
Tweaking and debugging signal processing	YFL			X	X	X	X		X	X		
Hardware construction	YFL							X	X			
Web back-end and file handling	MC	X										
Web UI for FFT using generated data	MC			X								
Sensor data input and display	MC				X							
Hardware construction	MC					X	X	X				
Apply Data to web UI elements	MC					X						
Testing and debugging of web UI	MC						X					
Addition web server functionality (such as security and running install scripts)	MC								X	X		
Beautify web UI design	MC										X	
Add serial implementation code to buffer	MJ	X		X								

[illegible]