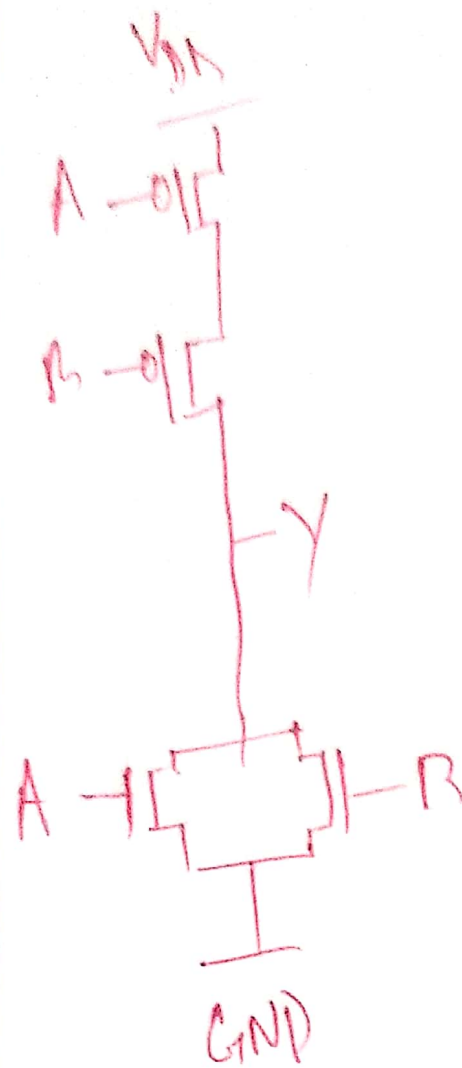**Question:** Consider any 2-input NOR Gate: $Y = \overline{A+B}$

Task 01. Draw schematic Diagram
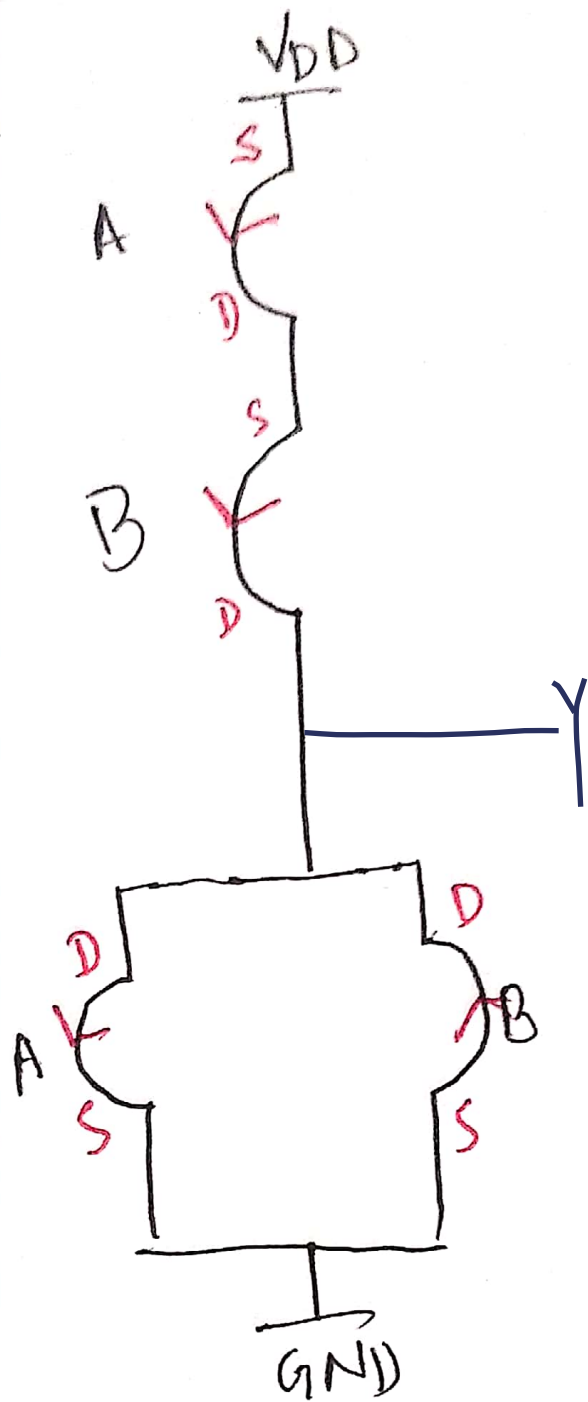
Task 02. Draw Euler's Path

Task 03. Draw Stick Diagram
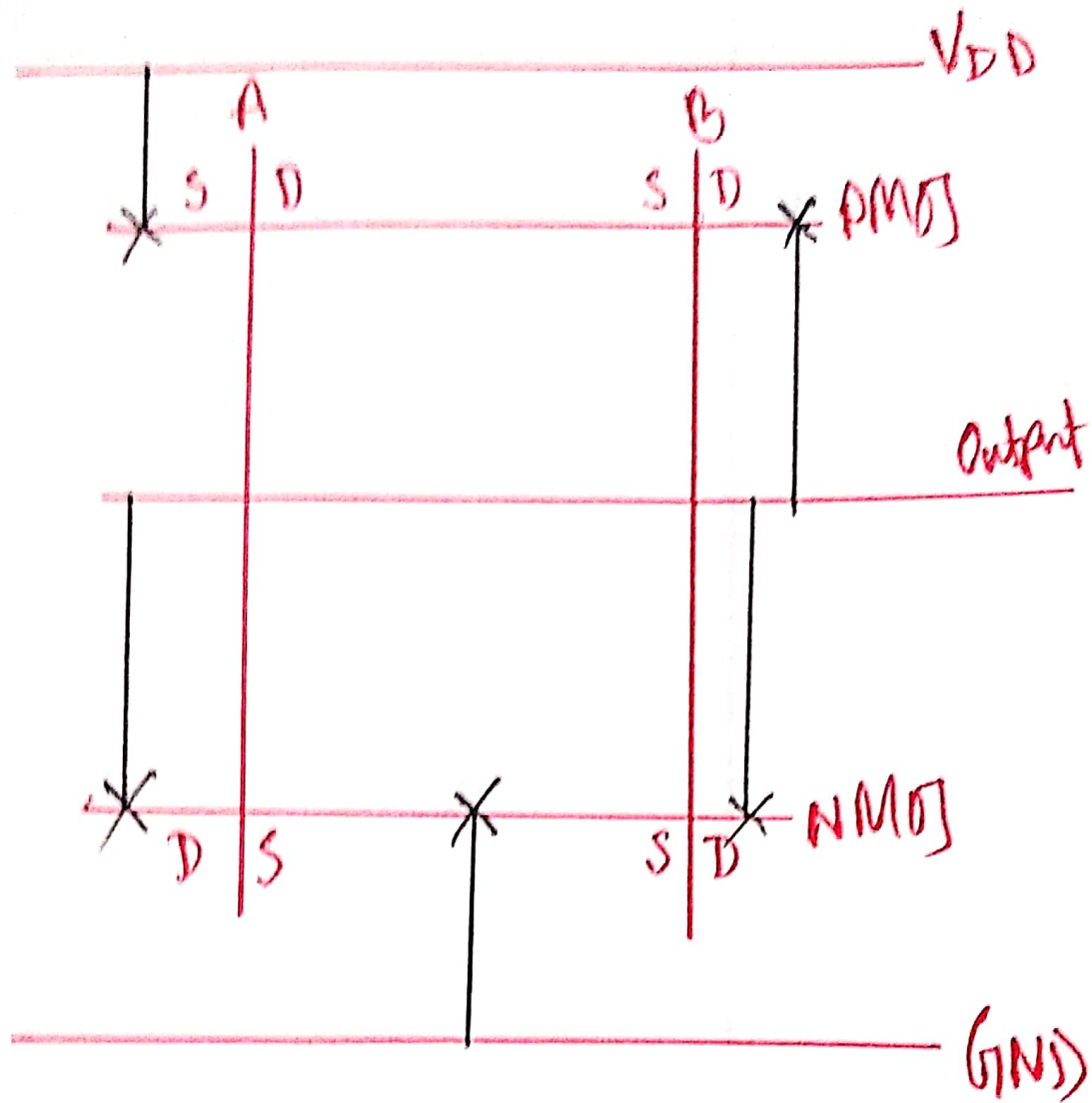
Task 04 Implement in microwind

Task 01

Remember
Series

Parallel

VDD

S

A

D

S

B

D

Y

D   D

A   B

S   S

GND

Task 02.

Path

Source- Drain

VDD

A
S | D
B
S | D
PMOS

Output

D | S
S | D
NMOS

GND

Task 03.

Remember
- connection wires
- Contactes
- Source Drain placement