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(1)

Answer to the Q. NO. 1

Given,

$$Y = \overline{(AB+C)\cdot D} \quad \cdots \quad (i)$$

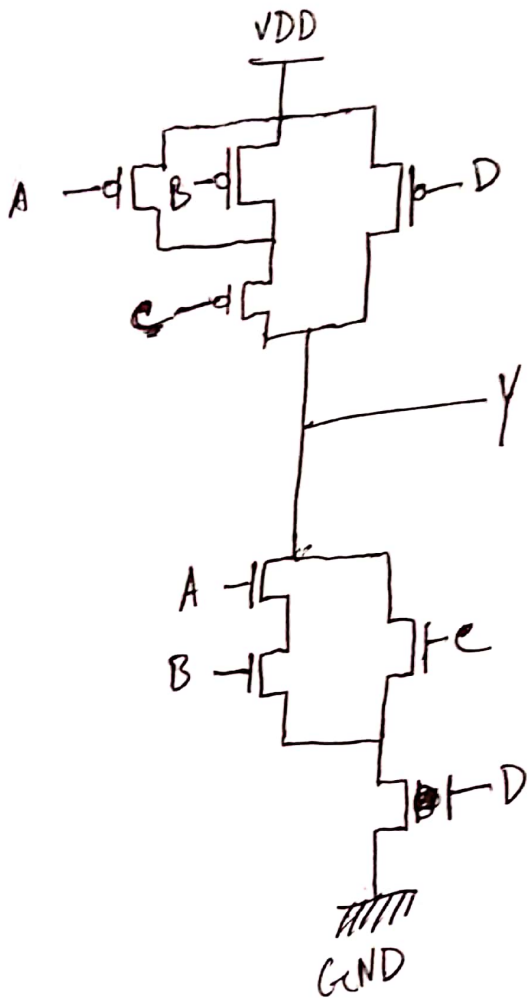

Fig: CMOS design of eqn (i)

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(2)

Answer to the Q. No. 2

For the given block diagram and truth table in Q. we can get, $Y = \bar{S_1}\,\bar{S_0} I_0 + \bar{S_1} S_0 I_1 + S_1 \bar{S_0} I_3 + S_1 S_0 I_4$

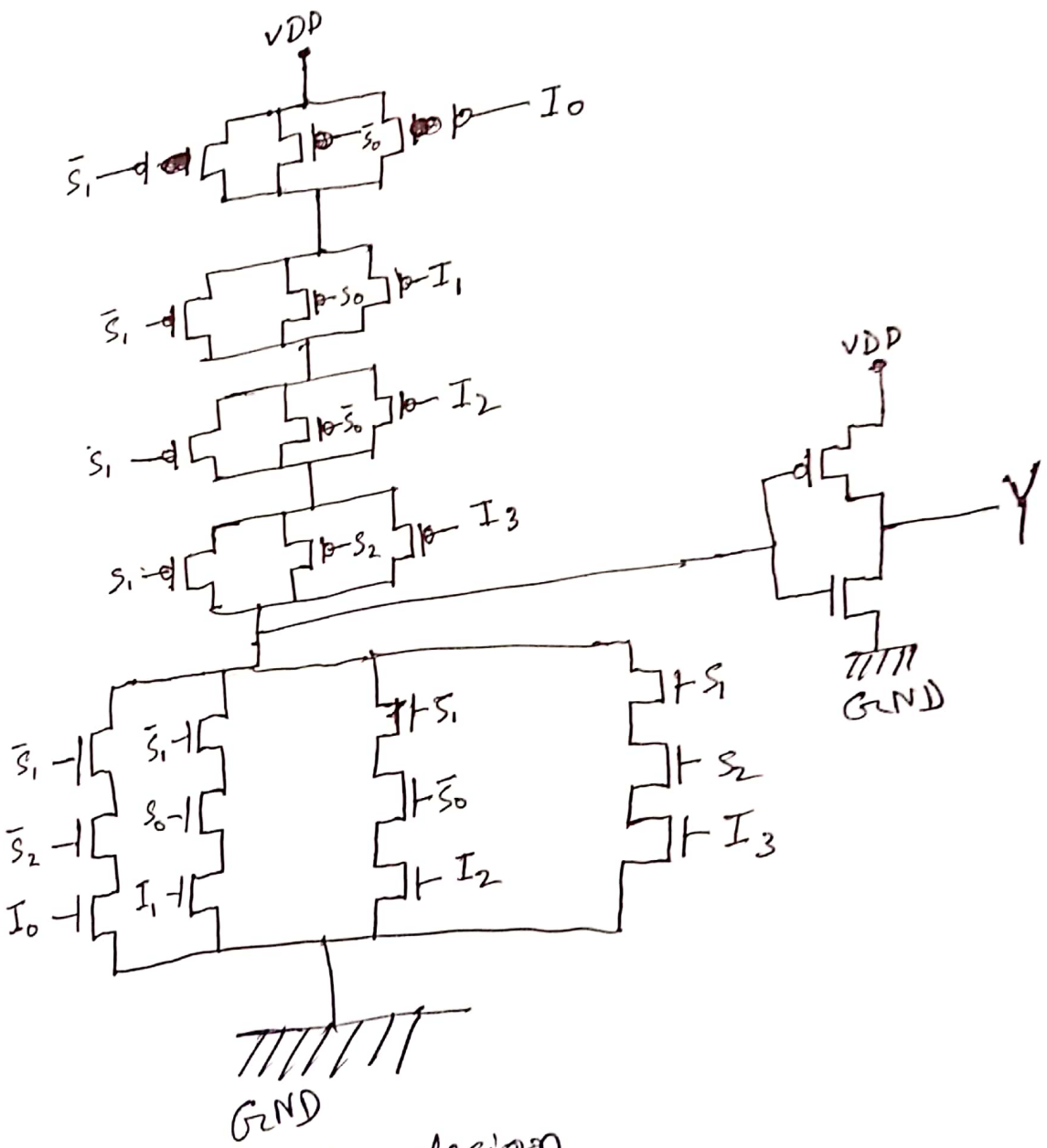

Fig: 4.1 MUX design

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