#### Resistor

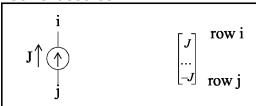
$$i \quad - \bigvee \bigvee - j \quad i \quad \begin{bmatrix} g \dots - g \\ \dots \dots \dots \\ j & -g \dots g \end{bmatrix}$$

## Capacitor

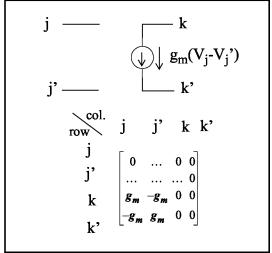
$$i \longrightarrow \begin{bmatrix} col. & i & j \\ i & \end{bmatrix}$$

$$i \longrightarrow \begin{bmatrix} sC \dots -sC \\ \dots \dots \dots \\ j & -sC \dots sC \end{bmatrix}$$

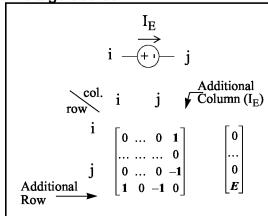
#### **Current source**



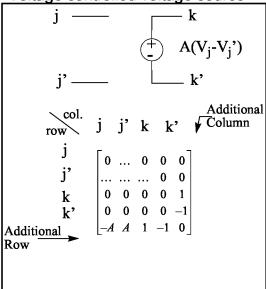
## Voltage controlled current source



## Voltage source



# Voltage controlled voltage source



### Inductor

