Figure 1: State machine at position 1
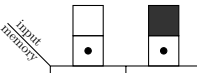
(a) Position 2   (b) Position 3   (c) Position 4

Figure 2: State machines (a) at position 2, (b) at position 3, (c) at position 4

00	11,∅	01,∅	01,∅	11,∅	01,∅	01,∅	11,∅	01,∅
01	00,W	00,N	00,N	10,∅	00,N	00,N	00,∅	00,∅
10	11,∅	11,∅	11,∅	11,∅	11,∅	11,∅	11,∅	01,∅
11	00,W	00,W	00,W	00,W	00,W	00,W	10,∅	00,∅

00	01,∅	01,∅	01,∅	01,∅	11,∅	11,∅	11,∅	11,∅
01	00,N	00,N	00,N	00,N	00,W	00,W	10,∅	10,∅
10	11,∅	11,∅	01,∅	01,∅	11,∅	11,∅	11,∅	11,∅
11	10,∅	10,∅	10,∅	00,∅	00,W	00,W	10,∅	10,∅
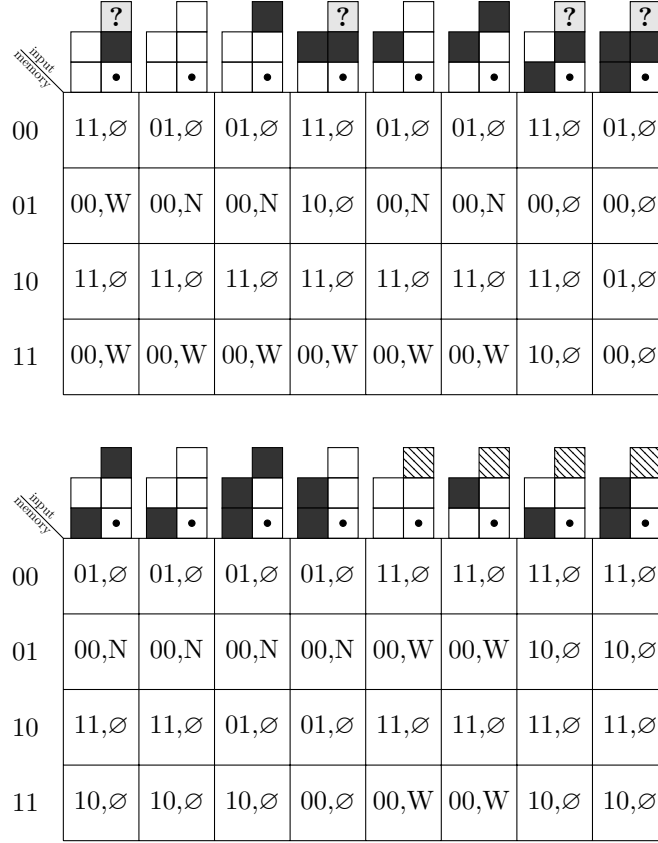
Figure 3: State machine at position 5

00	01,N	01,∅
01	00,N	00,∅
10	01,N	01,∅
11	00,N	00,∅
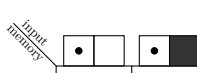
(a) Position 6

00	01,E	01,∅
01	00,E	00,∅
10	01,E	01,∅
11	00,E	00,∅
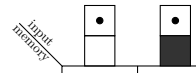
(b) Position 7

00	01,S	01,∅
01	00,S	00,∅
10	01,S	01,∅
11	00,S	00,∅
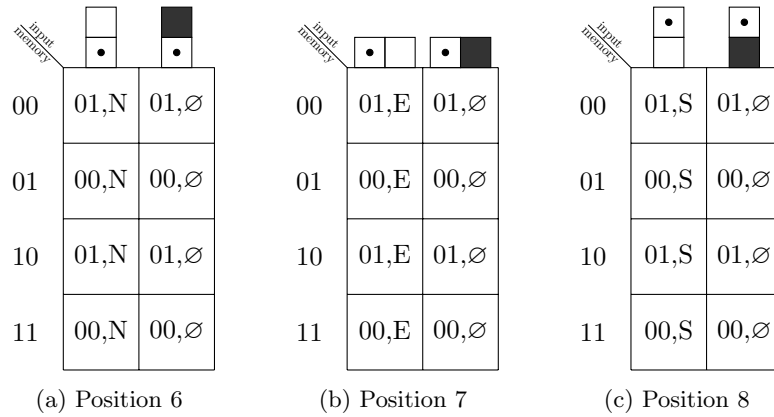
(c) Position 8

Figure 4: State machines (a) at position 6, (b) at position 7, (c) at position 8

memory \							
00	01,∅	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅
01	00,∅	00,N	10,∅	00,N	10,∅	00,N	10,∅	00,N
10	11,∅	01,∅	11,∅	01,∅	11,S	11,S	11,S	11,S
11	10,∅	00,∅	10,∅	00,∅	10,∅	10,∅	10,∅	10,∅

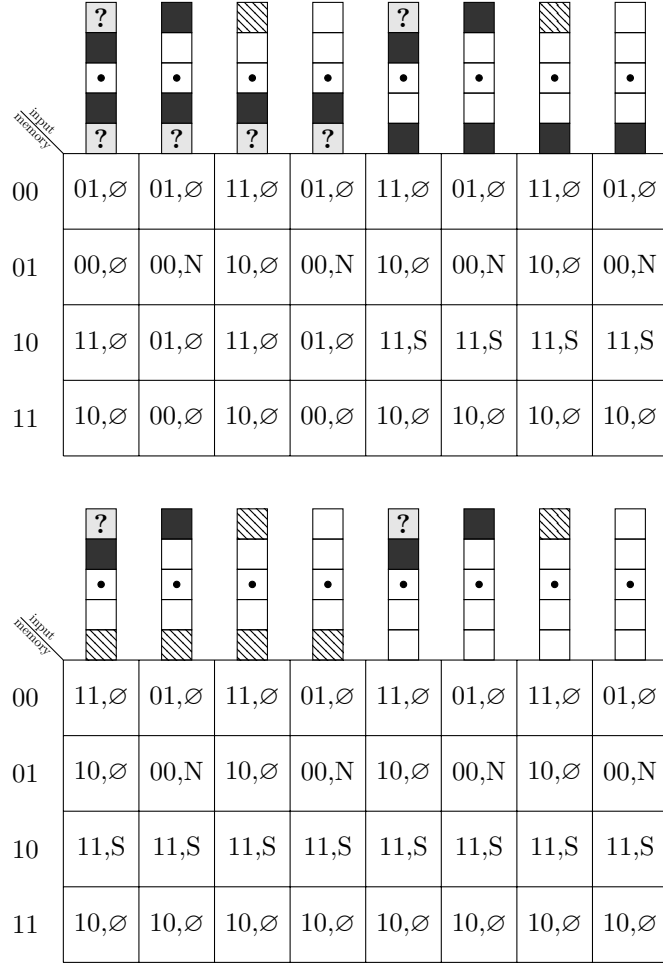| memory \							
00	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅
01	10,∅	00,N	10,∅	00,N	10,∅	00,N	10,∅	00,N
10	11,S	11,S	11,S	11,S	11,S	11,S	11,S	11,S
11	10,∅	10,∅	10,∅	10,∅	10,∅	10,∅	10,∅	10,∅

Figure 5: State machine at position 9