

Figure 1 consists of two parts, (a) and (b), illustrating state machines at different positions.

(a) Position 1:

State	Input Memory 1	Input Memory 2	Output
000	001, \emptyset	001, \emptyset	
001	110, W	010, \emptyset	
010	011, \emptyset	011, \emptyset	
011	000, \emptyset	000, \emptyset	
100			
101			
110			
111			

(b) Position 5:

State	Input Memory 1	Input Memory 2	Input Memory 3	Input Memory 4	Output
000	001, \emptyset	001, \emptyset	101, \emptyset	001, \emptyset	
001	110, W	110, W	010, \emptyset	010, \emptyset	
010	011, \emptyset	011, \emptyset	011, \emptyset	011, \emptyset	
011	000, \emptyset	000, \emptyset	000, \emptyset	000, \emptyset	
100	101, \emptyset	001, \emptyset	101, \emptyset	101, \emptyset	
101	110, N	010, \emptyset	110, N	110, \emptyset	
110	111, \emptyset	111, \emptyset	111, \emptyset	111, \emptyset	
111	100, \emptyset	100, \emptyset	100, \emptyset	100, \emptyset	

Figure 1: State machine(s) (a) at position 1, (b) at position 5.

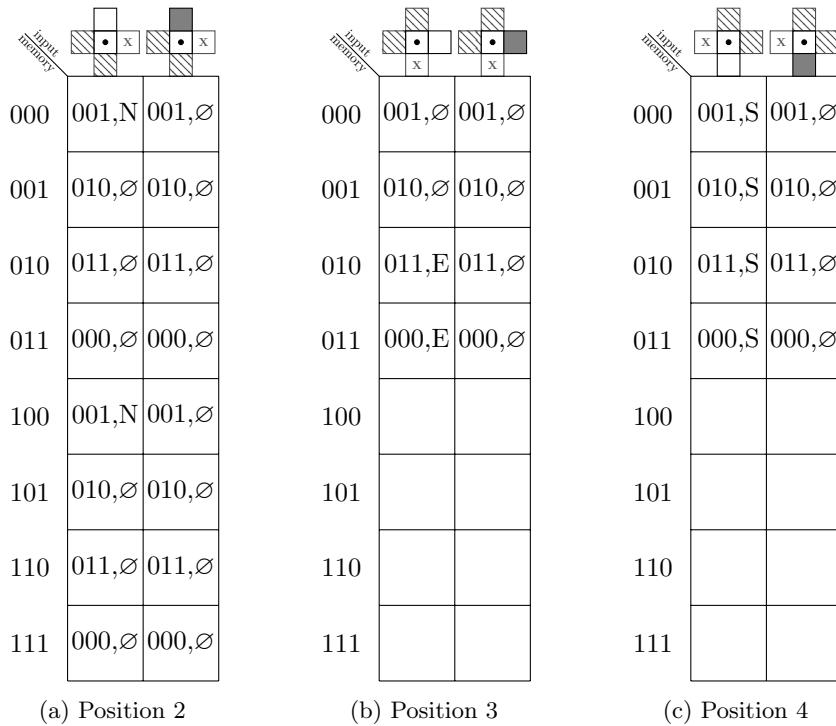


Figure 2: State machine(s) (a) at position 2, (b) at position 3, (c) at position 4.

		input memory			
		•	X	•	X
		X	X	X	X
000	001,N	001,	\emptyset		
001	010, \emptyset	010,	\emptyset		
010	011, \emptyset	011,	\emptyset		
011	000, \emptyset	000,	\emptyset		
100	001,N	001,	\emptyset		
101	010, \emptyset	010,	\emptyset		
110	011, \emptyset	011,	\emptyset		
111	000, \emptyset	000,	\emptyset		

(a) Position 6

		input memory			
		•	X	•	X
		X	X	X	X
000	001, \emptyset	001, \emptyset	001, \emptyset	001, \emptyset	
001	010, \emptyset	010, \emptyset	010, \emptyset	010, \emptyset	
010	011,E	011,E	011, \emptyset	011, \emptyset	
011	000,E	000,E	000, \emptyset	000, \emptyset	
100	001, \emptyset	001, \emptyset	001, \emptyset	001, \emptyset	
101	010, \emptyset	010, \emptyset	010, \emptyset	010, \emptyset	
110	011,S	011, \emptyset	011,S	011, \emptyset	
111	000, \emptyset	000, \emptyset	000, \emptyset	000, \emptyset	

(b) Position 7

Figure 3: State machine(s) (a) at position 6, (b) at position 7.

000	001,S	001,O	001,S	001,O
001	010,S	010,O	010,S	010,O
010	011,S	011,O	011,S	011,O
011	100,W	100,W	000,S	000,O
100				
101				
110				
111				

(a) Position 8

000	001,S	101,O	001,S	001,O
001	010,O	010,O	010,O	010,O
010	011,O	011,O	011,O	011,O
011	000,O	000,O	000,O	000,O
100	101,O	101,O	101,O	101,O
101	110,N	110,N	010,O	110,O
110	111,O	111,O	111,O	111,O
111	100,O	100,O	100,O	100,O

(b) Position 9

Figure 4: State machine(s) (a) at position 8, (b) at position 9.