

input memory			
00	01,∅	01,∅	
01	10,W	00,∅	
10			
11			

Figure 1: State machine at position 1

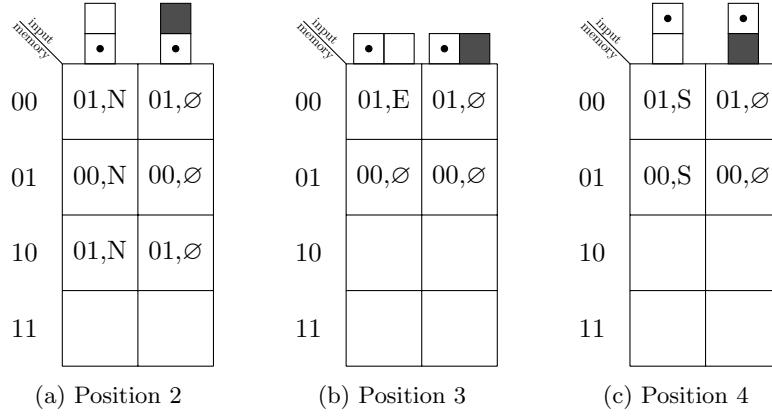


Figure 2: State machines (a) at position 2, (b) at position 3, (c) at position 4

input memory					
00	01,∅	01,∅	11,∅	01,∅	
01	10,W	10,W	00,∅	00,∅	
10	11,∅	11,∅	11,∅	11,∅	
11	10,N	00,∅	10,N	10,N	

Figure 3: State machine at position 5

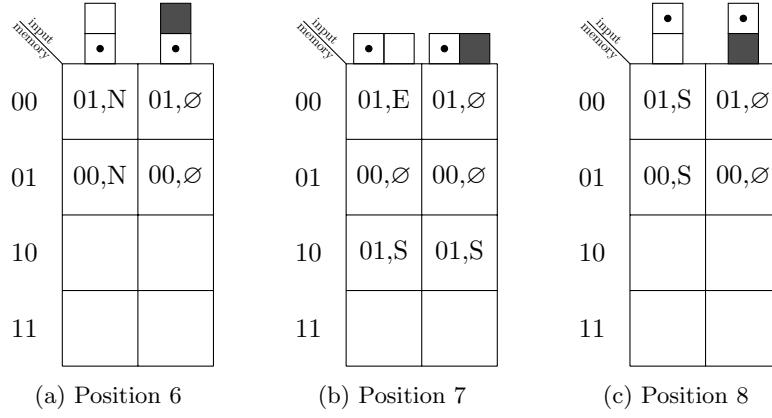


Figure 4: State machines (a) at position 6, (b) at position 7, (c) at position 8

	input memory			
00	01,S	11, \emptyset	01,S	01, \emptyset
01	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset
10	11, \emptyset	11, \emptyset	11, \emptyset	11, \emptyset
11	10,N	10,N	00, \emptyset	10, \emptyset

Figure 5: State machine at position 9