

Diagram of input memory for Figure 1. It consists of four pairs of cells. Each pair has a top cell and a bottom cell. The top cells are white, black, white, and black from left to right. The bottom cells are white with a black dot, white with a black dot, white with a black dot, and white with a black dot from left to right.

	00	01, \emptyset	01, \emptyset	01, \emptyset	01, \emptyset
	01	00, W	00, W	00, \emptyset	00, \emptyset
	10	01, \emptyset	01, \emptyset	01, \emptyset	01, \emptyset
	11	00, W	00, W	00, \emptyset	00, \emptyset

Figure 1: State machine at position 1

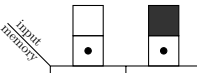


Diagram of input memory for Figure 2(a). It consists of two pairs of cells. The first pair has a white top cell and a white bottom cell with a black dot. The second pair has a black top cell and a white bottom cell with a black dot.

(a) Position 2

00	01, N	01, \emptyset
01	00, N	00, \emptyset
10	01, N	01, \emptyset
11	00, N	00, \emptyset

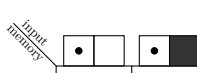


Diagram of input memory for Figure 2(b). It consists of two pairs of cells. The first pair has a white top cell with a black dot and a white bottom cell. The second pair has a white top cell with a black dot and a black bottom cell.

(b) Position 3

00	01, E	01, \emptyset
01	00, E	00, \emptyset
10	01, E	01, \emptyset
11	00, E	00, \emptyset

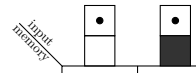


Diagram of input memory for Figure 2(c). It consists of two pairs of cells. The first pair has a white top cell with a black dot and a white bottom cell. The second pair has a white top cell with a black dot and a black bottom cell.

(c) Position 4

00	01, S	01, \emptyset
01	00, S	00, \emptyset
10	01, S	01, \emptyset
11	00, S	00, \emptyset

Figure 2: State machines (a) at position 2, (b) at position 3, (c) at position 4

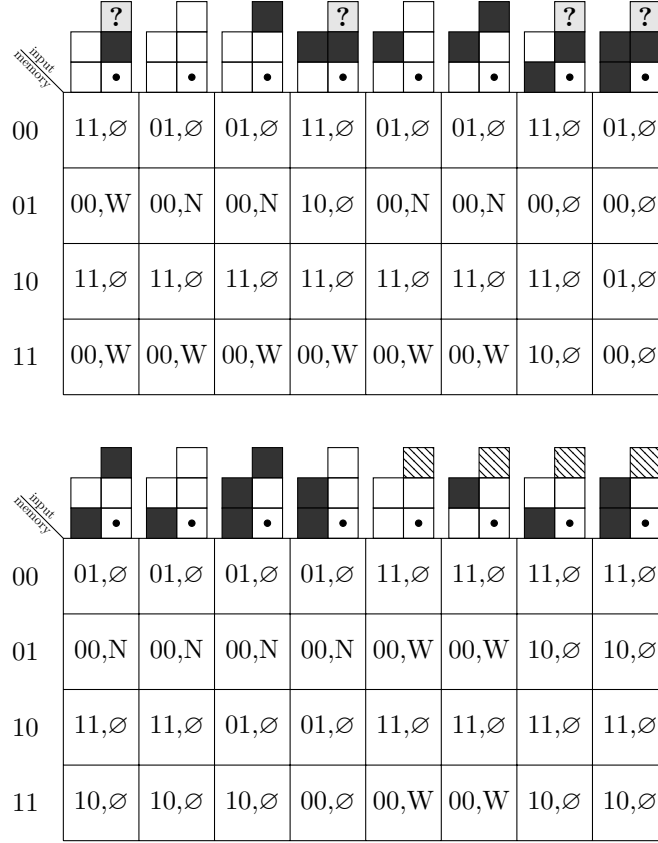


Figure 3: State machine at position 5

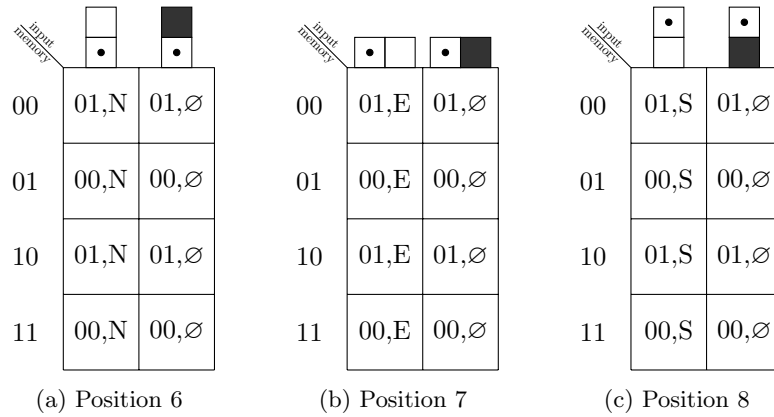


Figure 4: State machines (a) at position 6, (b) at position 7, (c) at position 8

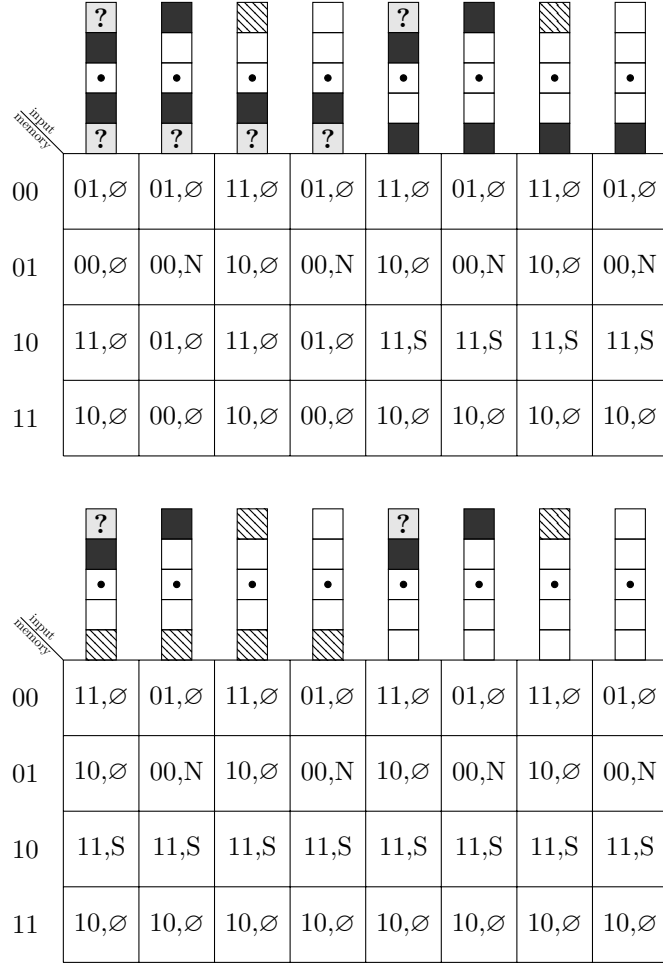


Figure 5: State machine at position 9