Figure 1: State machine at position 1
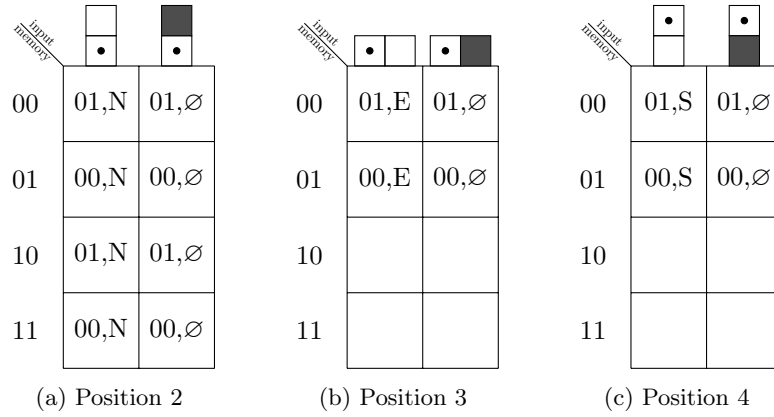

(a) Position 2   (b) Position 3   (c) Position 4

Figure 2: State machines (a) at position 2, (b) at position 3, (c) at position 4

input memory							
00	11,W	11,W	11,W	01,∅	01,∅	01,∅	01,∅	11,∅
01	10,W	10,W	10,W	10,W	10,W	10,W	00,∅	10,∅
10	11,W	11,N	11,∅	01,∅	11,N	11,∅	01,∅	11,∅
11	10,W	10,N	10,N	00,∅	10,N	10,N	10,∅	10,∅

input memory							
00	01,∅	01,∅	11,∅	11,∅	11,W	11,W	01,∅	01,∅
01	00,∅	00,∅	00,∅	10,∅	10,W	10,W	00,∅	00,∅
10	11,∅	11,N	11,∅	11,N	11,W	11,W	01,∅	01,∅
11	10,N	10,N	10,N	10,N	10,W	10,W	00,∅	00,∅

Figure 3: State machine at position 5

input memory		
00	01,N	01,∅
01	00,N	00,∅
10		
11		

(a) Position 6

input memory		
00	01,E	01,∅
01	00,E	00,∅
10		
11		

(b) Position 7

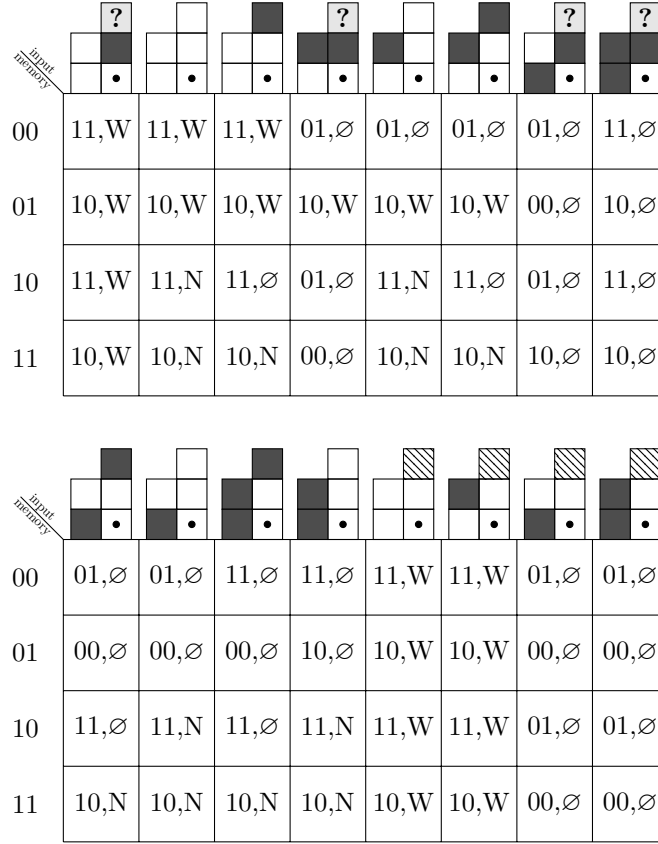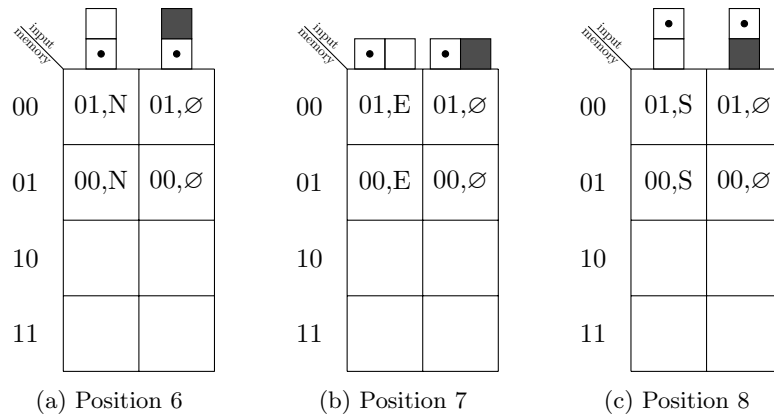| input memory		
00	01,S	01,∅
01	00,S	00,∅
10		
11		

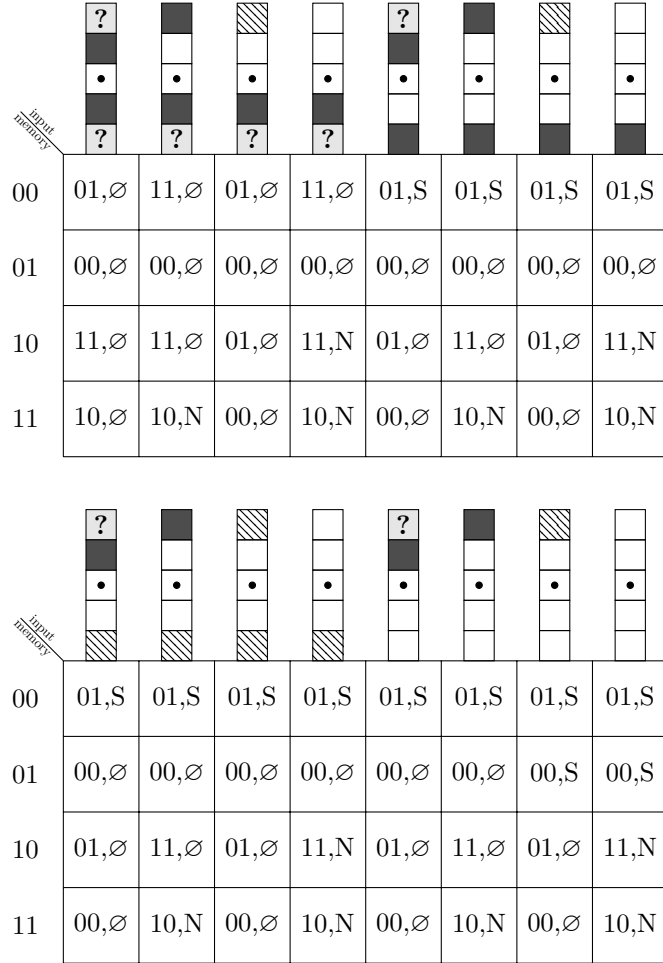(c) Position 8

Figure 4: State machines (a) at position 6, (b) at position 7, (c) at position 8

Figure 5: State machine at position 9