

input memory								
00	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset	01,S	01,S	01,S	01,S
01	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset
10	11, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset
11	10, \emptyset	10,N	00, \emptyset	10,N	00, \emptyset	10,N	00, \emptyset	10,N

input memory								
00	01,S	01,S	01,S	01,S	01,S	01,S	01,S	01,S
01	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset	00, \emptyset
10	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset	01, \emptyset	11, \emptyset
11	00, \emptyset	10,N	00, \emptyset	10,N	00, \emptyset	10,N	00, \emptyset	10,N