

Figure 1: State machine(s) (a) at position 1, (b) at position 2, (c) at position 3, (d) at position 4.

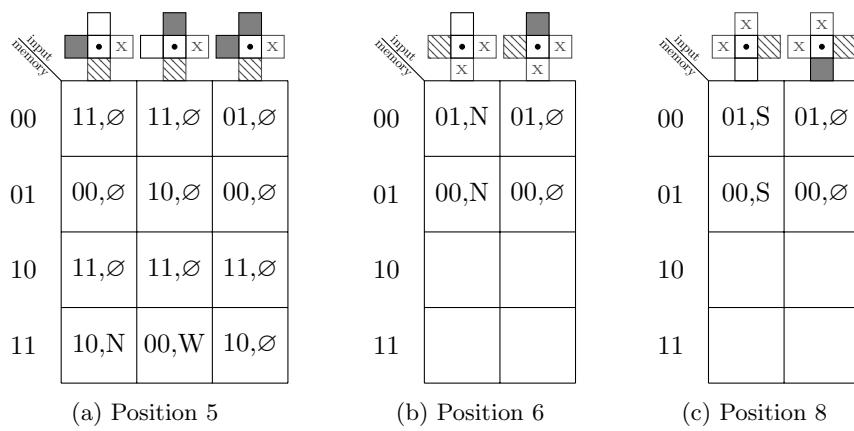


Figure 2: State machine(s) (a) at position 5, (b) at position 6, (c) at position 8.

Diagram illustrating the state machine at Position 7. The input memory is shown above the state table.

00	01, \emptyset	11, S	01, \emptyset
01	10, \emptyset	00, \emptyset	00, \emptyset
10	01, E	11, \emptyset	11, \emptyset
11	10, \emptyset	10, \emptyset	10, \emptyset

(a) Position 7

Diagram illustrating the state machine at Position 9. The input memory is shown above the state table.

00	01, S	11, \emptyset	01, \emptyset
01	00, \emptyset	00, \emptyset	00, \emptyset
10	11, \emptyset	11, \emptyset	01, \emptyset
11	00, \emptyset	10, N	10, \emptyset

(b) Position 9

Figure 3: State machine(s) (a) at position 7, (b) at position 9.