input/memory				
00	01,∅	01,∅	01,∅	01,∅
01	10,W	10,W	00,∅	00,∅
10				
11				

(a) Position 1

Figure 1: State machine(s) (a) at position 1.

input/memory		
00	01,N	01,∅
01	00,N	00,∅
10	01,N	01,∅
11		

(a) Position 2

input/memory		
00	01,E	01,∅
01	00,E	00,∅
10		
11		

(b) Position 3

input/memory		
00	01,S	01,∅
01	00,S	00,∅
10		
11		
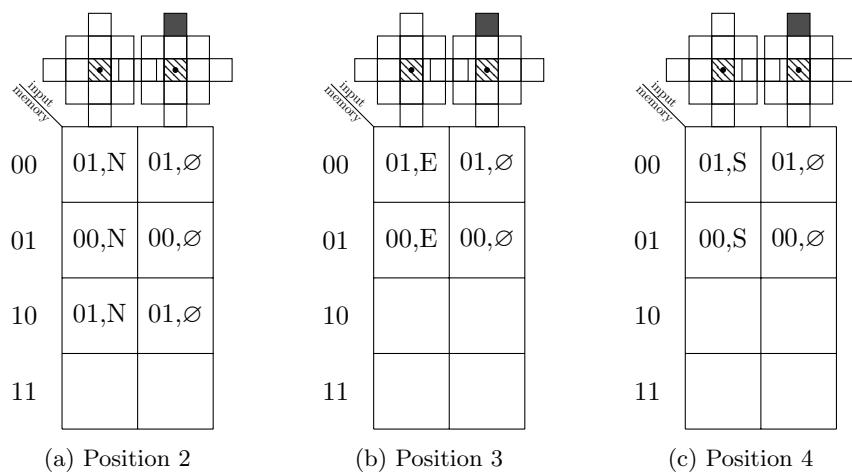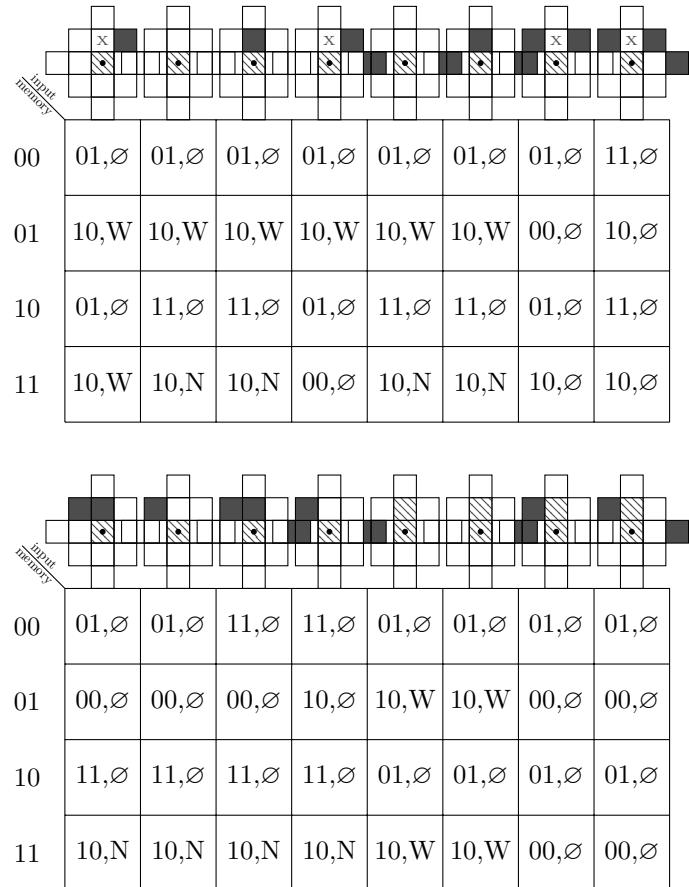
(c) Position 4

Figure 2: State machine(s) (a) at position 2, (b) at position 3, (c) at position 4.

00	01,∅	01,∅	01,∅	01,∅	01,∅	01,∅	01,∅	11,∅
01	10,W	10,W	10,W	10,W	10,W	10,W	00,∅	10,∅
10	01,∅	11,∅	11,∅	01,∅	11,∅	11,∅	01,∅	11,∅
11	10,W	10,N	10,N	00,∅	10,N	10,N	10,∅	10,∅

00	01,∅	01,∅	11,∅	11,∅	01,∅	01,∅	01,∅	01,∅
01	00,∅	00,∅	00,∅	10,∅	10,W	10,W	00,∅	00,∅
10	11,∅	11,∅	11,∅	11,∅	01,∅	01,∅	01,∅	01,∅
11	10,N	10,N	10,N	10,N	10,W	10,W	00,∅	00,∅

(a) Position 5

Figure 3: State machine(s) (a) at position 5.

00	01,N	01,∅
01	00,N	00,∅
10		
11		

(a) Position 6

00	01,E	01,∅
01	00,E	00,∅
10		
11		

(b) Position 7

|		
00	01,S	01,∅
01	00,S	00,∅
10		
11		
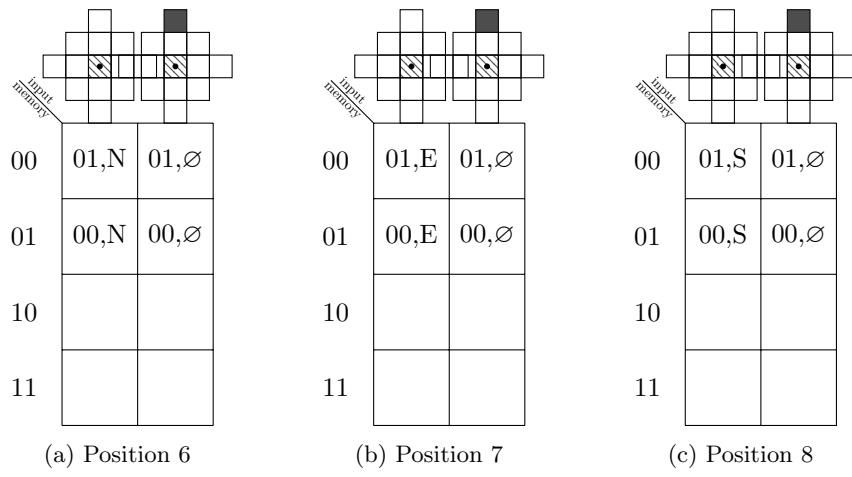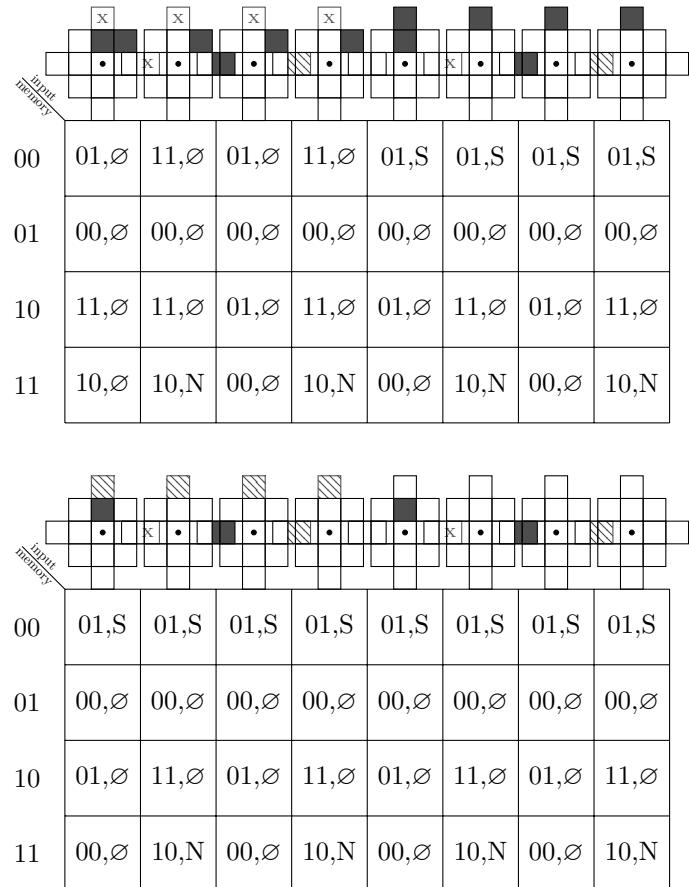
(c) Position 8

Figure 4: State machine(s) (a) at position 6, (b) at position 7, (c) at position 8.

input/memory							
00	01,∅	11,∅	01,∅	11,∅	01,S	01,S	01,S
01	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅
10	11,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅
11	10,∅	10,N	00,∅	10,N	00,∅	10,N	00,∅

(Row 00: 01,∅ | 11,∅ | 01,∅ | 11,∅ | 01,S | 01,S | 01,S | 01,S)

input/memory							
00	01,S	01,S	01,S	01,S	01,S	01,S	01,S	01,S
01	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅
10	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅
11	00,∅	10,N	00,∅	10,N	00,∅	10,N	00,∅	10,N

(a) Position 9

Figure 5: State machine(s) (a) at position 9.