

	<div>input memory</div>	<div></div>	<div>•</div>	<div></div>	<div>•</div>
00		01, \emptyset		01, \emptyset	
01		10, \emptyset		00, \emptyset	
10		11, \emptyset		11, \emptyset	
11		10,W		10, \emptyset	

Figure 1: State machine at position 1

	<div>input memory</div>	<div></div>	<div>•</div>	<div></div>	<div>•</div>
00		01,N		01, \emptyset	
01		00,N		00, \emptyset	
10		01,N		01, \emptyset	
11					

(a) Position 2

	<div>input memory</div>	<div>•</div>	<div></div>	<div>•</div>	<div></div>
00		01,E		01, \emptyset	
01		00, \emptyset		00, \emptyset	
10					
11					

(b) Position 3

	<div>input memory</div>	<div>•</div>	<div></div>	<div>•</div>	<div></div>
00		01,S		01, \emptyset	
01		00,S		00, \emptyset	
10					
11					

(c) Position 4

Figure 2: State machines (a) at position 2, (b) at position 3, (c) at position 4

	<div>input memory</div>	<div></div>	<div>•</div>	<div></div>	<div>•</div>	<div></div>	<div>•</div>	<div></div>	<div>•</div>
00		01, \emptyset		01, \emptyset		11, \emptyset		01, \emptyset	
01		10,W		10,W		00, \emptyset		00, \emptyset	
10		11, \emptyset		11, \emptyset		11, \emptyset		11, \emptyset	
11		10,N		00, \emptyset		10,N		10, \emptyset	

Figure 3: State machine at position 5

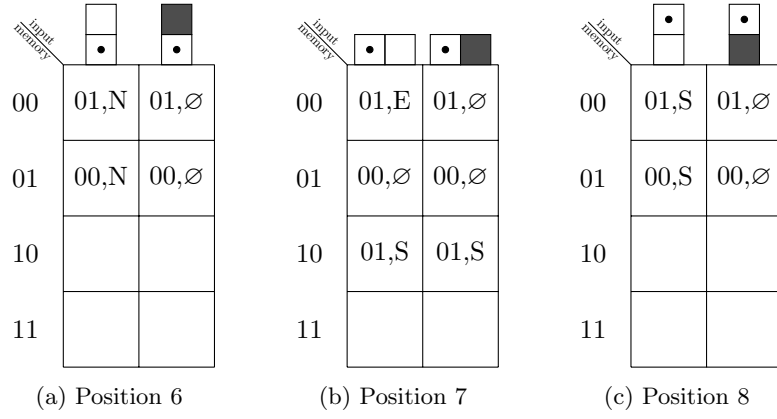


Figure 4: State machines (a) at position 6, (b) at position 7, (c) at position 8

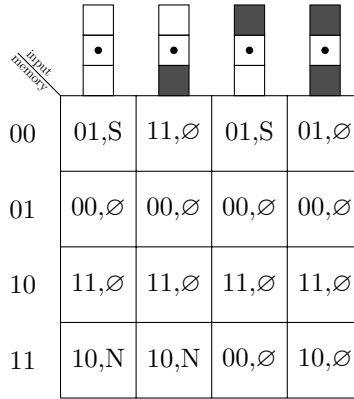


Figure 5: State machine at position 9