

Diagram illustrating a memory state transition table. The columns represent the initial state of the input memory, and the rows represent the final state. The states are represented by stacks of colored rectangles: black, white, hatched, and question mark.

The table shows the following transitions:

	01,?	11,?	01,?	11,?	01,S	01,S	01,S	01,S
00	01,∅	11,∅	01,∅	11,∅	01,S	01,S	01,S	01,S
01	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅
10	11,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅
11	10,∅	10,N	00,∅	10,N	00,∅	10,N	00,∅	10,N

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The table shows the following transitions:

	01,S							
00	01,S							
01	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅	00,∅
10	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅	01,∅	11,∅
11	00,∅	10,N	00,∅	10,N	00,∅	10,N	00,∅	10,N