4/30/2015 cache.c

## /home/diana/Documents/Comp Org/Project/cache.c

```
1 #include "cache.h"
4 #include<stdlib.h>
5 #include<math.h>
6 #include<stdio.h>
7 #include <sys/ioctl.h>
8 #include <unistd.h>
10 //#define CACHE DEBUG
11 //#define CACHE POINTER DEBUG
12 //#define CACHE INITIALIZE DEBUG
13
14 void updateLRU(struct Cache* cache, struct Block * tempBlock,
       unsigned long long targetIndex);
15
16
17 int debugFlag = 0, pointerFlag = 0;
18
20 struct Cache * initialize(int newCacheSize, int newBlockSize, int newAssociativity, int newMissTime, int newHitTime) {
21
     struct Cache *cache;
22
23
     // Create in memory
     cache = (struct Cache*) malloc(sizeof (struct Cache));
24
25
26
     // Initialize cache variable fields based on configuration files
27
     cache->cacheSize = newCacheSize;
28
     cache->blockSize = newBlockSize;
29
     cache->associativity = newAssociativity;
     cache->lengthOfWay = (newCacheSize / newBlockSize) / newAssociativity;
30
31
32
     // log(base2)(lengthOfWay)
33
     cache->indexFieldSize = log(cache->lengthOfWay) / log(2);
34
35
     // log(base2)(newBlockSize)
     cache->byteOffsetSize = log(newBlockSize) / log(2);
36
     cache->missTime = newMissTime:
37
38
     cache->hitTime = newHitTime;
39
40
    // Initialize tracking variables to 0
41
     cache->hits = 0;
    cache->misses = 0;
42
43
    cache->writeRefs = 0;
    cache->readRefs = 0;
45
    cache->insRefs = 0;
    cache->instructionTime = 0:
46
     cache->readTime = 0;
47
48
    cache->writeTime = 0;
49
    cache->flushTime = 0;
    cache->transfers = 0:
50
     cache->kickouts = 0;
51
52
     cache->dirtyKickouts = 0;
53
     cache->flushKickouts = 0;
54
55
56
     // Initialize array of blocks
     cache->blockArray = (struct Block **) malloc(sizeof (struct Block *) * (cache->lengthOfWay));
57
58
59
     int i = 0, j = 0; // used for iteration
60
     for (i = 0; i < \text{cache-}> \text{lengthOfWay}; i++) {
       // First column in blockArray will be a dummy pointer
61
       // => add an extra "way"(column)
62
63
       // Go through each row and allocate space for amount of blocks
       cache->blockArray[i] = (struct Block *)
64
65
            malloc(sizeof (struct Block) * (newAssociativity + 1));
66
       // Go through and initialize blocks (columns)
67
       for (j = 0; j < newAssociativity; j++) {
68
69
70
         // Set valid, dirty, and tag fields
         cache->blockArray[i][j].valid = 0;
71
         cache->blockArray[i][j].dirty = 0;
```

1/8

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4/30/2015
                                                                             cache.c
  73
            cache->blockArray[i][j].tag = 0;
  74
  75
            // Point to next block in LRU chain
  76
            cache->blockArray[i][j].nextBlock =
  77
                 &(cache->blockArray[i][j + 1]);
  78
  79
  80
         // Initialize last block to null
  81
         cache->blockArray[i][newAssociativity].nextBlock = NULL;
  82
         cache->blockArray[i][newAssociativity].valid = 0;
  83
         cache->blockArray[i][newAssociativity].dirty = 0;
  84
         cache->blockArray[i][newAssociativity].tag = 0;
  85
       }
  86
  87
       // Done
  88
       return cache;
  89 }
  90 //
  92
  93 unsigned long long moveBlock(struct Cache* cache, unsigned long long targetTag, unsigned long long targetIndex, int isDirty) {
       cache->transfers++; // Transferring data into cache
  95
  96
       struct Block *tempBlock; // Used for LRU policy implementation
  97
  98
       // Point to the first block in the index
  99
       tempBlock = cache->blockArray[targetIndex][0].nextBlock;
 100
 101
        while (tempBlock->nextBlock != NULL && tempBlock->valid) { // Find the LRU
          tempBlock = tempBlock->nextBlock;
 102
 103
 104
        if ((!tempBlock->valid) || (!tempBlock->dirty)) {
 105
          if (debugFlag) {
 106
 107
                       Block was invalid or clean: good to overwrite.\n");
 108
 109
          if (tempBlock->valid && !(tempBlock->dirty)) {
 110
 111
            cache->kickouts++; // Kicking out a previously valid, clean block
 112
 113
          // Block is invalid or clean, feel free to write over it
 114
          tempBlock->tag = targetTag;
 115
          tempBlock->valid = 1;
 116
 117
          tempBlock->dirty = 0;
 118
 119
          // May have been written back due to a dirty/flush kick-out
 120
 121
            tempBlock->dirty = 1; // Needs to be written as dirty
 122
 123
 124
          cache->writeRefs++; // Seen as a write request
 125
          cache->writeTime += cache->hitTime;
 126
 127
          // Update LRU chain
          // Move block to front of LRU chain
 128
 129
          if (cache->blockArray[targetIndex][0].nextBlock != tempBlock) {
 130
            updateLRU(cache, tempBlock, targetIndex);
 131
 132
          return 0;
 133
        }
 134
 135
        if (debugFlag) {
          printf("~~~> Block was dirty, need to return overwritten tag...");
 136
 137
 138
 139
       // If you made it to this point, you're pointing to the last block in chain
 140
       // All blocks before it would have been valid, and this block is dirty
 141
        unsigned long long returnedTag = tempBlock->tag; // track dirty tag
 142
        cache->dirtyKickouts++;
       cache->writeRefs++;
 143
       cache->writeTime += cache->hitTime;
 144
 145
 146
 147
       tempBlock->tag = targetTag; // Put in new targetTag into block
```

file:///home/diana/cache.html

```
148
      if (isDirty)
        tempBlock->dirty = 1; // mark as clean
149
150
      else
151
         tempBlock->dirty = 0; // mark as clean
152
153
      // Update LRU chain
154
      // Move block to front of LRU chain
155
      if (cache->blockArray[targetIndex][0].nextBlock != tempBlock) {
156
        updateLRU(cache, tempBlock, targetIndex);
157
158
159
      return returnedTag; // signal that there was a dirty kick out
160 }
161
162 int scanCache(struct Cache* cache, unsigned long long targetTag, unsigned long long targetIndex, char op) {
163
      struct Block * tempBlock; // Used for LRU policy implementation
164
      // Point to the first block in the index
165
      tempBlock = cache->blockArray[targetIndex][0].nextBlock;
166
167
168
      while (tempBlock != NULL) {
         if (tempBlock->valid) { // Only check blocks if they are valid
169
           if (tempBlock->tag == targetTag) {
170
171
             // Found the tag! Increment hits
             cache->hits++;
172
173
174
             if(op == 'W') {
175
                // valid targetTag found in cache, write operation
176
                // => mark block as dirty
177
                tempBlock->dirty = 1;
178
179
                // increase write time by hit penalty
                cache->writeTime += cache->hitTime;
180
181
                cache->writeRefs++;
              } else if (op == 'R') { // Read instructions
182
183
                // increase read time by hit penalty
                cache->readTime += cache->hitTime;
184
                cache->readRefs++;
185
              } else if (op == 'I') {
186
187
                // increase instruction time by hit penalty
                cache->instructionTime += cache->hitTime;
188
189
                cache->insRefs++;
190
191
192
             // Move block to front of LRU chain
193
             if (cache->blockArray[targetIndex][0].nextBlock != tempBlock) {
194
                updateLRU(cache, tempBlock, targetIndex);
195
196
197
             return 1;
198
199
           } else { // Tag didn't match
200
             tempBlock = tempBlock->nextBlock;
201
202
         } else {
           tempBlock = tempBlock->nextBlock;
203
204
205
      // Target tag not found in cache
206
207
      cache->misses++;
208
209
      // increase times by miss penalty
210
      if(op == 'W') 
211
         cache->writeTime += cache->missTime;
212
         cache->writeRefs++;
213
      } else if (op == 'R') {
214
         cache->readTime += cache->missTime;
215
         cache->readRefs++;
      } else if (op == 'I') {
216
217
        cache->instructionTime += cache->missTime;
         cache->insRefs++;
218
219
220
      return 0;
221 }
```

file://home/diana/cache.html 3/8

4/30/2015 cache.c

```
223 // Purpose: move tempBlock to the front of the LRU chain with index targetIndex
224
225 void updateLRU(struct Cache* cache, struct Block * tempBlock,
226
         unsigned long long targetIndex) {
227
      // firstBlock points to the old start of the chain
228
      struct Block * firstBlock = cache->blockArray[targetIndex][0].nextBlock;
229
230
      // Put block at start of chain
231
      cache->blockArray[targetIndex][0].nextBlock = tempBlock;
232
233
      tempBlock = firstBlock;
      while (tempBlock->nextBlock != cache->blockArray[targetIndex][0].nextBlock) {
234
235
        // Find block that used to be before tempBlock
236
         tempBlock = tempBlock->nextBlock;
237
238
      // Point tempBlock at start of chain
239
      struct Block * temp2 = cache->blockArray[targetIndex][0].nextBlock;
      tempBlock->nextBlock = temp2->nextBlock; //
240
      temp2->nextBlock = firstBlock;
241
242
      return;
243 }
244
245 void printCacheStatus(struct Cache * cache, int * cacheIndexCounter) {
246
      if (debugFlag) {
                      247
         printf("*
248
         printf("Cache length: %d, index field size: %d, byte field size: %d\n",
249
             cache->lengthOfWay, cache->indexFieldSize, cache->byteOffsetSize);
250
251
         printf("Number of hits: %llu, misses: %llu, kick-outs: %llu, dirty kick-outs: %llu\n",
             cache->hits, cache->misses, cache->kickouts, cache->dirtyKickouts);
252
253
254
         printf("Number of flush kick-outs: %llu, invalidates: %llu, flush time: %llu\n",
255
             cache->flushKickouts, cache->invalidates, cache->flushTime);
256
257
         printf("Reference Counts: writes = %llu, reads = %llu, instructions = %llu\n",
258
             cache->writeRefs, cache->readRefs, cache->insRefs);
259
         printf("Times: writes = %llu, reads = %llu, instructions = %llu\n",
260
261
             cache->writeTime, cache->readTime, cache->instructionTime);
262
263
         // Go through each row of the cache
         printf("----- Current Row Status -----\n");
264
265
266
      int i = 0, j = 0;
267
      for (i = 0; i < \text{cache-} > \text{lengthOfWay}; i++) 
268
         if (cacheIndexCounter[i] == 1) {
269
           printf("Index %llx:\t", (unsigned long long) i);
270
           for (j = 1; j < (cache->associativity + 1); j++) {
271
              // Skip the first column (dummy pointer)
272
             if (cache->blockArray[i][j].valid != 0) // Block has a tag
273
                printf("| V: %d, D: %d Tag: \t%llx|",
                  cache->blockArray[i][j].valid,
274
                  cache->blockArray[i][j].dirty,
275
276
                  cache->blockArray[i][j].tag);
             else // Block doesn't have a tag in it
277
               printf("| V: %d, D: %d Tag: - |",
278
279
                  cache->blockArray[i][j].valid,
280
                  cache->blockArray[i][j].dirty);
281
           printf("\n");
282
283
284
285
      printf("\n");
286
287
      if (pointerFlag) {
        printf("
288
                           ------ Pointer Contents -----\n\n'');
289
         int row = 0, col = 0;
290
         for (row = 0; row < cache->lengthOfWay; row++) {
291
           if (cacheIndexCounter[row] == 1) {
292
                      Index: %llx", (unsigned long long) row);
293
             for (col = 0; col < cache->associativity + 1; col++) {
294
                struct Block* thisBlock = &cache->blockArray[row][col];
295
                printf("||This block:%p, points to %p||\t", thisBlock,
296
                     thisBlock->nextBlock);
297
```

file://home/diana/cache.html 4/8

file://home/diana/cache.html 5/8

4/30/2015 cache.c 373 // Pull out the L2 index/tag 374 indexTemp = (~0) << (12Cache->indexFieldSize + 375 12Cache->byteOffsetSize); 376 tagTemp = indexTemp; $indexTemp = \sim indexTemp;$ 377 378 indexTemp = indexTemp & addressTemp; 379 // Pull out the L2 index

indexTemp = indexTemp >> iCache->byteOffsetSize;

380 381

```
382
                  tagTemp = tagTemp & addressTemp;
  383
                  tagTemp = tagTemp >> (iCache->indexFieldSize +
                      iCache->byteOffsetSize); // Pull out the L2 tag
  384
  385
  386
                  // Ready to write back to the L2 cache
  387
                  addressTemp = 0; // Reset to 0
  388
  389
                  if (debugFlag) {
  390
                    printf("~~~> Writing dirtyBlock back to L2 cache.\n");
  391
  392
                  addressTemp = moveBlock(I2Cache, tagTemp, indexTemp, 1);
  393
  394
                  // Move dirty block from L1->L2
                  iCache->flushTime += transferTime *
  395
  396
                      (iCache->blockSize / busWidth);
  397
  398
                  if (addressTemp) {
  399
                    // Kicked out another block, add to the flush time
  400
                    if (debugFlag) printf("---> Kicked out another dirty block in L2 cache, writing it back to main memory");
  401
                    12Cache->flushKickouts++; // Increment L2 flushKickouts
  402
                    iCache->flushTime += mainMemoryTime;
  403
  404
               }
  405
  406
             tempBlock = tempBlock->nextBlock;
  407
  408
  409
  410
        // Have completed flushing the iCache, repeat for the dCache
  411
        if (debugFlag) {
                     > FINISHED FLUSHING ICACHE!!! \n\n"
  412
           printf("~
                    Starting to flush the dCache\n");
  413
  414
        for (index = 0; index < dCache->lengthOfWay; index++) {
  415
           tempBlock = dCache->blockArray[index][0].nextBlock;
  416
  417
           // Start at the beginning of LRU chain and go all the way through
  418
           while (tempBlock != NULL) {
  419
             // Found a valid block
  420
             if (tempBlock->valid) {
               tempBlock->valid = 0;
  421
  422
               tempBlock->tag = 0;
  423
               dCache->invalidates++; // increment invalidate counter
  424
               if (debugFlag) {
  425
                  printf("Block is now invalid, moving on\n");
  426
  427
  428
               // Check to see if it was also dirty
               if (tempBlock->dirty) {
  429
  430
                  if (debugFlag) {
  431
                    printf("~~~> Block was dirty, incrementing dCache flush kickouts\n");
  432
  433
  434
                  dCache->flushKickouts++;
  435
                  tempBlock->dirty = 0;
  436
                  // Get ready to write it back to the L2 Cache
  437
                  // Start with 0-filled variables
  438
  439
                  tagTemp = 0;
                  indexTemp = 0;
  440
  441
                  addressTemp = 0;
  442
                 // Get the L1 tag and shift it over to the right spot
  443
                  tagTemp = tempBlock->tag;
  444
                  tagTemp = tagTemp << (dCache->indexFieldSize +
  445
  446
                      dCache->byteOffsetSize);
  447
file:///home/diana/cache.html
```

6/8

4/30/2015 cache.c 448 // Get the L1 index and shift it over to the right spot 449 indexTemp = index;indexTemp = indexTemp << dCache->byteOffsetSize; 450 451 // Combine for the original address, minus the byte size 452 453 addressTemp = indexTemp + tagTemp; 454 455 // Pull out the L2 index/tag 456  $indexTemp = (\sim 0) \ll (12Cache->indexFieldSize +$ 457 12Cache->byteOffsetSize); tagTemp = indexTemp; 458  $indexTemp = \sim indexTemp;$ 459 indexTemp = indexTemp & addressTemp; 460 461 // Pull out the L2 index 462 indexTemp = indexTemp >> 12Cache->byteOffsetSize; 463 tagTemp = tagTemp & addressTemp; 464 tagTemp = tagTemp >> (12Cache->indexFieldSize + 465 12Cache->byteOffsetSize); // Pull out the L2 tag 466 467 468 // Ready to write back to the L2 cache addressTemp = 0; // Reset to 0469 470 471 if (debugFlag) { printf("---> Writing dirtyBlock back to L2 cache. Check to see if it's in there\n"); 472 473 474 if (!scanCache(12Cache, tagTemp, indexTemp, 'W')) { 475 if (debugFlag) { 476 printf("~ → Block wasn't in the L2 Cache, Adding miss time (+%d).\n", 477 12Cache->missTime); 478 479 l2Cache->flushTime += l2Cache->missTime; // Desired index/tag is now in L2 as dirty 480 481 addressTemp = moveBlock(12Cache, tagTemp, indexTemp, 1); 482 483 if (addressTemp) { 484 // Kicked out another block, add to the flush time if (debugFlag) printf("---> Kicked out another dirty block in L2 cache, adding L2 -> main memory time(+%d)\n", mainMemoryTime); 485 486 // Increment L2 flushKickouts 12Cache->flushKickouts++; 487 l2Cache->flushTime += mainMemoryTime; 488 489 490 491 if (debugFlag) { 492 > Bringing desired block into L2 Cache, adding main memory -> L2 time (+%d)\n", printf("~ 493 mainMemoryTime); printf("~~~> Adding L2 replay time (+%d)\n", 494 495 12Cache->hitTime); 496 497 l2Cache->flushTime += mainMemoryTime; 498 12Cache->flushTime += 12Cache->hitTime; 499 } else { 500 if (debugFlag) { 501 >> Block was in the L2 Cache, Adding hit time (+%d).\n", 12Cache->hitTime); 502 l2Cache->flushTime += l2Cache->hitTime; 503 504 505 506 printf("Transferring dirty block from L1->L2, adding transfer time (+%d)\n", 507 transferTime \* (dCache->blockSize / busWidth)); 508 // Move dirty block from L1->L2 dCache->flushTime += transferTime \* 509 510 (dCache->blockSize / busWidth); 511 512 513 tempBlock = tempBlock->nextBlock; 514 515 516 // Have completed flushing the dCache, repeat for the 12Cache 517

file://home/diana/cache.html

518

519

520

521

if (debugFlag) {

> FINISHED FLUSHING DCACHE!!! \n\n"

> Starting to flush the l2Cache\n");

for (index = 0; index < 12Cache->lengthOfWay; index++) {

printf("

4/30/2015 cache.c

```
523
         tempBlock = 12Cache->blockArray[index][0].nextBlock;
524
        // Start at the beginning of the LRU chain and go all the way through
525
         while (tempBlock != NULL) {
526
           // Found a valid block
           if (tempBlock->valid) {
527
             tempBlock->valid = 0;
528
             tempBlock->tag = 0;
529
530
             12Cache->invalidates++; // increment invalidate counter
             if (debugFlag) {
531
               printf("Block is now invalid, moving on\n");
532
533
534
535
             // Check to see if it was also dirty
             if (tempBlock->dirty) {
536
               if (debugFlag) {
537
                  printf("~~
538
                            > Block was dirty, incrementing 12Cache flush kickouts\n");
539
540
541
               12Cache->flushKickouts++;
542
               tempBlock->dirty = 0; // Reset to clean
543
544
               // Write it back to main memory
545
               12Cache->flushTime += mainMemoryTime;
546
547
548
           tempBlock = tempBlock->nextBlock;
549
        }
550
      }
551
552
      if (debugFlag) {
        printf("---> L2 Cache flushed, all caches flushed, all blocks invalidated\n"
553
                  Completed flushCache function");
554
555
556 }
557
558 void setDebugStatus(int status) {
559
      debugFlag = status;
560
      return;
561 }
562
```

file:///home/diana/cache.html