

show running-config

```
Current configuration: 988 bytes
version 12.4
service timestamps debug datetime msec
service timestamps log datetime msec
no service password-encryption
hostname Router
boot-start-marker
boot-end-marker
logging message-counter syslog
no aaa new-model
dot11 syslog
ip source-route
ip cef
no ipv6 cef
multilink bundle-name authenticated
voice-card 0
archive
log config
hidekeys
interface FastEthernet0/0
ip address 192.168.1.10 255.255.255.0
duplex auto
speed auto
interface FastEthernet0/1
no ip address
shutdown
duplex auto
speed auto
interface Serial0/2/0
ip address 10.7.8.9 255.255.255.0
no fair-queue
clock rate 2000000
interface Serial0/2/1
ip address 10.5.100.78 255.255.255.252
clock rate 2000000
ip forward-protocol nd
no ip http server
no ip http secure-server
control-plane
line con 0
line aux 0
line vty 0 4
login
scheduler allocate 20000 1000
end
```



show ip int brief

Interface IP-Address OK? Method Status Protocol FastEthernet0/0 192.168.1.10 YES manual up up FastEthernet0/1 unassigned YES unset administratively down down Serial0/2/0 10.7.8.9 YES manual down down Serial0/2/1 10.5.100.78 YES manual down down



show version

Cisco IOS Software, 2800 Software (C2800NM-ADVENTERPRISEK9-M), Version 12.4(24)T2, RELEASE SOFTWARE (fc2)
Technical Support: http://www.cisco.com/techsupport

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ROM: System Bootstrap, Version 12.4(13r)T11, RELEASE SOFTWARE (fc1)

Router uptime is 1 week, 2 days, 23 hours, 45 minutes System returned to ROM by reload at 12:59:28 UTC Fri Dec 23 2016 System image file is "flash:c2800nm-adventerprisek9-mz.124-24.T2.bin"

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A summary of U.S. laws governing Cisco cryptographic products may be found at: http://www.cisco.com/wwl/export/crypto/tool/stqrg.html

If you require further assistance please contact us by sending email to export@cisco.com.

Cisco 2811 (revision 53.51) with 512000K/12288K bytes of memory. Processor board ID FCZ142270AY
2 FastEthernet interfaces
2 Serial(sync/async) interfaces
1 Virtual Private Network (VPN) Module
DRAM configuration is 64 bits wide with parity enabled.
239K bytes of non-volatile configuration memory.

Configuration register is 0x2102

62720K bytes of ATA CompactFlash (Read/Write)