



DEPARTMENT OF INFORMATICS

TECHNISCHE UNIVERSITÄT MÜNCHEN

Bachelor's Thesis in Informatics

**Implementation of Attribute-Based
Encryption in Rust on ARM Cortex M
Processors**

Daniel Bücheler



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**Implementierung von Attributbasierter
Verschlüsselung in Rust auf ARM Cortex
M Prozessoren**

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I confirm that this bachelor's thesis in informatics is my own work and I have documented all sources and material used.

Munich, 15.04.2021

Daniel Bücheler

Acknowledgements

Abstract

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1 Introduction

The Internet of Things (IoT) offers great potential for improved efficiency, raised comfort and better safety in many areas. Applications of the IoT include household devices (“Smart Home”), infrastructure, manufacturing, healthcare and many other fields.

The increasing ubiquity of connected sensors and actuators in the “things” around us raises serious concerns about privacy and security. For example, an insecure “Smart Home” might reveal to burglars that its owners are on vacation (e.g. if they can see that the heating is turned off and no windows have been opened for a few days). Even worse, the “Smart Door Lock” might even be exploited to let them right in.

The tools to secure even the largest networks exists; cryptography and related protocols (e.g. TLS) exist and are quite widespread.

Despite the very real threats if security in IoT is not implemented properly, many applications are still not secure. One reason for this is that, due to large numbers of connected devices, IoT hardware must be cheap. Therefore, most of the “things” participating in the IoT are severely constrained in terms of computational power, memory and storage size and power consumption. This limits the applicability of many modern cryptographic concepts (e.g. signatures and, sometimes, even public-key encryption) because the devices involved are simply too slow and small.

In recent years, small microcontrollers have become much cheaper and more powerful: Increasingly, 8-bit architectures are replaced by embedded 32-bit architectures such as ARM Cortex even for simple applications. This brings a large increase in computational performance, storage and memory size. For the first time, even more advanced cryptographic constructions such as those based on bilinear pairings become feasible even on the smallest of connected sensors.

The feasibility of one such relatively new type of cryptosystems shall be examined in this thesis: Attribute-Based Encryption (ABE) allows very intuitive and fine-grained access control by means of expressive access policies specified at the time of encryption or key issuing. With a few exceptions, ABE is based on bilinear pairings, which are quite computationally expensive. The feasibility of ABE on constrained devices such as Smartphones, powerful IoT nodes (e.g. Raspberry Pi) and small sensor nodes is disputed in literature [1]–[5].

The goal of this thesis is to evaluate the feasibility of an ABE library for a small 32-bit MCU. More specifically, the target system is an nRF52840 SoC with a 64MHz ARM

Cortex M4 CPU and 256KB of RAM. To this end, such a library is implemented using the Rust programming language and the performance of two ABE schemes on the SoC is evaluated.

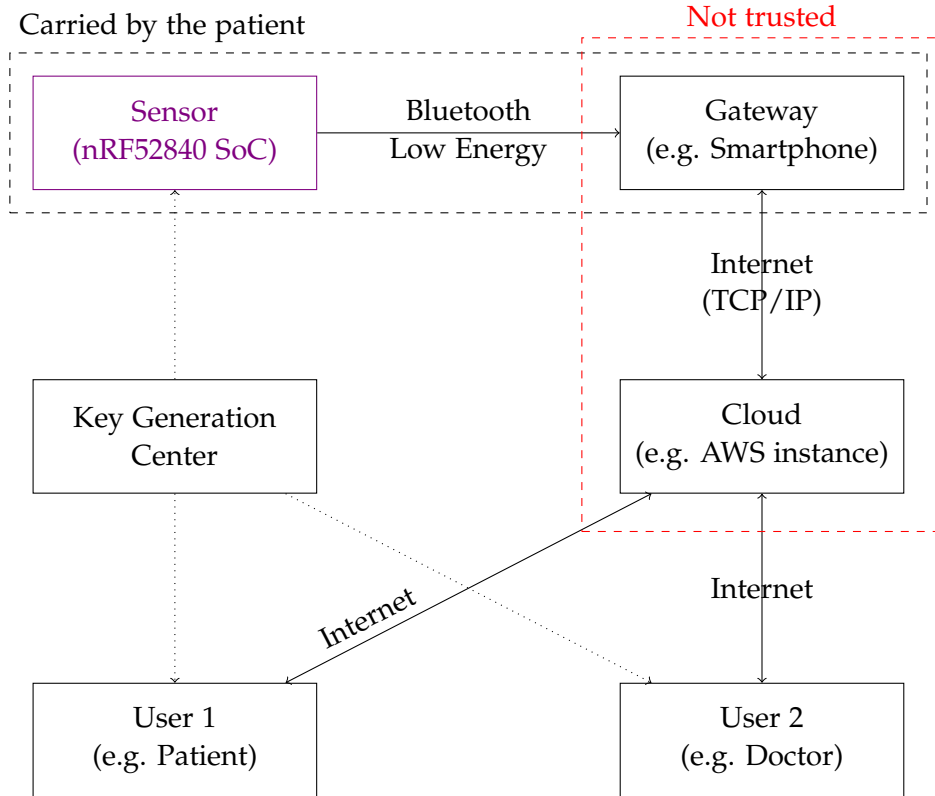


Figure 1.1: Simplified use case for end-to-end Attribute Based Encryption with encryption on a constrained sensor MCU. The ABE library developed for this thesis runs on the sensor.

For an example use case of such a library, see Figure 1.1. The patient receives a medical sensor from their doctor (e.g. embedded in a T-shirt). Because the sensor must have a small footprint and run on battery for an extended time period, it is not able to communicate via the internet directly. Instead, the user installs an app on their smartphone which acts as a gateway. This app receives the data from the sensor via bluetooth low energy (BLE) and uses the phone's internet connection to upload it to the cloud. Authorized users (e.g. the patient themselves or their doctor) can then download the data from the cloud.

Since the collected data in this system (e.g. the ECG) is related to the patient's health,

it must be protected with particular care: For example, the GDPR lists health data as a *special category* of data that enjoys extra protections compared to regular personal data. This is why simple transport encryption (e.g. by BLE between the sensor and gateway and TLS between the gateway and cloud) is not sufficient: It does not protect the data from a malicious gateway or cloud provider. Given the strong security requirements regarding medical data, we require end-to-end encryption, i.e. the data is encrypted at the point of origin (the sensor) and decrypted only locally when accessed by an authorized user (e.g. on the attending doctor's computer). The data is encrypted during transmission (both via BLE and over the internet) and storage on the cloud server. Neither the gateway nor the cloud provider need to be trusted. To achieve this, the encryption must be performed on the constrained MCU running the sensor.

In this use case, the fine-granular access control enabled by ABE is particularly interesting: If the system were using a regular symmetric encryption scheme, when seeing a new doctor, the patient would have to securely share with them their private key. This would allow the new doctor to see all the patient's medical information (assuming there is only one key) and provide no access control.

With a regular asymmetric encryption scheme, securely sharing the key would no longer be a problem. However, the system would have to know the public keys of all present and future doctors when encrypting the data. Again, all data encrypted with a doctor's public key will be visible to them; there are no further access control mechanisms.

Through the mechanisms more closely described in Section 2.2, KP-ABE allows much more fine-grained access control: At the time of encryption, data is described using certain attributes. The participants are then issued keys by the Key Generation Center (KGC), which allow them to decrypt a piece of data if and only if their key matches the attributes specified during encryption.

In this use case, only the encryption operation needs to be performed on the constrained MCU. As it will turn out, decryption requires much higher performance than encryption. Thus, implementing only encryption on the MCU is probably sufficient for many use cases: Oftentimes, devices that need to receive (i.e. decrypt) data are connected to some kind of actuator or user interface. In most networks, there are fewer of these nodes than there are sensor nodes, and thus these tend to be more powerful.

As such, I will be evaluating both encryption and decryption, but focus on the former. The thesis is considered a success if decryption can be performed reasonably quickly on the MCU, even if decryption is much more constrained.

2 Preliminaries

This chapter introduces Attribute-Based Encryption and the relevant mathematical background for implementing it.

2.1 Confidentiality with Classic Symmetric and Asymmetric Cryptography

Today's conventional cryptography knows two main classes of cryptosystems: symmetric encryption schemes and asymmetric encryption schemes. See Figure 2.1 for an illustration of the differences.

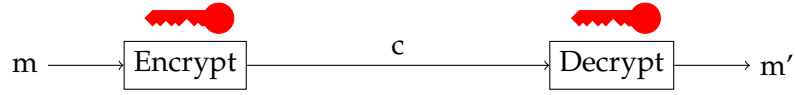
Consider n participants wanting to communicate securely (i.e. no user can read encrypted messages between two other users). Using a symmetric encryption scheme, each participant would need to agree on a unique key with every other participant, resulting in a total number of $\frac{n(n-1)}{2}$ keys. Using a asymmetric encryption scheme reduces the number of keys to only n , because each participant could obtain everyone else's public key and then send messages to them securely.

Another problem remains, however: Encrypting a single message to a large number of participants requires encrypting it with everyone's public key separately. For a large number of recipients, this is inefficient. So, for example, to encrypt a message for all students of a certain university, we'd need to obtain each student's public key and encrypt the message with each key separately.

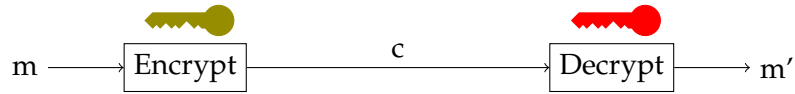
Even worse, what if we want to encrypt data for any student of said university, even if they *haven't joined the university yet*. In this case, our only option using classic asymmetric cryptography would be to have some trusted instance keep a plaintext copy and re-encrypt the data for any new student when they join the university. Attribute-Based encryption solves this problem.

2.2 Attribute-Based Encryption

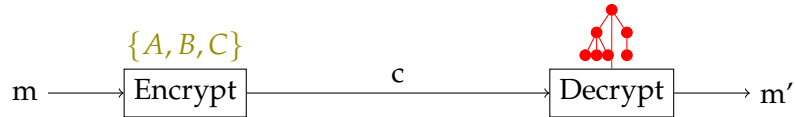
Attribute-Based Encryption (ABE) uses a combination of attributes to define a *group* of private keys that should be able to read encrypted data, instead of encrypting it for one



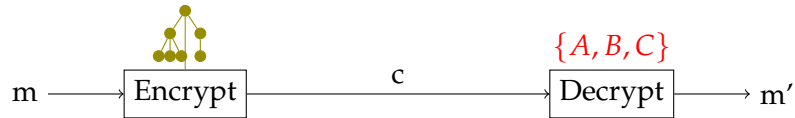
(a) Symmetric Encryption: Both keys are identical.



(b) Asymmetric Encryption: Different keys for encryption and decryption.



(c) Key-Policy Attribute-Based Encryption: Attributes for encryption, access structure for decryption. Note that the access structure of the decryption key may be public; the actual key consists of secret shares that allow the user to decrypt a ciphertext if the correct attributes are present.



(d) Ciphertext-Policy Attribute-Based Encryption: Access policy for encryption, attributes for decryption. Note that the attributes associated with the decryption key may be public; the actual key consists of corresponding secret shares.

Figure 2.1: Keys used for encryption and decryption in different classes of encryption schemes. Red information has to be kept secret, green information may be made publicly available. For the differences between the two types of ABE, see Section 2.2.3.

specific private key only (as in asymmetric encryption schemes). In Figure 2.1d, this is represented by a tree.

The combination of attributes may be as restrictive or permissive as needed. It is possible to create ciphertexts that can be read by almost all members of an ABE scheme, and ciphertexts that can be read by nobody except a few selected participants.

Figure 2.2 shows a small ABE system with the KGC initializing the system and issuing keys, and two users sharing an encrypted message.

2.2.1 Attributes and the Key Generation Center

In essence, attributes are strings describing certain characteristics or features of actors and objects. For example, a typical freshman student of informatics at TUM could be described by the attributes "semester count 1", "computer science", "tum", "is young", "started degree in 2017".

These attributes themselves don't contain any information to which users or object they apply; instead this is a matter of interpretation. Some attributes may be very clearly defined, e.g. "started degree in 2017" from above. For others, it may be more difficult to decide whether they apply, e.g. the attribute "is young": Until what age is a student young?

In any instance of ABE, there needs to exist an arbiter who decides whether an attribute applies to a certain user or object. This role is assumed by a trusted third party, the Key Generation Center (KGC). It has two main responsibilities: First, the KGC decides which attribute applies to which user. Second, it issues private keys corresponding to these attributes, and hands these to the users.

Without this KGC, there is no ABE. This differs from traditional public-key encryption schemes, where any user can independently create their own keypair.

Regarding the set of possible attributes (called the *attribute universe*), there are two possibilities: In a large universe construction, all possible strings can be used as attributes [6]. In a small universe construction, the universe of attributes is explicitly fixed when the system is instantiated, i.e. when the KGC runs the *Setup* algorithm (see below, section 2.2.2) [6]. With a small universe construction, the size of the public parameters usually grows with the size of the attribute universe [6].

2.2.2 Formal definition of an ABE Scheme

We will define a KP-ABE scheme here, for the difference between CP-ABE and KP-ABE and formal definitions of Access Trees, see the next sections.

Definition 2.1. A (Key-Policy) Attribute-Based Encryption scheme consists of the following four algorithms: [6]

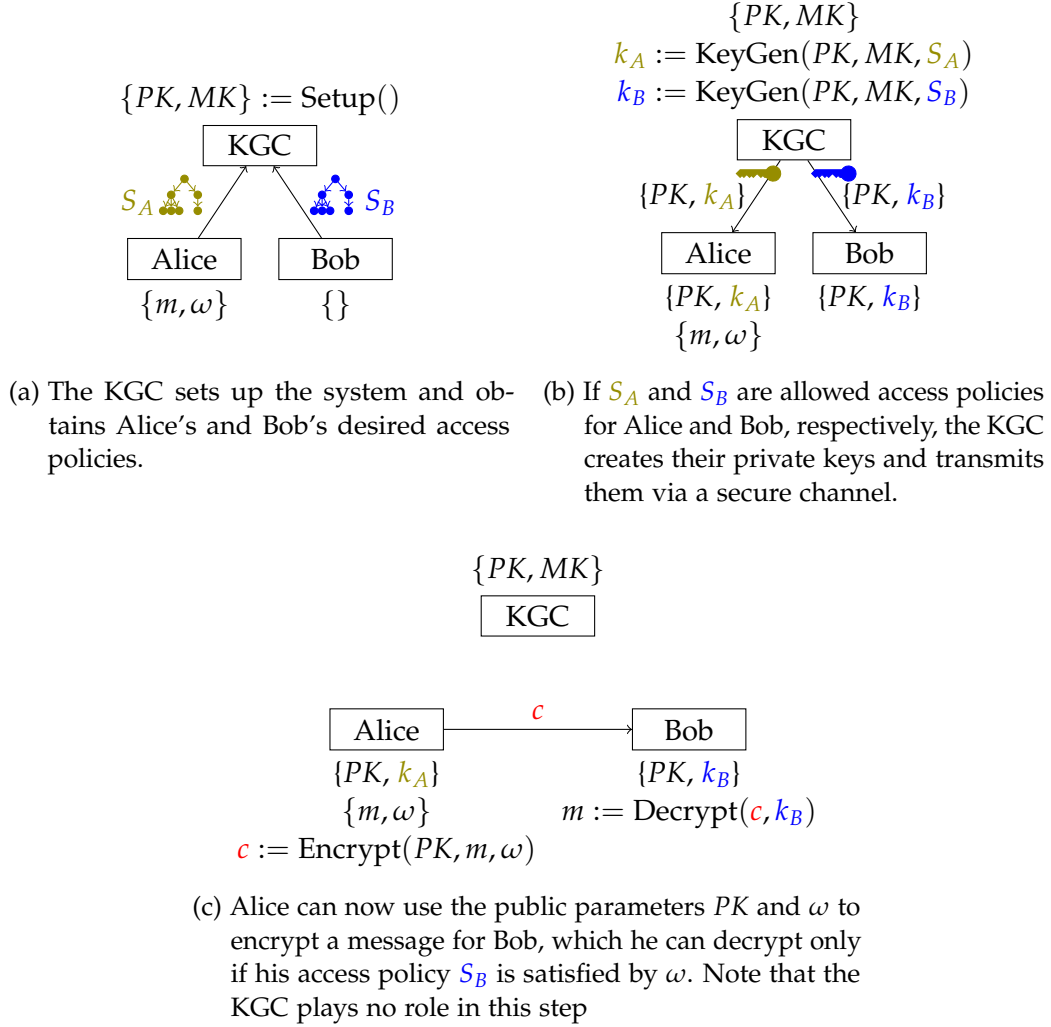


Figure 2.2: Alice wants to send an KP-ABE encrypted message m to Bob. She wants to encrypt the message under a set of attributes ω . Both Alice and Bob create a desired access policy, S_A and S_B , respectively. Note that the KGC will only issue a corresponding key if it deems that they should be allowed to obtain a key under the given access policy.

- *Setup*. Run once by the Key Generation Center (KGC). Sets up the system by generating public parameters PK and a private master key MK . The public parameters are shared with all participants, while the master key remains only known to the KGC.
- *KeyGen*(PK, s, S). Input: public parameters PK , master secret s and access structure S .
Run by the trusted authority once for each user to generate their private key. Returns a private key k corresponding to S .
- *Encrypt*(PK, m, ω). Input: public parameters PK , plaintext message m and set of attributes ω .
Run by any participant of the system. Encrypts m under ω and returns the ciphertext c .
- *Decrypt*(c, k). Input: ciphertext c (output of *Encrypt*) and key k (output of *KeyGen*).
Run by any participant holding a private key generated by *KeyGen*. Outputs the correctly decrypted message m' if and only if the set of attributes under which m was encrypted satisfies the access structure under which k was created.

The definition of a CP-ABE scheme is identical, except that *Encrypt*(PK, m, S) takes an access structure S and *KeyGen*(PK, s, ω) takes a set of attributes.

How exactly these algorithms work in concrete ABE schemes will be discussed in Chapter 4.

2.2.3 KP-ABE and CP-ABE

Two components are necessary to specify a group of keys that shall be able to decrypt a ciphertext: A number of attributes that are present, and a policy that defines a combination of required attributes. Each of these can either be associated with the ciphertext, or with the decryption key:

In Ciphertext-Policy ABE (CP-ABE), the key is associated with a set of attributes and the ciphertext is encrypted under an access policy. Key-Policy ABE (KP-ABE) works the other way around, so the ciphertext is associated with a set of attributes, and the key is associated with an access policy. See Figure 2.3 for illustration.

In both cases, a ciphertext can be decrypted if and only if the set of attributes specified in one part satisfy the access policy associated with the other part.

CP-ABE tends to be more intuitive because, when encrypting a plaintext, the encryptor controls rather explicitly who can decrypt their ciphertext: They set the access policy that defines which combinations of attributes are required from the users to successfully decrypt the ciphertext [7]. An example use case for CP-ABE in a hospital setting would



Figure 2.3: CP-ABE vs. KP-ABE: Association of key and ciphertext with Access Policy and set of attributes.

be sending an encrypted note about problems with a specific treatment to all doctors, patients that received that treatment and nurses of the department that administered the treatment. This could be specified by an access policy as (hospital-name AND (doctor OR (patient AND received-treatment-x) OR (nurse AND department-y))).

With KP-ABE, on the other hand, the encryptor doesn't have direct control over who will be able to access the data, except for the choice of attributes under which they encrypt the plaintext [7]. In the hospital setting from above, KP-ABE could be employed in a different use case: For encrypted storage of a patient's medical record, the patient's name could be used as an attribute in KP-ABE. If the patient sees a new doctor, they could simply have their key policy extended to include the patient's attribute. With CP-ABE, seeing a new doctor would require re-encrypting the entire data under a new access policy.

With KP-ABE, instead of the encryptor, the Key Generation Center must be trusted with intelligently deciding which key to give to the decrypting party [7]. This property can be desirable: Consider a constrained IoT device as an encryptor, which can reliably transmit data, but not receive. If a new doctor must be given access to a patient's data, CP-ABE would require updating the policy on this device. With KP-ABE, policies are handled by the KGC, which is in general much more powerful and better-connected than encryptors might be.

2.2.4 Access Structures

Access structures formally determine which sets of attributes are required to reconstruct the ciphertext under ABE. This definition is adapted from [8] for our setting of allowed attribute sets, instead of allowed parties.

Definition 2.2. Access Structure [8].

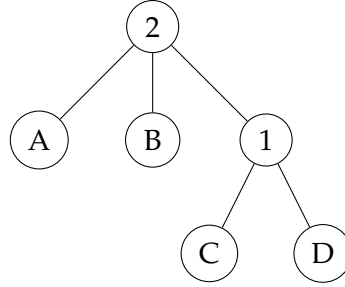


Figure 2.4: Sample Access Tree over the attributes A, B, C, D.

Let $U = \{A_1, \dots, A_n\}$ be the universe of attributes. A set $\mathcal{A} \subseteq 2^U$ is monotone if for all $B \in \mathcal{A}$ and $C \supseteq B$, $C \in \mathcal{A}$. An access structure \mathcal{A} is a non-empty subset of 2^U , i.e. $\mathcal{A} \in 2^U \setminus \{\emptyset\}$. A monotone access structure is an access structure that is monotone. The sets in \mathcal{A} are called the *authorized sets*, those not in \mathcal{A} are called the *unauthorized sets*.

Intuitively, the monotonicity of an access structure means that adding an attribute to an authorized set cannot result in an unauthorized set.

2.2.5 Access Trees

Explicitly specifying an access structure is not feasible, as its size may be exponential in the size of the attribute universe. Therefore, we will use the construction of *Access Trees* from Goyal et al. in [6]. Each leaf of this tree is labelled with an attribute, and each interior node is labelled with an integer, the threshold for it to be satisfied [6].

Figure 2.4 illustrates an example for an Access Tree. It is satisfied by any set of attributes that contains two of A, B and either C or D . That is, $\{A, B\}$ would satisfy the tree, just as $\{B, D\}$ would, but $\{C, D\}$ would not be sufficient. For an attribute universe of $U = \{A, B, C, D\}$ this would realize the access structure $\mathcal{A} = \{\{A, B, C, D\}, \{A, B, C\}, \{A, B, D\}, \{A, C, D\}, \{B, C, D\}, \{A, B\}, \{A, C\}, \{A, D\}, \{B, C\}, \{B, D\}\}$

Definition 2.3. Access Tree [6].

An internal node x of an access tree is defined by its children and a threshold value d_x . If x has num_x children, then its threshold value satisfies $0 < d_x \leq num_x$.

A leaf node x is defined by an attribute and a threshold value $k_x = 1$.

[6] also defines the following functions for working with access trees: The parent of a node x in the access tree is denoted by $\text{parent}(x)$. If x is a leaf node, $\text{att}(x)$ denotes the attribute associated with x ; otherwise it is undefined. The children of a node x are numbered from 1 to num_x . Then $\text{index}(y)$ denotes the unique index of y among the children of its parent node.

Definition 2.4. Satisfying Access Trees [6].

Let \mathcal{T} be an access tree with root r and \mathcal{T}_x the subtree with x as its root. If a set of attributes γ satisfies the access tree \mathcal{T}_x , we write $\mathcal{T}_x(\gamma) = 1$; otherwise $\mathcal{T}_x(\gamma) = 0$.

If x is a leaf node, then $\mathcal{T}_x(\gamma) = 1$ if and only if $\text{attr}(x) \in \gamma$.

If x is an internal node, then $\mathcal{T}_x = 1$ if and only if d_x or more of the children x' of x return $\mathcal{T}_{x'}(\gamma) = 1$.

The set of attribute sets that satisfy a tree \mathcal{T} is then the access structure it represents: $\mathcal{A} = \{\gamma \in 2^U \mid \mathcal{T}(\gamma) = 1\}$. Note that \mathcal{A} has to be monotone. It is not possible to specify that the *absence* of an attribute in the tree.

Using the threshold-gate construction, we can express $A \text{ AND } B$ as a node with two children A and B and threshold 2, and express $A \text{ OR } B$ as a node with two children A and B and threshold 1 [9].

2.3 Shamir's Secret Sharing

This secret sharing scheme based on polynomial interpolation was first introduced by Adi Shamir in 1979 [10]. It allows a secret s , which is generally just a number, to be shared among a number of n participants. The shares are computed such that s can be reconstructed if, and only if, at least k participants meet and combine their shares. Such a scheme is then called a (k, n) -threshold scheme. [10]

2.3.1 Lagrange interpolation

Shamir's scheme makes use of a property of polynomials: A polynomial of degree d is unambiguously determined by $d + 1$ points (x_i, y_i) . In other words, any polynomial of degree d can be unambiguously interpolated (reconstructed) from $d + 1$ distinct points.

To interpolate a polynomial of degree d from $d + 1$ given points $(x_1, y_1), \dots, (x_{d+1}, y_{d+1})$, we can make use of the lagrange basis polynomials: [9]

Definition 2.5. Lagrange interpolation: Given a set of $d + 1$ points $(x_1, y_1), \dots, (x_{d+1}, y_{d+1})$.

Then the polynomial

$$L(x) = \sum_{k=0}^d \Delta_{\omega, x_k}(x) \cdot y_k \quad (2.1)$$

is the lagrange interpolation polynomial for that set of points, where $\omega = \{x_1, \dots, x_{d+1}\}$ and $\Delta_{\omega, k}(x)$ are the Lagrange basis polynomials:

$$\Delta_{\omega, k}(x) = \prod_{\substack{i \in \omega \\ i \neq k}} \frac{x - i}{k - i} \quad (2.2)$$



Figure 2.5: Example for a $(5, 4)$ -threshold scheme with $s = 8$ and $p(x) = 8 + 7x - 6x^2 + x^3$. The five green-colored points are distributed as the secret shares. As $p(x)$ has degree three, at least four shares are required to reconstruct s .

This polynomial has degree d . If the points (x_i, y_i) lie on a d -degree polynomial, then the lagrange interpolation $L(x)$ is *exactly* that polynomial.

On the other hand, if there are less than $d + 1$ points of a d -degree polynomial known, there are infinitely many d -degree polynomials that pass through all given points. [10]

2.3.2 Secret sharing with polynomials

To share our secret, we now hide it in a polynomial and give out points on this polynomial as secret shares. Using the lagrange basis polynomials, we can then reconstruct $p(x)$ and thus the secret if we know enough shares [10].

Definition 2.6. Shamir's (k, n) -threshold secret sharing scheme [10]. To share a secret s among n participants such that s can be recovered if and only if k or more shares are combined, do:

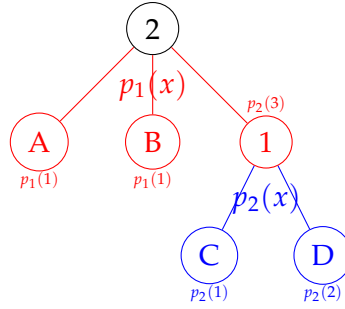


Figure 2.6: Access Tree from Figure 2.4 showing how Shamir's Secret Sharing is employed recursively. $p_1(x)$ is a the polynomial of a $(2, 3)$ -threshold scheme, $p_2(x)$ of a $(1, 2)$ -threshold scheme. Shown in small are the secret shares embedded into each node.

1. Pick coefficients a_1, \dots, a_{k-1} at random
2. Set $a_0 = s$. This results in the polynomial $p(x) = a_0 + a_1x + \dots + a_{k-1}x^{k-1}$. Note that $p(0) = s$.
3. The secret shares are $(1, p(1)), (2, p(2)), \dots, (n, p(n))$. Give one to each participant.

To reconstruct the secret from any subset of k shares, interpolate the polynomial $p(x)$ and evaluate $p(0) = s$.

See also Figure 2.5 for illustration. In practice, the numbers would be far bigger and calculations wouldn't be performed over the real numbers, but rather a finite field modulo a prime [10].

2.3.3 Secret Sharing in Attribute Based Encryption

To realize an Access Tree that „gives away“ a secret if and only if it is satisfied by a set of attributes, we can recursively use Shamir's Secret Sharing scheme:

We use a secret-sharing polynomial on each internal node of the Access Tree: For a node x with threshold d_x and num_x children, we define a (d_x, num_x) -threshold scheme and embed one share of the secret in each child. Begin in the root, and set s as the secret we want to embed in the tree. For all other nodes, set s as the secret share received from the parent node.

If the child is a leaf, we modify the share such that it can only be used if the relevant attribute is present (how exactly this is done differs between CP-ABE and KP-ABE).

Now, let ω be a set of attributes. We have built our tree in such a way that the share embedded in a leaf node u can be used only if $\text{attr}(u) \in \omega$. That means, a leaf node's secret share can be used if and only if the set of attributes satisfies this leaf node.

For the internal nodes x , the use of a (d_x, num_x) -threshold scheme ensures that the secret embedded in x can be reconstructed if and only if the secret shares of at least d_x child nodes can be used, i.e. at least d_x child nodes are satisfied. Following this recursive definition up to the root, we can see that our secret s embedded in the root can be reconstructed exactly if ω satisfies the Access Tree.

See Figure 2.6 for an illustration with the tree from Figure 2.4: Two (k, n) -threshold schemes are employed, one for each internal node of the access tree. $p_1(x)$ is the polynomial of the root's $(2, 3)$ -threshold scheme, sharing s , the secret to be embedded in the tree (i.e. $p_1(0) = s$). $p_2(x)$ is the polynomial for the $(1, 2)$ -threshold scheme belonging to the node labelled "1" and shares the value $p_1(3)$ that it received from the $(2, 3)$ -threshold scheme of the layer above (i.e. $p_2(0) = p_1(3)$).

2.3.4 Revocation

So far, it is not possible to take away privileges from a user: Once the private key has been issued, it can not be taken back. A user's capabilities can only be extended (e.g. by issuing a key with additional attributes or with a more permissive policy). This is an problem, e.g. if their private key is compromised [11].

The simplest approach is to simply renew the keys of valid users from time to time [11]. When a user is revoked, their key will not be updated any longer. Thus any ciphertexts encrypted after the next key update will not be readable for the revoked user [11].

This approach requires the KGC to update or re-issue one key per valid user and requires a secure channel to the KGC [11].

Attrapadung and Imai [12] differentiate between *direct* and *indirect revocation*: With direct revocation, the list of revoked users is directly specified by the encrypting party (i.e. the encryption takes a "black list" of revoked users). Indirect revocation achieves revocation by means of updating the keys of valid users, as described in the naive approach above.

Direct revocation requires the encryptor to know the list of revoked users [12] (i.e. the encrypting party is responsible for correct revocation of the users on the revocation list). This can be a major drawback, especially in large systems or when encryptors are severely constrained (as in our case, Internet of Things (IoT) devices). With indirect revocation, the encryptor does not need to do anything except use the most recent version of the public parameters [12].

The advantage of direct revocation, however, is that it works instantly: As soon as

an encryptor knows about the revocation of a user, they will include them on their revocation list for future encryptions. With indirect revocation, a revoked user can decrypt all ciphertexts created until the next key update is distributed by the KGC [12].

In either case, ciphertext encrypted before a user's revocation becomes effective, remains readable using the revoked key. Some schemes include a proxy-reencryption mechanism that allows an untrusted third party to update ciphertexts such that they cannot be decrypted by revoked users [13].

2.4 Elliptic Curves

The mathematics of modern cryptosystems (including, but not limited to ABE) work any group that satisfies the axioms (see below), and elliptic curves are just one of them. Because Elliptic Curves allow for shorter key lengths than, e.g. groups modulo a prime, they have become very popular for use in cryptography. Exact definitions and notations differ, these are taken from the textbook *Introduction to Modern Cryptography* by Katz and Lindell [14].

2.4.1 Group Axioms

Definition 2.7. [14]. A *Group* consists of a set \mathbb{G} together with a binary operation \circ for which these four conditions hold:

- Closure: For all $g, h \in \mathbb{G}$, $g \circ h \in \mathbb{G}$.
- Existence of identity: There is an element $e \in \mathbb{G}$, called the *identity*, such that for all $g \in \mathbb{G}$, $g \circ e = g = e \circ g$.
- Existence of inverse: For every $g \in \mathbb{G}$ there exists an *inverse* element $h \in \mathbb{G}$ such that $g \circ h = e = h \circ g$.
- Associativity: For all $g_1, g_2, g_3 \in \mathbb{G}$, $(g_1 \circ g_2) \circ g_3 = g_1 \circ (g_2 \circ g_3)$.

When \mathbb{G} has a finite number of elements, the group \mathbb{G} is called finite and $|\mathbb{G}|$ denotes the order of the group.

A group \mathbb{G} with operation \circ is called *abelian* or commutative if, in addition, the following holds:

- Commutativity: For all $g, h \in \mathbb{G}$, $g \circ h = h \circ g$.

When the binary operation is clear from context, we simply use \mathbb{G} to denote the group.

We also define *Group Exponentiation*: $g \in \mathbb{G}, m \in \mathbb{N}^+$, then $mg = \underbrace{g \circ \dots \circ g}_{m \text{ times}}$.

Usually, the symbol used to denote the group operation is not the \circ from above, but either $+$ or \cdot . These are called *additive* and *multiplicative* notation, respectively. It is important to remember, though, that the group operation might be defined completely differently!

In multiplicative notation, the group exponentiation of $g \in \mathbb{G}$ with $m \in \mathbb{N}^+$ is written as g^m , in additive groups it is written as $m \cdot g$.

2.4.2 Elliptic Curves

Definition 2.8. Given a prime $p \geq 5$ and $a, b \in \mathbb{Z}_p$ with $4a^2 + 27b^2 \not\equiv 0 \pmod{p}$, the Elliptic Curve over \mathbb{Z}_p is: [14]

$$E(\mathbb{Z}_p) := \{(x, y) \mid x, y \in \mathbb{Z}_p \text{ and } y^2 = x^3 + ax + b \pmod{p}\} \cup \{\mathcal{O}\} \quad (2.3)$$

a and b are called the curve parameters, and the requirement that $4a^2 + 27b^2 \not\equiv 0 \pmod{p}$ makes sure that the curve has no repeated roots [14]. The curve is simply the set of points $(x, y) \in \mathbb{Z}_p \times \mathbb{Z}_p$ that satisfy the curve equation $y^2 = x^3 + ax + b \pmod{p}$. One special point is added, the *point at infinity* denoted by \mathcal{O} . This will help define the point addition as a group operation in the next paragraph. [14]

2.4.3 Point Addition

Now, it is possible to show that every line intersecting a curve $E(\mathbb{Z}_p)$ intersects it in exactly three points, if you (1) count tangential intersections double and (2) count any vertical line as intersecting the curve in the point at infinity \mathcal{O} [14]. Therefore, \mathcal{O} can be thought of as sitting “above” the end of the y-axis [14]. Figure 2.7 shows all four different combinations, feel free to convince yourself that this statement indeed makes sense for the plotted curve.

Using this intersecting line, we can define an operation on curve points:

Definition 2.9. Given an Elliptic Curve $E(\mathbb{Z}_p)$, we define a binary operation called (*point*) *addition* and denoted by $+$: [14]

Let $P_1, P_2 \in E(\mathbb{Z}_p)$.

- For two points $P_1, P_2 \neq \mathcal{O}$ and $P_1 \neq P_2$, their sum $P_1 + P_2$ is evaluated by drawing the line through P_1 and P_2 . This line will intersect the curve in a third point, $P_3 = (x_3, y_3)$. Then the result of the addition is $P_1 + P_2 = (x_3, -y_3)$, i.e. P_3 is reflected in the x-axis (Figure 2.7-1). If $P_3 = \mathcal{O}$, then the result of the addition is \mathcal{O} (Figure 2.7-3).
- If $P_1, P_2 \neq \mathcal{O}$ and $P_1 = P_2$, as above but draw the line as tangent on the curve in P_1 (Figure 2.7-2 and -4).

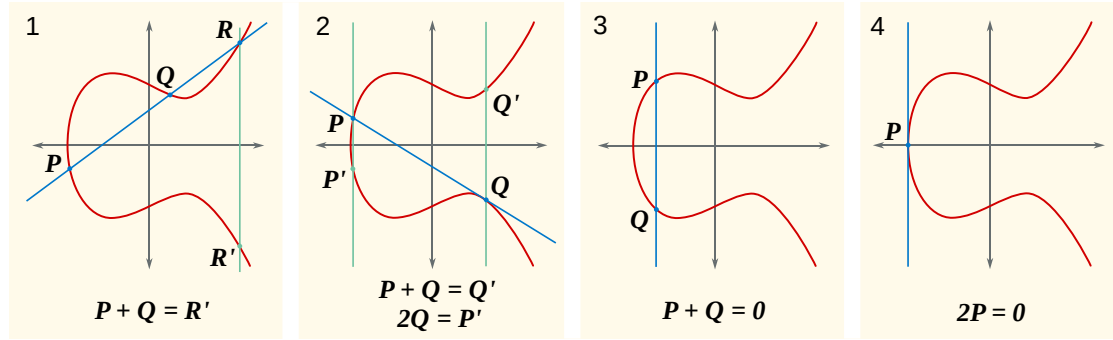


Figure 2.7: Elliptic Curve point addition

(Image by SuperManu, licensed under Creative Commons.)

- If $P_1 = \mathcal{O}$, then $P_1 + P_2 = P_2$ and vice-versa.

We will be adding points to themselves a lot. Therefore, we define for ease of notation:

Definition 2.10. Point-Scalar multiplication: Given a point $P \in E(\mathbb{Z}_p)$ and a scalar $d \in \mathbb{N}$:

$$d \cdot P = \underbrace{P + P + \cdots + P}_{d \text{ times}} \quad (2.4)$$

That is exactly the definition of group exponentiation, applied to our additive Elliptic Curve group. Note that the product of a scalar with a point is again a point on our curve.

2.4.4 Groups on Elliptic Curves

Theorem 2.1. The points of an Elliptic Curve $E(\mathbb{Z}_p)$ plus the addition law as stated in Definition 2.9 forms an abelian (commutative group) [14], [15]:

Proof. A formal proof is outside the scope of this thesis, but here's some informal reasoning about the group axioms:

- Existence of Identity: $P + \mathcal{O} = P$ (as per definition)
- Commutativity: For all $P_1, P_2 \in E(\mathbb{Z}_p)$, $P_1 + P_2 = P_2 + P_1$ (obvious, because the line through P_1 and P_2 will be the same)
- Unique inverse: For any point $P = (x, y) \in E(\mathbb{Z}_p)$, the unique inverse is $-P = (x, -y)$ (obvious).

- Associativity: For all $P_1, P_2, P_3 \in E(\mathbb{Z}_p)$, $(P_1 + P_2) + P_3 = P_1 + (P_2 + P_3)$ (much less obvious, see e.g. [15, Chapter 2.4] for a proof).

□

Of particular interest to cryptography are *cyclic* groups on elliptic curves:

Definition 2.11. A (multiplicative) group G is cyclic if there is an element $g \in G$ that generates G , i.e. $G = \langle g \rangle = \{g^n \mid n \in \mathbb{Z}\}$.

Translated to our (additive) groups on elliptic curves, this means that there is a generator point $P \in E(\mathbb{Z}_p)$, such that every point $Q \in E(\mathbb{Z}_p)$ can be written as $Q = nP$ with some $n \in \mathbb{N}$.

Theorem 2.2. [14] Let G be a finite group of order n , i.e. $|G| = n$. Let $g \in G$ be an element of G with order k , i.e. $k = |\langle g \rangle|$

Then $k \mid n$, i.e. the order of g divides the group order n .

Proof. See [14, Proposition 8.54].

□

There is an important consequence to this fact: If a group has prime order, all points except the identity are generators. This stems from the fact that a prime number has exactly two divisors: One (the order of the identity) and itself (the order of all other points).

This follows from the fact that for any point $P \in E(\mathbb{Z}_p)$, its order $\text{ord}(P) = |\langle P \rangle|$ must divide the group order. A prime has exactly two divisors: One (the order of \mathcal{O}) and itself (the order of all other points).

Again, translated to Elliptic Curves this means that if the number of points $\#E(\mathbb{Z}_p)$ on a curve is prime, all points except \mathcal{O} are generators. These cyclic elliptic curve groups (or, cyclic subgroups of non-cyclic elliptic curves) are exactly the groups we are interested in for doing actual cryptography. For a detailed description why, see [14, p. 321].

2.4.5 Bilinear Pairings

Definition 2.12. Bilinear pairing [16].

Let G_1 and G_2 denote cyclic groups with prime order n . Let G_T be another cyclic group of the same order n . G_1 and G_2 are written additively, G_T is written using multiplicative notation.

A *bilinear pairing* then is a function $e : G_1 \times G_2 \rightarrow G_T$ with the following properties:

- *Bilinearity.* For all $P_1, P_2 \in G_1, Q_1, Q_2 \in G_2$

- $e(P_1 + P_2, Q_1) = e(P_1, Q_1) \cdot e(P_2, Q_1)$
- $e(P_1, Q_1 + Q_2) = e(P_1, Q_1) \cdot e(P_1, Q_2)$
- *Non-Degeneracy.*
 - for each $P \in \mathbb{G}_1, P \neq 0$ there is a $Q \in \mathbb{G}_2$ with $e(P, Q) \neq 1$
 - for each $Q \in \mathbb{G}_2, Q \neq 0$ there is a $P \in \mathbb{G}_1$ with $e(P, Q) \neq 1$
- *Computability.* There is an algorithm that computes e efficiently.

If $\mathbb{G}_1 = \mathbb{G}_2$, the pairing is called a *symmetric pairing*, otherwise it is an *asymmetric pairing*.

There are a few different concrete pairing functions, e.g. the Weil pairing, Tate pairing and the Ate pairing [16]. Usually the source groups \mathbb{G}_1 and \mathbb{G}_2 are subgroups of certain elliptic curves [16] and the target \mathbb{G}_T is a finite field (*not* another point on a curve) [17].

3 Related Work

3.1 Theoretical work on ABE schemes

Attribute-Based Encryption was introduced by Sahai and Waters in 2005 [18]. They proposed a new type of identity-based encryption where identities are a set of attributes. Their so-called *fuzzy* identity-based encryption scheme allows a user to decrypt a ciphertext even if their identity doesn't exactly match the identity specified at the time of encryption [18]. Instead, an overlap larger than some threshold value between the attributes in the ciphertext's identity with the attributes of the key's identity is sufficient [18]. This property is realized by means of a (k, n) -threshold secret sharing scheme.

Sahai and Water's construction can already be seen as an ABE scheme with very limited expressiveness, i.e. it only works with "k-out-of-n" access structures [6].

In 2006, Goyal, Pandey, Sahai and Waters [6] extended this into the first expressive KP-ABE scheme using the access tree construction described in Chapter 2. Their main construction uses access trees and a small attribute universe, but they also give constructions with a large attribute universe and for linear secret sharing scheme (LSSS) access structures, respectively.

The first expressive CP-ABE scheme was proposed by Bethencourt, Sahai and Waters in [7]. It is also a large-universe construction and uses access trees. Waters [19] later also gives the first CP-ABE schemes with a security proof in the standard model, not only in the generic group model (the distinction is not relevant for this thesis).

Both the schemes in [6] and in [7] only support monotonic access structures.

In [6], an inefficient realization of general (non-monotonic) access structures is proposed, which is to simply represent the absence of an attribute as a separate attribute. This is inefficient because it doubles the total number of attributes in the system [6]. Non-monotonic access structures over a universe of n attributes are represented by monotonic access structures over a universe of $2n$ attributes. It also requires every ciphertext to be associated with n attributes (i.e. either with their positive or negated of a corresponding attribute). Note that the size of ciphertexts or keys is linear in the number of attributes in all expressive ABE schemes.

The first efficient construction for non-monotonic access structures was given in [20]. However, this construction leads to large private keys. More specifically, the size

is $\mathcal{O}(t \log(n))$, where t is the number of leaf nodes in the key's access tree and n a system-wide bound on the number of attributes a ciphertext may have [21].

In [21] direct revocation is related to the realization non-monotone access structures and a scheme with efficient direct revocation is presented. The authors also present an efficient construction for non-monotone access structures with keys of size $\mathcal{O}(t)$ [21], where t is again the number of leaf nodes in the key's access tree.

The difference between direct and indirect revocation is introduced in [12], and a *Hybrid Revocable* ABE scheme is given. It allows the encryptor to choose the revocation mode separately for every message [12].

All of these schemes are built using a bilinear pairing as introduced in Section 2.4.5. A pairing-free KP-ABE scheme was proposed by Yao, Chen and Tian [9] in 2015. Their scheme only uses a single group and no bilinear pairing. Instead of encrypting a group element that encodes a message, their scheme yields a random group element which is then used as a key for a symmetric encryption algorithm [9].

In [22] a cryptanalysis of the scheme in [9] is performed. It is shown that the scheme is not secure, but the authors propose an effective fix and prove its security. They also extend the scheme to allow for key delegation (i.e. a hierarchical KP-ABE scheme) [22].

[23] presents a pairing-free ABE scheme with indirect revocation. It is an adaptation of the schemes in [9], [22], see also [24].

All three of these schemes were attacked by Herranz in [24] (one attack for all three schemes is given, as they are very similar). [24] argues that it is not possible to build secure ABE schemes in the (non-bilinear) discrete-logarithm setting (i.e. on elliptic curves without bilinear pairings). For this reason, the security of pairing-free schemes like [9], [22], [23] remains questionable, even if further improved.

3.2 Evaluation on unconstrained hardware

One of the major factors for the performance of the implementation of an ABE scheme is the underlying pairing computation (except for [9] and its derivatives, of course). Not only ABE is based on bilinear pairings, but a large variety of cryptographic schemes, e.g. a three-party Diffie-Hellman Key Exchange [25] or short digital signature schemes [26].

Therefore, a fast implementation of the bilinear pairing is vital. Comparing different pairing implementations is difficult because the performance greatly depends on the security level, the concrete pairing implemented, the choice of elliptic curves and of course the speed of the hardware and architecture used. Furthermore, many implementations are not portable due to hand-optimized assembly code or the use of architecture-specific instructions.

One of the first notable implementations was the *Pairing-Based Cryptography Library (PBC)*¹ [27]. The efficiency improvements implemented by the PBC library were first described by its author, Ben Lynn, in [27]. This implementation runs sufficiently fast on standard PC hardware, e.g. it takes 20.5ms to compute a pairing on a 224-bit MNT curve on a 2.4GHz Intel Core i5 processor [28].

Implementations of ABE on standard PC hardware are well-studied [7], [29], [30]; for an overview see [31].

In [28], a pairing-based ABE scheme is evaluated on a standard computer and an ARM-based smartphone (iPhone 4). On the smartphone, only decryption is implemented because encryption is not needed in their scenario. This implementation uses the PBC library and 224-bit MNT curve from [27]. They conclude that for policies with less than 30 leaves, decryption on a smartphone is feasible (taking around 2 to 7 seconds, depending on the scheme) [28].

In [32], a pairing library and ABE scheme is implemented using the NEON instructions, a set of SIMD vector instructions for ARM processors. They evaluate their implementations on several ARM Cortex A9 and A15 processors with clock frequencies between 1GHz and 1.7GHz. The use of NEON improves performance by 20-50%, depending on the chip. Note that the NEON instruction set is not available on our SoC.

In [1], CP- and KP-ABE are evaluated for different security levels on an Intel Atom-based smartphone using a Java implementation. They conclude that ABE on smartphones is not fast enough to be practical. This is subsequently challenged in [3], where a C implementation also using the PBC library from [27] is evaluated on another smartphone with a 1.2GHz ARM Cortex A9 CPU. This implementation is significantly faster than the one in [1] at comparable security levels. As such, the authors conclude that ABE is indeed feasible on smartphones.

3.3 Evaluation on constrained devices

Despite pairing computation being very computationally demanding, there exist libraries even for the smallest microcontrollers: For example, the *TinyPBC* library². It takes a minimum of 1.9s to compute a pairing on a 7MHz ATmega128L processor with optimized assembly code [33]. Their choice of elliptic curves, however, only provides a security level of 80 bit (the 224-bit MNT curve from the PBC library is closer to a 128-bit security level [28]).

¹<https://crypto.stanford.edu/pbc/>

²<https://sites.google.com/site/tinypbc/>

Scott [34] provides a fast implementation of the 254-bit BN curve (the same as used in `rabe-bn`) in the *MIRACL Core Cryptographic Library*³. They also evaluate their library on the same SoC as used in this thesis (nRF52840, 64MHz ARM Cortex M4 CPU) and compute a pairing of the 254-bit BN curve in 439ms [34, Table 4].

The authors of [3] also test their ABE implementation on IoT devices in [2]. They evaluate the performance of the same library on, among others, a Raspberry Pi Zero (1GHz ARM11 CPU) and conclude that ABE is feasible on these devices, too. However, they note that especially lower security levels are suitable and that the penalty for increasing the security level is very high (e.g. increasing the security level from 80 to 128 bits without increasing the encryption time requires reduction of the number of attributes by a factor of 10) [2].

The setting in [5] is much closer to ours: ABE is implemented bare-metal (i.e. without operating system) on a sensor equipped with an STM32L151VCT6 SoC with a maximum clock frequency of 32MHz. They use the pairing library *RELIC Toolkit*⁴ and evaluate a C implementation of the CP-ABE scheme in [19]. The author again concludes that ABE encryption on the sensor is feasible if the policy size is rather small and the runtime of several seconds is acceptable [5]. In this case, the encryption time is over 10s already for just six attributes [5].

[4] provides a similar analysis for the slightly faster ESP32 board (240MHz Xtensa LX6 processor). They also test the pairing-free YCT14 scheme [9] and evaluate the energy consumption of ABE operations.

³<https://github.com/miracl/core>

⁴<https://github.com/relic-toolkit/relic>

4 Constructions

This chapter will describe two ABE schemes in detail; those were implemented in this thesis. In addition to a detailed description of the schemes, any modifications of the schemes are described.

4.1 Goyal, Pandey, Sahai and Waters, 2006

This scheme was the first ABE scheme with expressive access policies. Policies are associated with the key (KP-ABE). It was described by Goyal, Pandey, Sahai and Waters [6] in 2006. This scheme will be referred to as GPSW.

Goyal *et. al.* extend the earlier work from Sahai and Waters [18] to allow arbitrary access structures expressed by access trees, not just a "k-out-of-n" attributes. They are the first to use Shamir's Secret Sharing hierarchically in the access tree as described in Section 2.3.3.

The GPSW scheme encrypts a message represented by a point of the bilinear pairing's target group \mathbb{G}_T . The main construction follows the small universe approach, but a construction allowing arbitrary attributes is also given. The construction described here is the small universe construction.

The construction given here is exactly as implemented; it differs from the original construction in the use of an asymmetric pairing ($e : \mathbb{G}_1 \times \mathbb{G}_2 \rightarrow \mathbb{G}_T$) instead of a symmetric pairing ($e : \mathbb{G}_1 \times \mathbb{G}_1 \rightarrow \mathbb{G}_T$).

In the GPSW construction, the pairing is only evaluated during decryption phase for leaf nodes (see below). There, the curve point on one side comes from the ciphertext, and the point on the other side from the key. Originally, a symmetric pairing is used, so their order can be swapped freely. As we want to improve the speed of the encryption, we use the shorter elements of \mathbb{G}_1 for everything that has to do with the ciphertext, because that way only elements of \mathbb{G}_1 need to be manipulated during encryption.

To speed up encryption and decryption, the plaintext is not encrypted with the GPSW ABE scheme directly. Instead, a random group element is chosen and encrypted under GPSW (i.e. a $k \in \mathbb{G}_T$). This element is hashed to obtain a symmetric key, which is then used to encrypt the plaintext with AES-GCM (an AEAD mode of operation). The ciphertext now consists of the GPSW-encrypted group element plus the AES-GCM ciphertext.

Let \mathbb{G}_1 and \mathbb{G}_2 be bilinear groups of prime order q . Let P be a generator of \mathbb{G}_1 and Q be a generator of \mathbb{G}_2 . Let $e : \mathbb{G}_1 \times \mathbb{G}_2 \rightarrow \mathbb{G}_T$ be a bilinear map. Note that \mathbb{G}_1 and \mathbb{G}_2 are written additively, but \mathbb{G}_T is written using multiplicative notation.

Setup [6]. The attribute universe is defined as $U = \{1, 2, \dots, n\}$ and is fixed. For every attribute $i \in U$, choose uniformly at random a secret number $t_i \in \mathbb{Z}_q$. Then the public key of attribute i is $T_i = t_i \cdot P$. Also, choose uniformly at random the master private key $y \in \mathbb{Z}_p$, from which the master public key $Y = e(P, Q)^y$ is derived.

Publish $Params = (Y, T_1, \dots, T_n)$ as the public parameters, privately save $MK = (y, t_1, \dots, t_n)$ as the master key.

KeyGen(Γ, MK) [6]. Input: access tree Γ and master key MK .

For each node u in the access tree Γ , recursively define polynomials $q_u(x)$ with degree $(d_u - 1)$, starting from the root.

For the root r , set $q_r(0) = s$ and randomly choose $d_r - 1$ other points to determine the polynomial $q_r(x)$. Then, for any other node u , including leaf nodes, set $q_u(0) = q_{\text{parent}(u)}(\text{index}(u))$ and choose $d_u - 1$ other points at random to define the polynomial. For all leaf nodes u , create a secret share $D_u = q_u(0) \cdot t_i^{-1} \cdot Q$ where $i = \text{att}(u)$.

The set of these secret shares is the decryption key $D = \{D_u | u \text{ leaf node of } \Gamma\}$.

Encrypt($M, \omega, Params$) [6]. Input: Message $M \in \mathbb{G}_T$, set of attributes ω and public parameters $Params$.

Choose $s \in \mathbb{Z}_q$ at random and compute $E' = M \cdot Y^s$. For each attribute $i \in \omega$ compute $E_i = s \cdot T_i$.

Return the ciphertext as $E = (\omega, E', \{E_i | i \in \omega\})$

Decrypt(E, D) [6]. Input: Ciphertext E and decryption key D .

First, define a recursive procedure $\text{DecryptNode}(E, D, u)$ which takes as inputs a ciphertext $E = (\omega, E', \{E_i | i \in \omega\})$, the decryption key D and a node u of the access tree associated with the decryption key. It outputs either an element of \mathbb{G}_T or \perp .

If u is a leaf node, then $i = \text{att}(u)$ and

$$\text{DecryptNode}(E, D, u) = \begin{cases} e(E_i, D_u) = e(s \cdot t_i \cdot P, q_u(0) \cdot t_i^{-1} \cdot Q) = e(P, Q)^{s \cdot q_u(0)} & i \in \omega \\ \perp & i \notin \omega \end{cases} \quad (4.1)$$

If u is not a leaf node, instead call $\text{DecryptNode}(E, D, v)$ for all child nodes v of u and store the result in F_v . Let S_u be an arbitrary d_u -sized subset of child nodes v with $F_v \neq \perp$. If no such set exists, the node was not satisfied. In this case return \perp . Then

compute with $i = \text{index}(z)$ and $S'_u = \{\text{index}(z) | z \in S_u\}$.

$$\begin{aligned}
 F_u &= \prod_{z \in S_u} F_z^{\Delta_{i,S'_u}(0)} \\
 &= \prod_{z \in S_u} (e(P, Q)^{s \cdot q_z(0)})^{\Delta_{i,S'_u}(0)} \\
 &= \prod_{z \in S_u} (e(P, Q)^{s \cdot q_{\text{parent}(z)}(\text{index}(z))})^{\Delta_{i,S'_u}(0)} \\
 &= \prod_{z \in S_u} e(P, Q)^{s \cdot q_u(i) \cdot \Delta_{i,S'_u}(0)} \\
 &\stackrel{(*)}{=} e(P, Q)^{s \cdot q_u(0)}
 \end{aligned} \tag{4.2}$$

The equality $(*)$ holds because, in the exponent, the product becomes a sum: $\sum_{i \in S'_u} s \cdot q_u(i) \cdot \Delta_{i,S'_u}(0)$ is exactly the lagrange interpolation of $s \cdot q_u(0)$.

Let the root of the access tree be r , then the decryption algorithm simply calls $\text{DecryptNode}(E, D, r) = e(P, Q)^{s \cdot y} = Y^s$, if the ciphertexts's attributes satisfy the access tree. If they don't, then $\text{DecryptNode}(E, D, r) = \perp$.

To retrieve the message from $E' = M \cdot Y^s$, simply calculate and return $M' = E' \cdot (Y^s)^{-1}$.

Of course, it is rather difficult (and slow) to encode the full plaintext as a group element of G_T . Therefore, it is advisable to simply generate a random $K \in G_T$ and encrypt the plaintext using a secure symmetric cipher with key $k = \text{KDF}(K)$, where KDF is a key derivation function. Then encrypt the point K using the GPSW scheme and attach its ciphertext to the symmetric ciphertext. Correct decryption of $K \in G_T$ then allows a receiver to decrypt the actual payload.

4.2 Yao, Chen and Tian 2015

This scheme was described by Yao, Chen and Tian [9] in 2015. In 2019, Tan, Yeow and Hwang [22] proposed an enhancement, fixing a flaw in the scheme and extending it to be a hierarchical KP-ABE scheme.

Yao, Chen and Tian's ABE scheme (hereafter written just YCT) is a KP-ABE scheme that does not use any bilinear pairing operations. Instead, the only operation performed on Elliptic Curves are point-scalar multiplication [9]. This makes it especially useful for our resource-constrained context, as bilinear pairings are significantly more costly in terms of computation and memory.

As opposed to other ABE schemes based on pairings, YCT uses a hybrid approach similar to Elliptic Curve Integrated Encryption Standard (ECIES): The actual encryption

of the plaintext is done by a symmetric cipher, for which the key is derived from a curve point determined by the YCT scheme [9]. If a key's access structure is satisfied by a certain ciphertext, this curve point and thus the symmetric encryption key can be reconstructed, allowing for decryption [9].

The original description of this scheme uses the x- and y-coordinates as keys for separate encryption and authentication mechanisms. Instead, my implementation uses a combined AEAD scheme (more specifically, AES-256 in CCM mode). This uses a single key (derived by hashing the curve point) to ensure confidentiality and integrity of the data.

The description below includes the fix proposed in [22], for which an additional PRF is used to randomize the value of the $\text{index}(\cdot)$ function for nodes of the access tree. For this, instead of $\text{index}(\cdot)$, the modified $\text{index}'(\cdot) = \text{PRF}(r_l, \text{index}(\cdot))$ is used [22]. r_l is a random seed value that differs for each layer l of the access tree [22]. In our implementation, HMAC-SHA512 is used as the PRF.

The four algorithms of the YCT scheme are defined as follows:

Setup [9]. The attribute universe is defined as $U = \{1, 2, \dots, n\}$ and is fixed.

For every attribute $i \in U$, choose uniformly at random a secret number $s_i \in \mathbb{Z}_q^*$. Then the public key of attribute i is $P_i = s_i \cdot G$ (i.e. a curve point).

Also, choose uniformly at random the master private key $s \in \mathbb{Z}_q^*$, from which the master public key $PK = s \cdot G$ is derived.

Publish $Params = (PK, P_1, \dots, P_n)$ as the public parameters, privately save $MK = (s, s_1, \dots, s_n)$ as the private master key.

KeyGen(Γ, MK) [9]. Input: access trees Γ and master key MK .

For each layer $l = 0, 1, \dots$ of the access tree, generate a random seed value $r_l \in \mathcal{K}_{PRF}$ from the PRF's key space.

For each node u in the Access Tree Γ , recursively define polynomials $q_u(x)$ with degree $(d_u - 1)$, starting from the root.

For the root r , set $q_r(0) = s$ and randomly choose $(d_r - 1)$ other points to determine the polynomial $q_r(x)$. Then, for any other node u (including leafs), set $q_u(0) = q_{\text{parent}(u)}(\text{index}'(u))$ and choose $(d_u - 1)$ other points for q_u , similar to above.

Whenever u is a leaf node, use $q_u(x)$ to define a secret share $D_u = \frac{q_u(0)}{s_i}$; where $i = \text{attr}(u)$, s_i the randomly chosen secret number from *Setup* and s_i^{-1} the inverse of s_i in \mathbb{Z}_q^* .

Return the generated key as $D = (\{D_u | u \text{ leaf node of } \Gamma\}, \{r_0, r_1, \dots\})$.

Encrypt($m, \omega, Params$) [9]. Input: Message m , set of attributes ω and public parameters

Params.

Randomly choose $k \in \mathbb{Z}_q^*$ and compute $C' = k \cdot PK$. If $C' = \mathcal{O}$, repeat until $C' \neq \mathcal{O}$. $C' = (k_x, k_y)$ are the coordinates of the point C' . k_x is used as the encryption key and k_y as the integrity key.

Then compute $C_i = k \cdot P_i$ for all attributes $i \in \omega$.

Encrypt the actual message as $c = \text{Enc}(m, k_x)$, generate a Message Authentication Code $\text{mac}_m = \text{HMAC}(m, k_y)$.

Return the ciphertext $CM = (\omega, c, \text{mac}_m, \{C_i | i \in \omega\})$

Decrypt($CM, D, Params$) [9]. Input: Ciphertext CM , decryption key D and public parameters $Params$.

Decryption is split into two phases: Reconstructing the curve point C' to get the encryption and integrity keys, and actual decryption of the ciphertext.

First, define a recursive decryption procedure for a node u : $\text{DecryptNode}(CM, D, u)$. For leaf nodes with $i = \text{attr}(u)$:

$$\text{DecryptNode}(CM, D, u) = \begin{cases} D_u \cdot C_i \stackrel{(*)}{=} q_u(0) \cdot k \cdot G & i \in \omega \\ \perp & i \notin \omega \end{cases}$$

Where the equality $(*)$ holds because s_i and s_i^{-1} cancel out:

$$D_u \cdot C_i = q_u(0) \cdot s_i^{-1} \cdot k \cdot P_i = q_u(0) \cdot s_i^{-1} \cdot k \cdot s_i \cdot G = q_u(0) \cdot k \cdot G$$

For an internal node u on layer l , call $\text{DecryptNode}(CM, D, v)$ for each of its children v . If for less than d_u of the child nodes $\text{DecryptNode}(CM, D, v) \neq \perp$, return $\text{DecryptNode}(CM, D, u) = \perp$. Then let ω_u be an arbitrary subset of d_u child nodes of u , where for all $v \in \omega_u$, $\text{DecryptNode}(CM, D, v) \neq \perp$. Then $\text{DecryptNode}(CM, D, u)$ is defined as follows, where $i = \text{index}(v)$, $\omega'_u = \{\text{index}(v) | v \in \omega_u\}$.

$$\begin{aligned} & \text{DecryptNode}(CM, D, u) \\ &= \sum_{v \in \omega_u} \Delta_{\omega'_u, i}(0) \cdot \text{DecryptNode}(CM, D, v) \\ &= \sum_{v \in \omega_u} \Delta_{\omega'_u, i}(0) \cdot q_v(0) \cdot k \cdot G \\ &= \sum_{v \in \omega_u} \Delta_{\omega'_u, i}(0) \cdot q_{\text{parent}(v)}(\text{index}(v)) \cdot k \cdot G \\ &= \sum_{v \in \omega_u} \Delta_{\omega'_u, i}(0) \cdot q_u(i) \cdot k \cdot G \\ &\stackrel{(*)}{=} q_u(0) \cdot k \cdot G \end{aligned}$$

The equality (*) holds because $\sum_{v \in \omega'_u} \Delta_{\omega'_u, i}(0) \cdot q_u(i) = q_u(0)$ is exactly the lagrange interpolation polynomial $q_u(x)$ at $x = 0$ with respect to the points $\{(index(v), q_v(0)) | v \in \omega_u\}$.

This means for the root r of the access tree Γ , we have

$$\text{DecryptNode}(CM, D, r) = q_r(0) \cdot k \cdot G = s \cdot k \cdot G = (k'_x, k'_y)$$

With k'_x the decryption key for m and k'_y the integrity key. Therefore now decrypt $m' = \text{Dec}(c, k'_x)$.

Now check if $\text{HMAC}(m', k'_y) = \text{mac}_m$. If yes, the ciphertext has been correctly decrypted and was not tampered with. Return m' , otherwise return \perp .

5 Implementation

5.1 Building Blocks

5.1.1 Hardware

The main goal of this project was to implement an ABE scheme on a constrained embedded ARM processor. More specifically, the chip used was a Nordic Semiconductor nRF52840 with a 64MHz Cortex M4 CPU, 256kB of RAM and 1MB of flash storage. For the detailed specifications, see [35]. This SoC will be referred to simply as ‘the SoC’.

For reference, the implementation was also tested on a standard laptop, referred to as ‘the Laptop’. More specifically, this system has a 2.7GHz Intel i7-7500U CPU and 16GB of RAM. It runs a Linux-based operating system.

5.1.2 Programming language and libraries

Rust

Rust was chosen as programming language for this project. First, it is a compiled language and thus incurs little overhead at runtime. Its speed is comparable to that of C/C++. Second, it provides much stronger memory safety guarantees than other compiled languages (especially C/C++ where extreme care is required to avoid introducing exploitable vulnerabilities). This is especially attractive for security-critical components like an encryption library.

The rabe-bn library

A Rust-only implementation of elliptic curves and a pairing is provided by the open-source library `rabe-bn`¹, a derivative of the `bn` library by Zcash [36]. It implements a concrete pairing on 256-bit *BN curves*. BN curves are a family of pairing-friendly elliptic curves proposed by Barreto and Naehrig [37].

The 256-bit modulus of the BN curve used in `rabe-bn` was originally believed to provide a security level of 128 bits [38]. Due to the discovery of better attacks on the

¹<https://github.com/georgbramm/rabe-bn>

underlying cryptographic assumptions, the estimate for the security level has been revised down to 100 bits [39].

The library provides four structs: `G1`, `G2` and `Gt`, elements of the groups G_1 , G_2 and G_T , respectively. Let their orders be r , then `Fr` represents an element of the field \mathbb{F}_r .

For the elliptic curve groups (structs `G1` and `G2`), additive notation is used and the `*` operator is conveniently overloaded. The target group (struct `Gt`) uses multiplicative notation. For this reason, the description of the schemes in Chapter 4 has also been adapted to use compatible notation.

nRF52840 HAL crate

For easier access to the peripherals of the SoC, the hardware abstraction layer (HAL) crate `nrf52840-hal` was also used. It provides simplified access to the hardware random number generator (RNG) and the timers. Strictly speaking, these were not necessary to build a Rust library (timers are only needed for evaluation and the library interface allows the caller to pass their desired random number generator). However, for testing and actual use of the library bare-metal on the SoC, both the RNG and the timers were needed.

heapless crate

The `heapless`² crate provides stack-allocated versions of some of the data structures from `std::collections`. Most important were `heapless::Vec` (replaces `std::vec::Vec`) and `heapless::IndexMap` (replaces `std::collections::HashMap`). These data structures are statically allocated and expect their desired capacity as an additional generic type parameter.

5.2 Porting rabe-bn to the SoC

The implementation of `rabe-bn` unfortunately relied on the standard library (mostly through the use of heap-allocated dynamic vectors, i.e. `std::vec::Vec`) and is thus not suited for bare-metal applications. Rust provides the dependency-free and platform-agnostic core library as an alternative to the standard library. This library does not depend on an operating system or dynamic memory allocation, and thus does not include heap-allocated data structures (like `std::vec::Vec`).

Therefore, I rewrote the `rabe-bn` library to introduce a cargo-feature `std` which controls the inclusion of the standard library and is enabled by default. If this feature is

²<https://crates.io/crates/heapless>

disabled, the `core` library and stack-allocated collections of fixed size from the `heapless` crate are used instead.

Some further modifications were necessary to implement the `core::fmt::Display` trait for the `Gt` struct in a bare-metal compatible manner. The implementation of this trait was used in conjunction with SHA-3 as a key derivation function to create an AES key from curve points. The behavior of the `core::fmt::Display` implementation stayed exactly the same to ensure interoperability with the original `rabe-bn` library.

With these modifications, the `rabe-bn` library runs on the SoC.

5.3 Random Number Generation

Regular Rust programs use the `rand` crate's `ThreadRng` struct to generate random numbers. `ThreadRng` is cryptographically secure [40], but it relies on the operating system randomness pool for seeding.

Therefore, this generator is unavailable on the SoC. Instead we use the hardware RNG. The `nrf52840-hal` crate directly implements the trait `rand::RngCore` for the hardware RNG, which makes it extremely easy to use. This generator, however, is quite slow and speed can differ greatly: With bias correction enabled (required for uniform distribution of the generated data), typically around $120\mu\text{s}$ per byte [35].

To alleviate this, the hardware RNG is only used to seed a ChaCha20 pseudorandom number generator (crate `rand_chacha`). This is essentially the same construction as the current implementation of `ThreadRng` [40].

5.4 Representation of Access Trees

The Rust type system is very well suited to represent the type of tree structures we need for Access Trees. A simple implementation might look like the one in Listing 5.1.

Listing 5.1: Simple Implementation of Access Trees (using the standard library)

```
enum AccessTree<'a> {  
    // threshold, vector of children  
    Node(u64, std::vec::Vec<AccessTree<'a>>),  
    // reference to the attribute label  
    Leaf(&'a str),  
}
```

This, however, does not work when the `std::vec::Vec` is replaced by a stack-allocated `heapless::Vec`: The `std::vec::Vec` is allocated on the heap and thus only

a pointer to the vector needs to be stored in the `AccessTreeNode`. This pointer has constant size, of course.

A `heapless::Vec` is not located on the heap, but directly inside the `AccessTreeNode`. Even if there is a limit on the number of children a single inner node might have, there is no limit to the depth of the access tree. Therefore, an `AccessTreeNode` might be arbitrarily large because the `heapless::Vec` might need to hold an arbitrary number of child nodes.

Because of this, Access Trees were implemented as a flat slice of nodes as in Listing 5.2. The vector of children doesn't hold references to the children themselves, but only their index within the vector of Access Tree nodes. This again introduces an indirection (like the heap pointer in the simple implementation) and allows the enums to have constant size.

Listing 5.2: Refined implementation of Access Trees (works without standard library)

```
type AccessTree<'a, 'b> = &'b [AccessNode<'a>];
enum AccessNode<'a> {
    // threshold, vector of child indexes
    Node(u64, heapless::Vec<u8, consts::U16>),
    // reference to the attribute label
    Leaf(&'a str),
}
```


6 Evaluation

6.1 Performance of rabe_bn

See Table 6.1 for performance measurements of random element sampling, group operations, group-scalar exponentiation and the pairing operation. The times have been measured using randomly sampled elements and averaged over 100 calls each.

Operation	SoC [ms]	Laptop [ms]
Smpl. from \mathbb{F}_r	2.164	0.003
Smpl. from G_1	152.852	0.600
Smpl. from G_2	626.686	2.398
Smpl. from G_T	2427.512	9.325
Group op. G_1	0.614	0.002
Group op. G_2	2.684	0.010
Group op. G_T	4.653	0.017
Exp. in G_1	149.973	0.576
Exp. in G_2	641.408	2.462
Exp. in G_T	1414.849	5.279
Pairing	1641.741	6.412

Table 6.1: Execution times for various operations on the SoC and the laptop

It is apparent that the the cost of the operations differs greatly between the groups. Sampling a random element takes about the same time as group exponentiation for G_1 and G_2 , but is significantly more costly for G_T . Looking at the implementation, the reason for this is obvious: Sampling from G_1 and G_2 simply generates a random $z \in \mathbb{F}_r$ and returns the group element $z \cdot G$ for G a generator. Sampling from G_T is done by generating random elements of G_1 and G_2 and computing their pairing. This is reflected in the measured timings.

Interestingly, RAM size seems to be a limiting factor when computing pairings on embedded devices. During development, I also tested the ported rabe_bn library on the nRF52832 SoC, which has 64KB of RAM (vs. 256KB in the nRF52840). On this chip, a pairing could be computed successfully only if the library was built *without debug*

symbols. With debug symbols, there was not enough RAM available and the pairing computation failed. This suggests that the memory use during pairing computation is close to 64KB, which would still be a quarter of the RAM on the nRF52840 SoC. While this memory is not consumed permanently, it still needs to be available when the pairing function is called.

6.2 Performance of the ABE schemes

For performance measurements of the GPSW and YCT ABE schemes, see Figure 6.1. The diagrams show the time required to compute Setup, Encryption, Key Generation and Decryption on the nRF52840 SoC and the laptop. The timings for the laptop are purely there to give some context; the evaluation focuses on the performance on the SoC.

6.2.1 Methods of measurement

All times were measured using a hardware timer (on the SoC) and the operating system time (on the laptop) with microsecond resolution.

For Setup, the number of attributes refers to the total number of attributes in the system. For Encrypt, the number of attributes refers to the number of attributes under which a ciphertext is encrypted.

For KeyGen, the number of attributes refers to the number of leaf nodes in the used access policy. The access trees used for this evaluation were generated using a python script, starting with a single node and incrementally adding leaf nodes in random positions to the tree. To minimize the effects of specific access trees, ten sets of policies with size 1 to 30 were generated. The timings for KeyGen and Decrypt represent the average for the policies of the respective size from each of the ten sets.

For Decrypt, the same access policies from KeyGen are used to decrypt a ciphertext encrypted with all system attributes. This ensures that the ciphertext can always be decrypted, and does not influence the decryption speed. The decryption algorithms perform a pre-calculation of a minimal subset of the access tree required to satisfy the policy to reduce the number of pairings (which are necessary only at the leaves). Therefore, the timing for decryption greatly depends on the specific structure of an access tree and the values given here must be understood as a rough heuristic.

Due to the small RAM size, KeyGen and Decrypt failed for policies of larger size with GPSW on the SoC. This is why the KeyGen and Decrypt timings were omitted for policies of more than 25 attributes in the first diagram: For access trees with 26 attributes, KeyGen and Decrypt succeeded only for two of the ten tested trees.

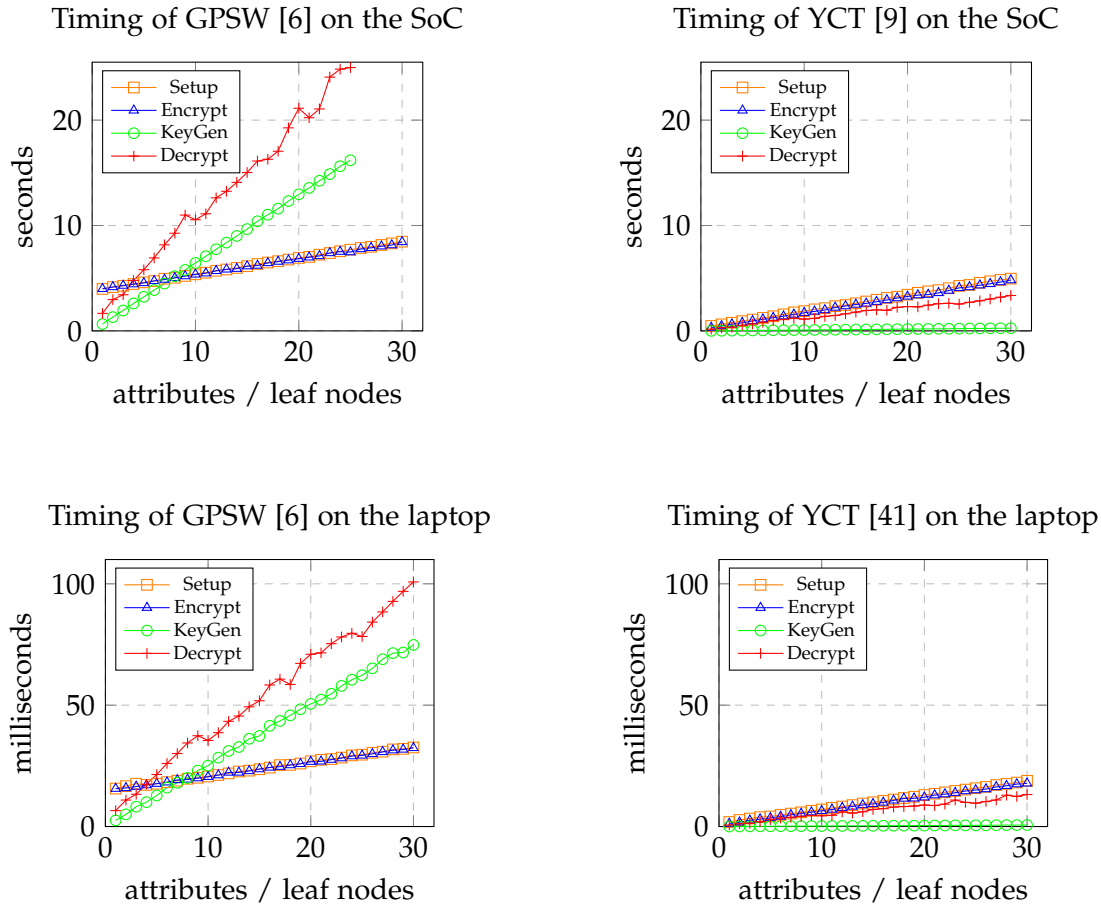


Figure 6.1: Performance of GPSW and YCT on the SoC and on the laptop. Note the different scales between the SoC and the laptop (seconds vs. milliseconds!)

6.2.2 Results

Two results are immediately visible from the diagrams: First, the laptop is obviously much faster than the SoC. This was expected and any other result would be very surprising.

Second, the pairing-free YCT scheme is significantly faster than the pairing-based GPSW scheme on both platforms. For both schemes, Setup takes about the same time as Encryption for the same number of attributes in the system or ciphertext, respectively. In both Setup and Encryption, the difference between GPSW and YCT for the same number of attributes is constant: On the SoC, setup and encryption with a single attribute take about 4 seconds with GPSW and only about 0.4 seconds with YCT. With

30 attributes, GPSW requires about 8.5 seconds and YCT about 5 seconds. The runtimes increase linearly, with each additional attribute adding about 150ms.

For the KeyGen algorithm, difference is even larger: The runtime of GPSW increases considerably for a larger number of attributes. With one attribute, KeyGen takes about 650ms. With 25 attributes, it already takes more than 16 seconds. The runtime of YCT only increases slightly from 35ms for one attribute to 210ms for 25 attributes. For more than 25 attributes, GPSW-KeyGen failed because of limited RAM on the SoC.

The performance of the Decrypt algorithm is very difficult to evaluate because the number of nodes that needs to be evaluated depends on the key's access tree and the ciphertext's attributes. Here again, YCT is much, much faster than GPSW: For larger policies, decryption takes about eight to ten times longer for GPSW than it takes for YCT.

6.3 Discussion

The results for Setup and Encrypt are in line with the time-consuming operations performed by the algorithms: In both schemes, each additional attribute results in one additional exponentiation in G_1 . This takes about 150ms as per Table 6.1, which is exactly the additional time per attribute. The constant overhead of about 3.5 with GPSW is a result of the pairing computation and exponentiation in G_T (for Setup) and the sampling and exponentiation in G_T (for Encrypt).

For the KeyGen algorithm, the speed difference between YCT and GPSW is especially striking: The YCT algorithm is extremely fast for a small number of attributes (below 100ms) and only increases slightly as more attributes are added. GPSW is not only slower already for a few attributes, but its runtime also increases much more quickly when increasing the policy size. Again, looking at the schemes, the reason becomes evident: YCT's KeyGen only works on elements of \mathbb{F}_r , which are small and easy to calculate with. GPSW uses secret shares from G_2 , for which operations take considerably more time. This is also the case for decryption: YCT only requires exponentiation and point addition in G_1 , whereas GPSW performs pairings and multiplications and exponentiations in G_T .

6.4 Further Improvements / Future Work

For embedded applications, computation time is not the only important factor. The large size of ABE ciphertexts can be a problem, e.g. when transmitting data via low-power wireless protocols (such as Bluetooth LE). In addition to the hybrid encryption approach taken in this thesis (key encapsulation, i.e. choosing a random symmetric

key to encrypt the payload and then encrypting this key using ABE), measures such as point compression can be taken.

Another option to reduce both message sizes and encryption times would be to re-use the ABE-encapsulated symmetric key: Instead of encrypting each message with a new random symmetric key that is encapsulated with ABE, the same symmetric key can be used for many messages. For example, an embedded sensor could choose a new symmetric key once per day, encrypt it using ABE and transmit it to the cloud. Then it could use this key to encrypt all messages for the next 24 hours, requiring very short encryption times and little overhead for the message size. This construction was already implemented for the *Medisec* application, but not evaluated for this thesis because it is not a “true” Attribute-Based Encryption scheme.

7 Conclusion

ABE is feasible, but only for encryption, only when a few seconds wait time is ok and for small policy sizes / attribute numbers.

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Glossary

access policy A policy that defines what combination of attributes shall be required to access data. Formalized by an access structure and usually realized by an access tree, see Section 2.2.4 plural. 5, 7–9, 24, 42, 43

access structure defines the attribute combinations that are required and sufficient to decrypt a ciphertext. See Section 2.2.4 and Section 2.2.5. 8, 10, 20, 21, 27

access tree construction to realize (monotone) access structures. See Section 2.2.5. 20, 21, 24–27, 35, 37

asymmetric encryption scheme type of encryption scheme where different keys are used for encryption and decryption. The encryption key may be made public, while the decryption key is kept private.. 3, 4, 6

attribute Property of an actor or object, e.g. „is student“ or “has blonde hair”. 3, 25, 37, 42, 43

attribute universe set of possible attributes. 6

ciphertext-policy ABE Variant of ABE where the key is associated with an access policy and the ciphertext is associated with a set of attributes. 5, 8, 22, 45

crate bundle of Rust code that groups some related functionality together. Can be published to `crates.io` to share with other developers. 31, 32

diffie-hellman key exchange key agreement protocol that allows two parties A and B to agree on a shared secret value over an insecure channel. A and B can derive the same secret value, while any adversaries cannot (as long as they only passively eavesdrop, but not modify, the information exchanged between A and B). 21

digital signature scheme asymmetric cryptographic scheme/protocol for ensuring message authenticity and integrity. 21

elliptic curve Algebraic structure that forms a group, see Section 2.4. 15, 17

generic group model formal security model assuming that the attacker only has oracle access to the group operation (provides less formal security than the standard model). 20

group A set together with a binary operation that satisfies the group axioms, see Section 2.4.1. 42

identity-based encryption type of encryption where data is encrypted using a unique identity (e.g. an email address or phone number), and only the participant holding the secret key corresponding to that identity is able to decrypt the ciphertext.. 20

key derivation function function that derives a suitable cryptographic key from some other data, that may be too long or not in the right format to serve as a key. Usually, hash functions are used as KDF. 26, 32

key generation center Trusted central authority that sets up an ABE scheme and generates keys for users of an ABE scheme . 3, 6, 8, 9, 45

key-policy ABE Variant of Attribute-Based Encryption (ABE) where the ciphertext is associated with an access policy and the key is associated with a set of attributes. 5

large universe type of ABE construction where any string can be used as an attribute. 6

linear secret sharing scheme secret sharing scheme in which the share generation can be described by a matrix. See [8]. 20, 45

monotone span program linear algebraic computation model that is equivalent to LSSS. See Section ?? 45

security level measure of the strength of a cryptographic scheme, usually given in bits. A security level of n bits means that the most efficient attack needs to perform at least around 2^n operations to break the scheme. Note that this does not directly translate to the size of the used parameters: to guarantee a security level of n bits, usually the the size of the field underlying our elliptic curve (i.e. the number of bits of its modulus) needs to be *at least* $2n$, sometimes much larger. 21

small universe type of ABE construction where the possible attributes have to be fixed when the system is instantiated. 6, 24

standard model formal security model that imposes no restrictions on the attacker, except for the limit on the complexity of their computations. 20, 43

symmetric encryption scheme type of encryption scheme where the same key is used for encryption and decryption. This means that the key has to be shared among all parties via some secure channel (e.g. a personal meeting).. 3, 4

Acronyms

ABE Attribute-Based Encryption. 1, 3–6, 8, 9, 20–24, 30, 37, 38, 42, 43, 45

ABE scheme Attribute-Based Encryption scheme. 2, 6, 20–22, 24, 35, 38, 40, 43

AEAD authenticated encryption and associated data. 24, 27

BLE bluetooth low energy. 2, 3

CP-ABE Ciphertext-Policy ABE. 6, 8, 9, 13, 20, 23, 40, *Glossary*: ciphertext-policy ABE

GDPR general data protection regulation. 3

HAL hardware abstraction layer. provides abstract access to chip-specific peripherals.
31

IoT Internet of Things. 1, 14, 23

KGC Key Generation Center. 3, 6–9, 14, 15, *Glossary*: Key Generation Center

KP-ABE Key-Policy ABE. 6–9, 13, 20–22, 24, 40, *Glossary*: key-policy ABE

LSSS linear secret sharing scheme. 20, 43, *Glossary*: linear secret sharing scheme

MSP monotone span program. *Glossary*: monotone span program

PRF pseudorandom function. Function that has a (seemingly) random relation between its in- and outputs. Usually hash functions are used as PRF. 27

RNG random number generator. 31, 32

SHA-3 Secure Hash Algorithm 3. Cryptographic hash function standardized by NIST.
32

SIMD single instruction, multiple data. Vectorized CPU instruction that processes several pieces of data at once. 22

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