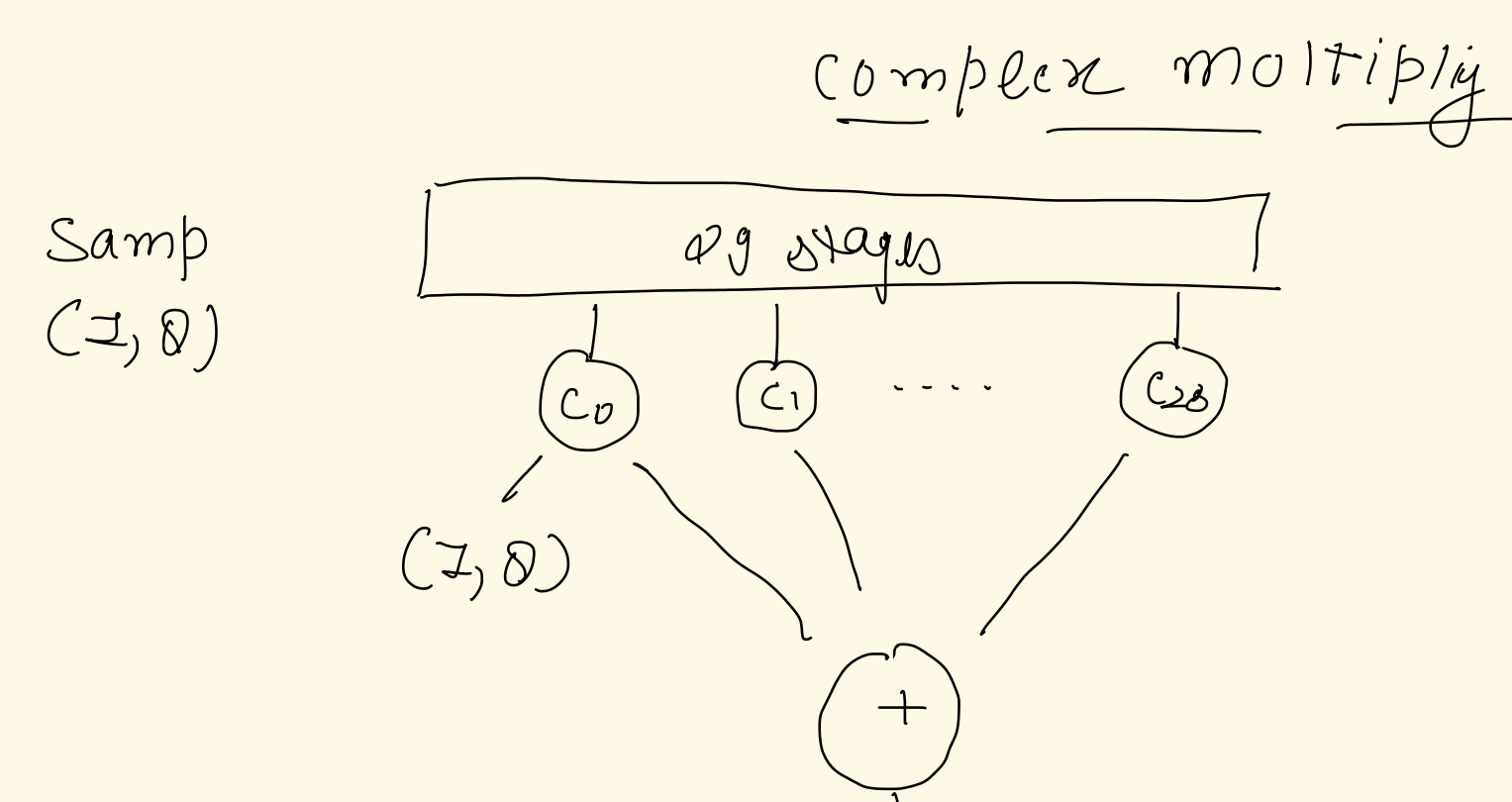


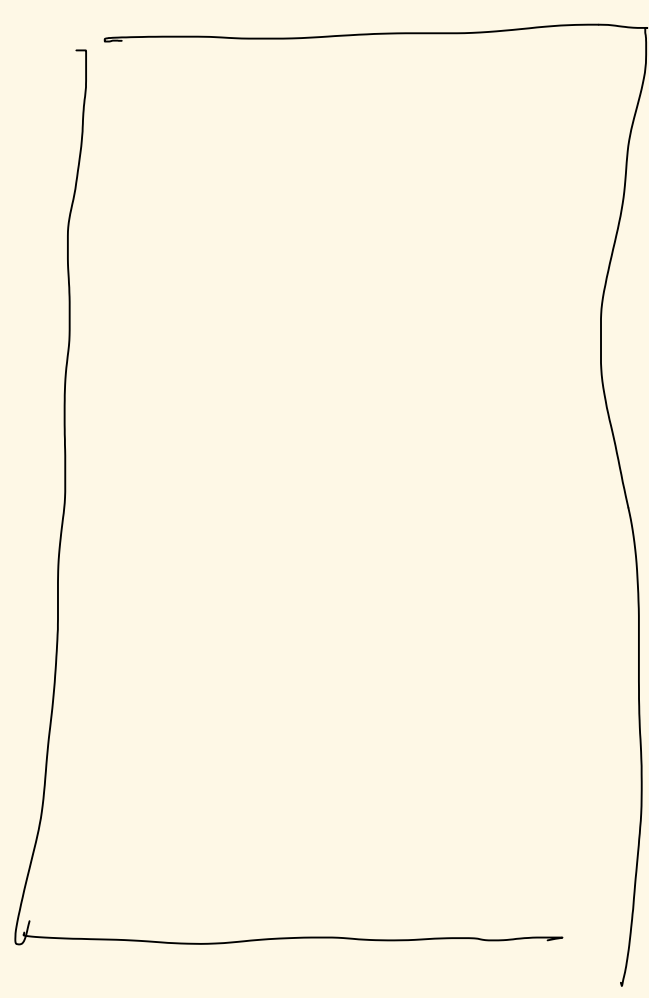
29 tap filter



$\{0, 1, 2, 3, \dots, 28\}$

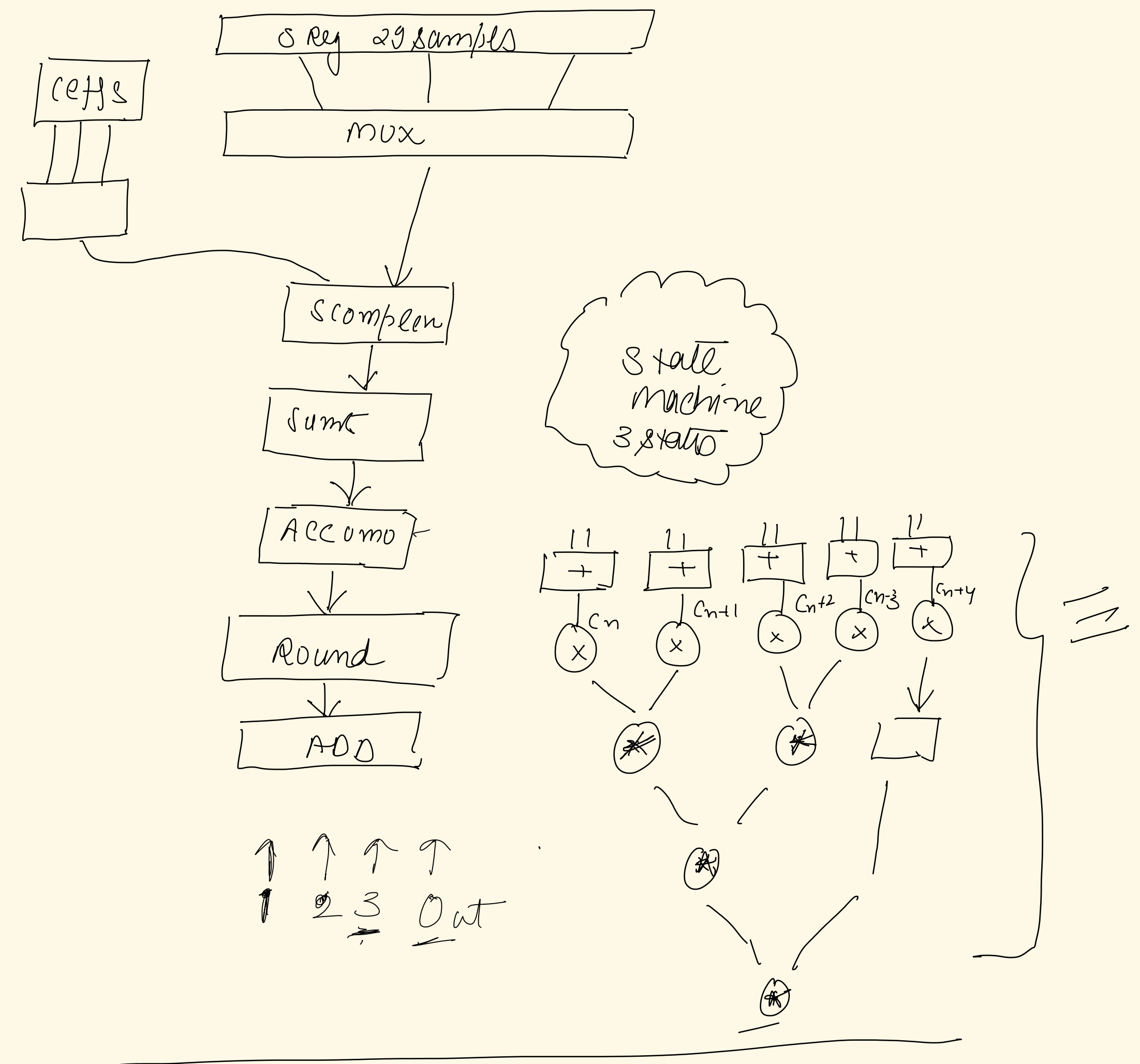
1 1

- 300MHz / sample at every clock
- 5 complex multiplies → 1 DW mult
- 1/2 to w/ 29 tap filter model.
- 



29 samples (48 bits) = 1392 } 2202 Hz

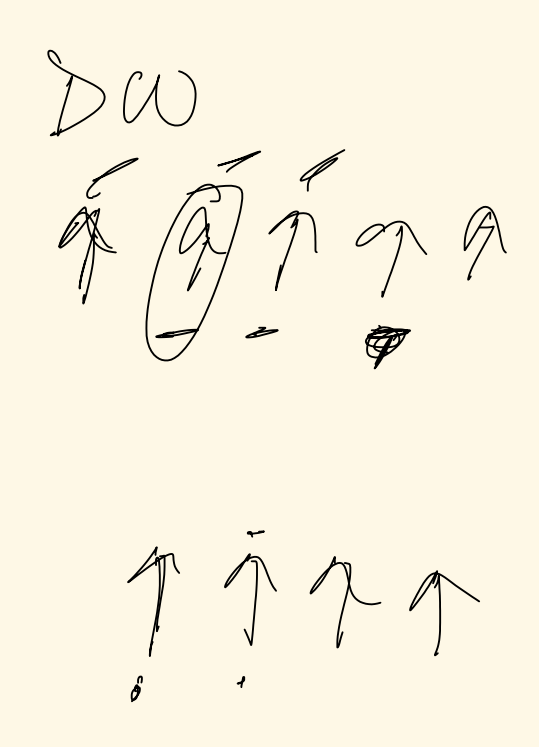
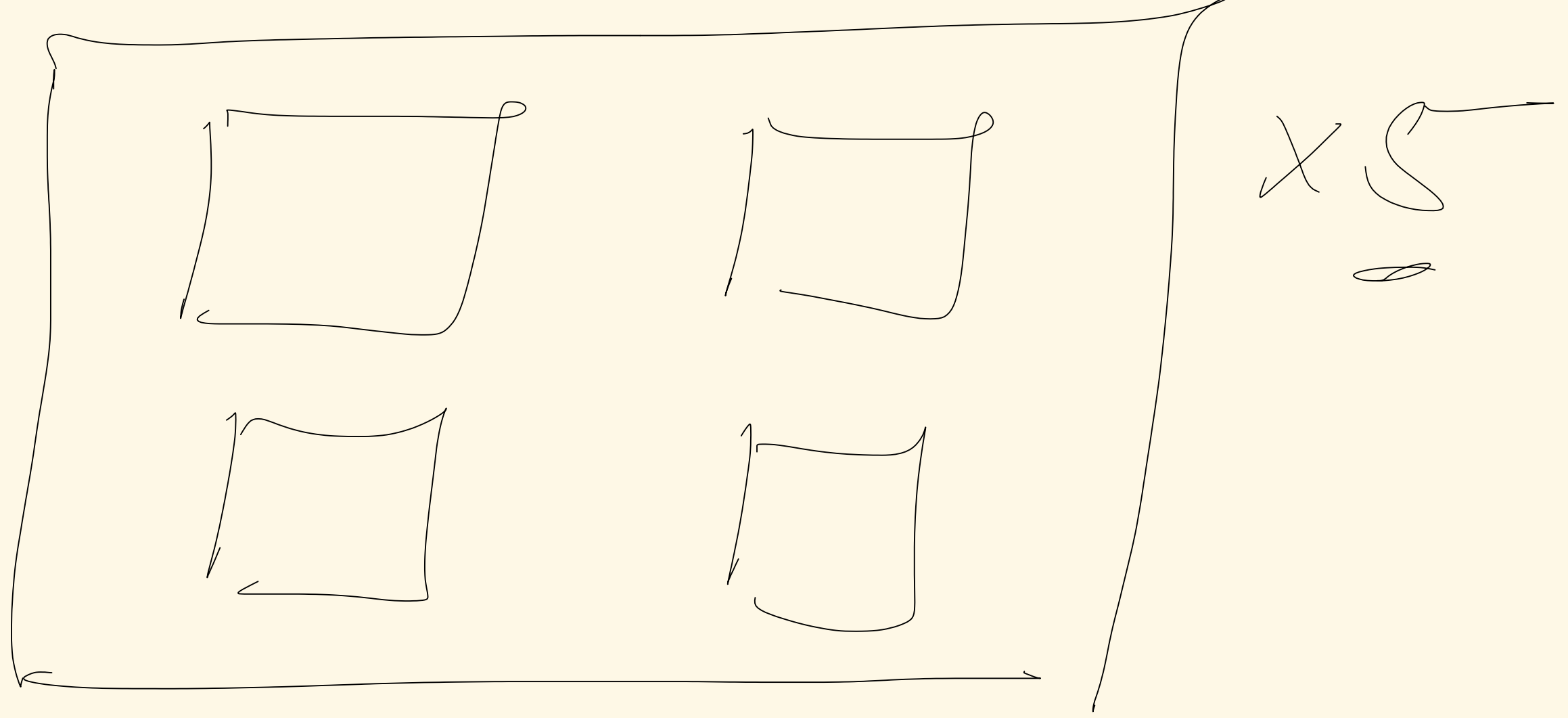
5 samples (54 bits) = 810



$$\begin{aligned} &A \quad B \\ &(A_1 + jA_2) \times (B_1 + jB_2) \\ &= A_1B_1 - A_2B_2 + j(A_1B_2 + A_2B_1) \\ &= (A_1B_1 - A_2B_2) + j(A_1B_2 + A_2B_1) \end{aligned}$$

$$(A + jB) * (C + jD)$$

$$AC + BC + AD + BC$$



- cell and address pre mult update every cycle
- coeff taken at start of every filter

