

ORDERING INFORMATION

Z80 CPU NMOS/CMOS

NMOS 4MHz

40pin DIP 44pin PLCC
Z840004DSE Z840004VSC
Z840004PSC

CMOS 4MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C0004DEE Z84C0004VEC Z84C0004FEC*
Z84C0004PSC Z84C0004VSC
Z84C0004PEC

NMOS 6MHz

40pin DIP 44pin PLCC
Z840006DSE Z840006VSC
Z840006PSC

CMOS 6MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C0006DEE Z84C0006VEC Z84C0006FEC*
Z84C0006PSC Z84C0006VSC
Z84C0006PEC

NMOS 8MHz

40pin DIP 44pin PLCC
Z840008PSC Z840008VSC

CMOS 8MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C0008PSC Z84C0008VEC Z84C0008FEC*
Z84C0008PEC Z84C0008VSC

CMOS 10MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C0010PEC Z84C0010VEC Z84C0010FEC*

Z80 DMA NMOS/CMOS: (Continued)

CMOS 6MHz

40pin DIP 44pin PLCC
Z84C1006PEC Z84C1006VEC
Z84C1006DEE

CMOS 8MHz

40pin DIP 44pin PLCC
Z84C1008PEC Z84C1008VEC
Z84C1008DEE

Z80 PIO NMOS/CMOS:

NMOS 4MHz

40pin DIP 44pin PLCC
Z0842004DSE Z0842004VSC
Z0842004PSC

CMOS 4MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C2004DEE Z84C2004VEC Z84C2004FEC*
Z84C2004PSC Z84C2004VSC
Z84C2004PEC

NMOS 6MHz

40pin DIP 44pin PLCC
Z0842006DSE Z0842006VSC
Z0842006PSC

CMOS 6MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C2006DEE Z84C2006VEC Z84C2006FEC*
Z84C2006PSC Z84C2006VSC
Z84C2006PEC

CMOS 8MHz

40pin DIP 44pin PLCC 44pin QFP
Z84C2008PSC Z84C2008VEC Z84C2008FEC*
Z84C2008PEC Z84C2008VSC

Z80 DMA NMOS/CMOS:

NMOS 4MHz

40pin DIP 44pin PLCC
Z0841004PSC Z0841004VSC
Z0841004DSE

CMOS 4MHz

40pin DIP 44pin PLCC
Z84C1004PEC Z84C1004VEC
Z84C1004DEE

ORDERING INFORMATION

CODES

PACKAGE

Preferred

D = Cerdip

P - Plastic

V = Plastic Chip Carrier

Longer Lead Time

C = Ceramic

E = Plastic Quad Flat Pack

TEMPERATURE

S = 0°C to +70°C

$E = -40^{\circ}\text{C}$ to 100°C

M = -55°C to +125°C

Example:

Z84C0010PEC is a CMOS 8400, 10 MHz, Plastic, -40°C to 100°C, Plastic Standard Flow.

The diagram illustrates the timing relationships between several digital signals. The horizontal axis represents time, and the vertical axis represents signal levels. The signals are as follows:

- Z**: A single pulse starting at the beginning of the frame.
- 84C00**: A continuous high signal.
- 10**: A pulse starting after the Z signal ends.
- P**: A pulse starting after the 10 signal ends.
- E**: A pulse starting after the P signal ends.
- C**: A pulse starting after the E signal ends.
- XXXX**: A continuous low signal.

The pulses for 10, P, E, and C are overlapping, while Z, 84C00, and XXXX are non-overlapping.

ENVIRONMENTAL

Preferred

C = Plastic Standard

F = Hermetic Standard

Longer Lead Time

A = Hermetic Stressed

B = 833 Class B Military

D = Plastic Stressed

J = JAN 38510 Military