Neuromorphic engineering I

Lab 6: Integrator Circuits

Group number: 18

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Objectives of this lab: In this lab we will begin to explore the time domain using the follower-integrator and the follower-differentiator circuit.

Both circuits simply contain a transconductance amplifier and a capacitor to implement a low-pass or high-pass filter.

We will use the follower-integrator (FOI) and follower-differentiator circuit on the CoACH chip. The capacitance for both capacitors is $1pF$.

The objectives of the lab are:

1. Understand the behavior of the first-order low-pass follower-integrator and high-pass follower-differentiator circuit in the time and frequency domain in small signal operation.

First-order means that the transfer function amplitude decreases as 1/frequency.

Low-pass (*High-pass*) means that the circuit passes low (high) frequencies and blocks high (low) frequencies.

Follower-integrator (*Follower-differentiator*) means that the output follows the input at low (high) frequencies, and integrates (differentiates) at higher (lower) frequencies.

1. Understand the large signal behavior and other limitations of using a transconductance amplifier to model a linear resistor.

1 Reading

See Chapters 8 and 9 of the Carver Mead book ("Analog VLSI and Neural Systems"), paying particular attention to the time and frequency domain treatments of the RC circuit, pages 240-241 and 246-249 in Chapter 8, and the follower-integrator circuit, pages 252-256 in Chapter 9. Slides are also available introducing linear systems analysis.

2 Prelab

1. How are capacitors constructed in CMOS chip technology? There are several different possible implementations. How are they constructed in neurons? What is the capacitance per square micron of a SiO_2 capacitor with oxide thickness of 10nm? What is the capacitance per square micron area of a lipid-bilayer capacitance with thickness of 5nm? (You will need to look up the dielectric constants for SiO_2 and lipid bilayers; remember to provide your sources in your writeup. One standard source for lipid bilayers is Ohki, Shinpei. "Dielectric constant and refractive index of lipid bilayers." Journal of Theoretical Biology 19.1 (1968): 97-115\footnote{\url{https://www.sciencedirect.com/science/article/pii/0022519368900088}}.)

- MOS capacitors are made out of a semi-conductor substrate (n or p type), an insulator (silicon dioxide layer), and a metal (heavily doped polycrystalline silicon) electrode (the gate). Drain and source of a classic FET can be grounded, in order to mimic the previous MOS capacitor architecture.

- In neurons, the membrane (or phospholipid-bilayer) acts as a capacitor. The charge carriers would be the ions.

- $C = \epsilon \frac{A}{d}$ with $\epsilon = \epsilon_0 \epsilon_{SiO_2}$ all constants, transistor Width and Length (useless as we want capacitance per square micron) from practical 3.

$$\epsilon_0 = 8.86 \times 10^{-12} [F \cdot m - 1] \tag{1}$$
$$\epsilon_{SiO_2} = 3.9 \tag{2}$$
$$d = 10 \times 10^{-9} [m] \tag{3}$$
$$A = 1[m^2] = 1 \times 10^{-12} [\mu m^2] \tag{4}$$

Plugging everything in we get:

$$C = 8.86 \times 10^{-12} \times 3.9 \times \frac{1 \times 10^{-12}}{10 \times 10^{-9}} = 0.0034 [F/m^2] = 3.4 [fF/\mu m^2] \tag{5}$$

- $C = \epsilon \frac{A}{d}$ with $\epsilon = \epsilon_0 \epsilon_{Bilayer}$ (from the "Dielectric constant and refractive index of lipid bilayers"), transistor Width and Length from practical 3.

$$\epsilon_0 = 8.86 \times 10^{-12} [F \cdot m - 1] \tag{6}$$
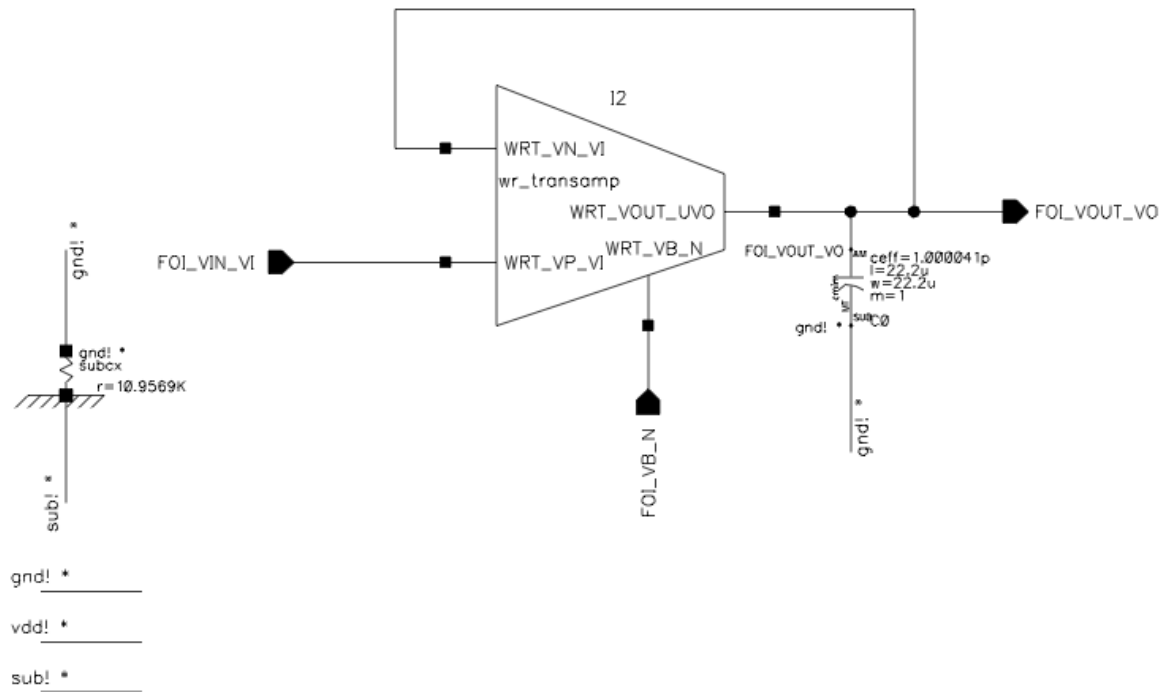$$\epsilon_{Bilayer} = 2.2 \tag{7}$$
$$d = 5 \times 10^{-9} [m] \tag{8}$$
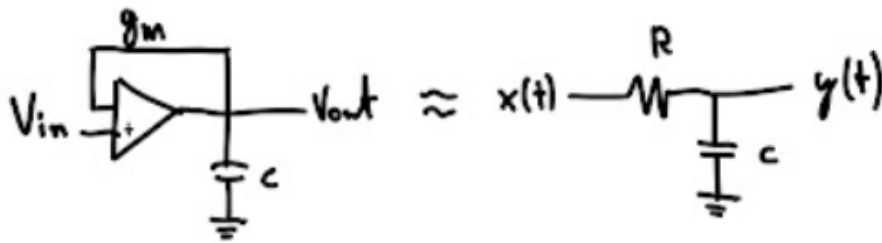$$A = 1[m^2] = 1 \times 10^{-12} [\mu m^2] \tag{9}$$

Plugging everything in we get:

$$C = 8.854 \times 10^{-12} \times 2.2 \times \frac{1 \times 10^{-12}}{10 \times 10^{-9}} = 1.9 [fF/\mu m^2] \tag{10}$$

1. Derive the transfer function $H(s) = \dfrac{V_{out}}{V_{in}}$ for the follower-integrator, using the s-plane

   notation, expressed in terms of complex frequency s and the time constant τ.

Assuming small signal regime:

Follower integrator Transfer Function

1) Apply KCl   current through capacitor $i = C\frac{dV}{dt}$

(1) $i_R = i_C$

$G = \frac{k}{2U_T}$ (2) $I_b \tanh\left(\frac{x(V_{in} - V_{out})}{2U_T}\right) = C\frac{dV_{out}}{dt}$

(2.1) $G(V_{in} - V_{out}) = C\frac{dV_{out}}{dt}$

(3) $\frac{(V_{in} - V_{out})}{R} = C\frac{dV_{out}}{dt}$

(4) $V_{in} - V_{out} = \underset{\tau}{\underbrace{RC}}\frac{dV_{out}}{dt}$

(5) $V_{in} = \tau\frac{dV_{out}}{dt} + V_{out}$

(6) $x(t) = \tau\,\dot{y}(t) + y(t)$

$y(t)$ is of the form: $y(t) = Y e^{-\frac{t}{\tau}}$
$= Y e^{st}$

2) To Laplace domain

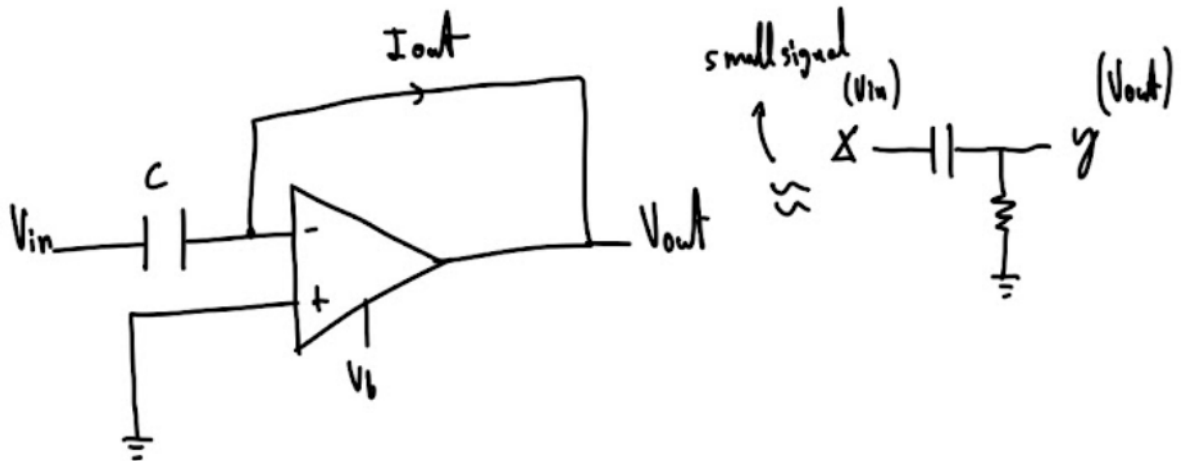s operator $\cong \frac{d(\ldots)}{dt}$ complex freq. domain

(1) $X = \tau \cdot s \cdot Y + Y$

(2) $X = Y(\tau \cdot s + 1)$

(3) $\frac{Y}{X} = \frac{1}{(\tau \cdot s + 1)} = H(s) = \frac{V_{out}}{V_{in}}$

1. Compute the transfer function $H(s) = \dfrac{V_{out}}{V_{in}}$ for the follower-differentiator, using the s-plane notation, expressed in terms of complex frequency s and the time constant τ.

Follower diff. transfer function

1)

(1) $\dot{i}_C = \dot{i}_R$

(2) $C \dfrac{d((V_{in} - V_{out}))}{dt} = G V_{out}$

2) $\tau \dfrac{d((V_{in} - V_{out}))}{dt} = V_{out}$

3) $\tau \, \dot{x} - \dot{y} = y$

4) $\tau s X - \tau s Y = Y$

5) $\tau s X = Y + \tau s Y$

6) $\tau s X = Y (\tau s + 1)$

7) $\dfrac{Y}{X} = \dfrac{\tau s}{(\tau s + 1)} = H(s)$

1. Compute the magnitude $|H(s)|$ for the follower integrator for input angular frequency ω. At what frequency f in Hz does the power drop to half its low frequency value (amplitude drops to $1/\sqrt{2}$)?

$$s = w j$$

2) $H(jw) = \dfrac{1}{1 + jw\tau}$ $j^2 = -1$

$$|H(jw)| = \dfrac{1}{\sqrt{1 + (w\tau)^2}}$$ (8.9.4)

$$= \dfrac{\sqrt{1 + (w\tau)^2}}{(1 + (w\tau)^2)} = \dfrac{1}{\sqrt{2}}$$

→ solve for w

$$\sqrt{2 + (w\tau)^2 \cdot 2} = 1 + (w\tau)^2$$

$$2 + (w\tau)^2 \cdot 2 = \left(1 + (w\tau)^2\right)^2$$

$$2 + 2w^2\tau^2 = 1 + w^4\tau^4 + 2w^2\tau^2$$

$$w^4\tau^4 = 1$$

$$w\tau = 1$$

$$w = \dfrac{1}{\tau}$$

$$\underbrace{F = \dfrac{w}{2\pi} = \dfrac{1}{2\pi\tau}}_{\text{basics}} //$$

(Where F is in Hz)

1. Compare the simple *RC* integrator, constructed from a resistor and a capacitor, and the follower-integrator to show how the transfer function falls short in describing the follower integrator. In particular, how does the follower integrator respond to large signal inputs? This question is related to the next one, which is

When the input to the follower-integrator is large, the output current (from the transconductance amplifier) will saturate at $\pm I_b$ and act as a constant current source (and not as a linear conductance). Also, while $|V_{out} - V_{in}| > 4U_T$, V_{out} is linear (vs time). As small signal regime is entered, V_{out} increases or decreases exponentially (and the amp acts like a linear conductance).

1. What does "small-signal" mean? In other words, what voltage range will this regime correspond to? For the follower-integrator circuit is it the *amplitude* of the input or the output or the *difference* between the two that matters? Why?

Small signal means : $|V_{out} - V_{in}| < 4U_T$ It's the difference between the two that matters, if it is greater than 4UT, the response will be linear instead of exponential

3 Setup

3.1 Connect the device

```
In [ ]:   # import the necessary library to communicate with the hardware
          import pyplane
          import time
          import numpy as np
          import matplotlib.pyplot as plt
```

```
In [ ]:   # create a Plane object and open the communication
          if 'p' not in locals():
              p = pyplane.Plane()
              try:
                  p.open('/dev/ttyACM0')
              except RuntimeError as e:
                  del p
                  print(e)
```

```
In [ ]:   p.get_firmware_version()
```

```
Out[ ]:   (1, 8, 4)
```

```
In [ ]:   # Send a reset signal to the board, check if the LED blinks
          p.reset(pyplane.ResetType.Soft)
          time.sleep(0.5)
          # NOTE: You must send this request events every time you do a reset operetion, otherwi
          # Because the class chip need to do handshake to get the communication correct.
          p.request_events(1)
```

```
In [ ]:   # Try to read something, make sure the chip responses
          p.read_current(pyplane.AdcChannel.GO0_N)
```

```
Out[ ]:   7.250976352679572e-08
```

3.1 Chip configuration

Both circuits we use today uses the same configuration, so we just need to do it once at the beginning.

```
In [ ]:   p.send_coach_events([pyplane.Coach.generate_aerc_event(
              pyplane.Coach.CurrentOutputSelect.SelectLine0,
```

```
        pyplane.Coach.VoltageOutputSelect.SelectLine1,
        pyplane.Coach.VoltageInputSelect.SelectLine2,
        pyplane.Coach.SynapseSelect.NoneSelected,0)])
```

3.2 Bias Generator (BiasGen or BG)

In a simplified form, the output of a branch of the BiasGen will be the gate voltage V_b for the bias current I_b, and if the current mirror has a ratio of w and the bias transistor operates in subthreshold-saturation:

$$I_b = w \frac{BG_{fine}}{256} I_{BG_{master}} \tag{11}$$

Where $I_{BG_{master}}$ is the `BiasGenMasterCurrent` $\in \{60\,\text{pA}, 460\,\text{pA}, 3.8\,\text{nA}, 30\,\text{nA}, 240\,\text{nA}\}$, BG_{fine} is the integer fine value $\in [0, 256)$

To set a bias, use the function similar to the following:

```
    p.send_coach_event(pyplane.Coach.generate_biasgen_event(\

    pyplane.Coach.BiasAddress.BIAS_NAME_STARTS_WITH_THREE_LETTER_CIRCUIT_NAME,
     \
        pyplane.Coach.BiasType.MATCH_LAST_CHAR_OF_BIAS_NAME, \
        pyplane.Coach.BiasGenMasterCurrent.MASTER_CURRENT, FINE_VALUE))
```

4 Follower-integrator (FOI)

4.1 Schematic and pin map
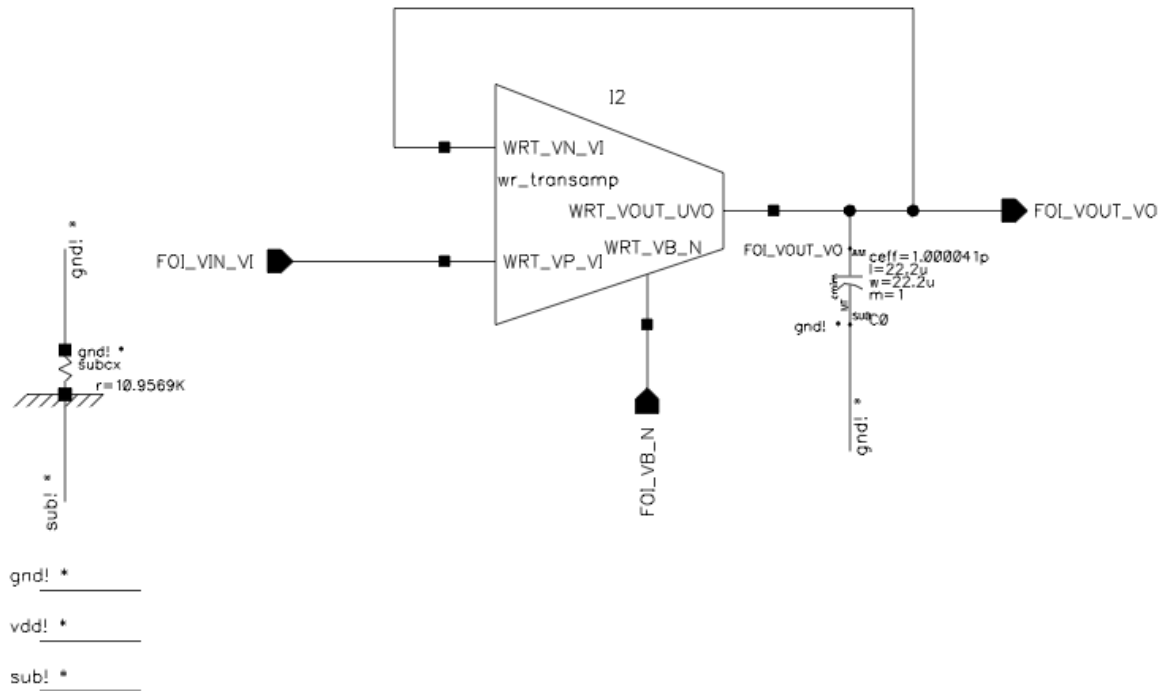
V_{in} = **FOI_VIN_VI = AIN9**

V_{out} = **FOI_VOUT_VO = ADC[10]**

C = **ceff = 1 pF**

- The W/L of the output transistor of the BiasGen is 4u/12u, and the bias transistor M_b of wide-range-transamp is 1u/1u, which give a ratio of w = **3**. This means if you set the bias current to (3.8 nA, 225), you will get $I_b = 3.8 \times \frac{67}{256} \times 3 = 3$ nA instead of 1 nA.

4.2 Time-domain response of small signal

4.2.1 Set parameters

- The maximum sampling frequency of the ADC is 100 kHz (10 μs), and the maximum number of samples is 250. Let's say if we want to cover the time period of 10τ with these 250 samples, what should the value of τ be (assume $\kappa = 1$)?

In order to cover a period of 10τ with n samples, each sample should have the duration

$$\Delta t = \frac{10\tau}{n}.$$

As

$$f = \frac{1}{\Delta t},$$

it can be inferred that

$$\tau = \frac{n}{10f}.$$

Setting $f = 100 \cdot 10^3 \text{Hz}$ and $n = 250$ yields

$$\tau = 250 \cdot 10^{-6}\text{s} = 250\mu\text{s}.$$

- To get this τ, what is the value of I_b?

From the prelab, it is known that

$$\tau = \frac{2U_T}{I_b\kappa}C.$$

With $U_T \approx 25\text{mV}$, $\kappa = 1$, $\tau = 250\mu\text{s}$ and $C = 1\text{pF}$ it follows that

$$I_b = \frac{2U_T}{\tau}C \approx 200\text{pA}.$$

- What `MasterCurrent` and `fine` value should we use for I_b? Please set below.

```
In [ ]:  p.send_coach_events([pyplane.Coach.generate_biasgen_event(
             pyplane.Coach.BiasAddress.FOI_VB_N,
             pyplane.Coach.BiasType.N,
             pyplane.Coach.BiasGenMasterCurrent.I460pA, 37)])
```

For a given value of I_b, it is sensible to choose the next largest value for $I_{BG_{master}}$. In the case of $I_b = 200\text{pA}$, this is

$$I_{BG_{master}} = 460\text{pA}.$$

With $I_b = 200\text{pA}$ and $w = 3$, it can be calculated that

$$BG_{fine} = \frac{256I_b}{wI_{BG_{master}}} \approx 37.10,$$

which has to be rounded down to

$$BG_{fine} \approx 37$$

as BG_{fine} must be an integer. This yields the bias current

$$I_b = w\frac{BG_{fine}}{256}I_{BG_{master}} \approx 199.5\text{pA}.$$

- What should the input before the step $(V_{in}(t < 0))$ be (Hint: common-mode voltage of the (wide-range) transamp)? What is the corresponding V_{out} in steady state $(V_{out}(t = 0^-))$?

```
In [ ]:  Vi_pre = 0.9
```

In stead state, the value of V_{in} should be high enough that the transamp is capable of operating, e.g.

$$V_{in}(t < 0) = 0.9\text{V}.$$

The value of the output voltage will then also be

$$V_{out}(t < 0) = 0.9\text{V}.$$

- What does "small signal" mean? What should the magnitude of the step input (ΔV_{in}) be so that it can be treated as "small"?

```
In [ ]:  dVi = 0.09
```

In the prelab, it was determined that small signals can be assumed if the change between the input and output voltage is limited to approximately[1] $|V_{out} - V_{in}| = |\Delta V_{in}| < 4U_T$ over one time interval τ

$$\frac{d\,|\Delta V_{in}|}{d\tau} < 4U_T \approx 100\text{mV}.$$

As a step response is considered, the change $|\Delta V_{in}|$ occurs instantaneously, and the above condition can simply be written as

$$|\Delta V_{in}| < 4U_T \approx 100\text{mV}.$$

Thus, it is sensible to set e.g. $|\Delta V_{in}| \approx 90\text{mV}$.

[1] For $\kappa \approx 1$.

4.2.2 Data aquisition

- Verify the steady state

```
In [ ]:  p.set_voltage(pyplane.DacChannel.AIN9, Vi_pre)
         time.sleep(0.5) # wait for the circuit to reach steady state
         Vo_pre = p.read_voltage(pyplane.AdcChannel.AOUT10)
         print(Vo_pre)
```

0.885424792766571

- There is a small offset of the DAC of around -10 mV, but the ADC is correct, so we want to correct `Vi_pre` accordingly.

Ignore offset as setting resolution of the DAC isn't good enough to compensate the offset.

```
In [ ]:  Vi_off = Vo_pre - Vi_pre
         Vi_pre_corr = Vi_pre - Vi_off
         print(Vi_pre_corr)
```

```
0.914575207233429
```

- Acquire the step response.

In [ ]:
```python
p.set_bit_depth(pyplane.BitDepth(10))
```

In [ ]:
```python
Vout_ex_4_2_2 = np.zeros(511)
for _ in range(10):
    p.set_voltage(pyplane.DacChannel.AIN9, Vi_pre_corr)
    time.sleep(0.5) # wait for the circuit to reach steady state
    Vout_ex_4_2_2 = Vout_ex_4_2_2 + p.acquire_transient_response(pyplane.DacChannel.AI
    time.sleep(0.5) # wait to receive the measured data from the microcontroller

Vout_ex_4_2_2 /= 10
print(Vout_ex_4_2_2)
```

```
[0.90210205 0.90000733 0.90548584 0.9075      0.90685546 0.91056153
 0.91362305 0.9187793  0.92369385 0.92530517 0.92546631 0.928125
 0.93134766 0.93497315 0.94037109 0.94182129 0.94085449 0.94383544
 0.94649414 0.95020019 0.95390626 0.95664551 0.95463135 0.95583985
 0.95825683 0.96010986 0.96421875 0.96679688 0.96446046 0.96510498
 0.96663574 0.96800538 0.97163087 0.97404786 0.971792   0.97114747
 0.97235596 0.97372558 0.97638428 0.97928466 0.97759277 0.97525635
 0.97630371 0.97654541 0.97968751 0.98258789 0.98170166 0.97791504
 0.9792041  0.9799292  0.98170167 0.98347412 0.98347412 0.9794458
 0.98041259 0.98154052 0.98202392 0.98500488 0.98492432 0.98089599
 0.98170166 0.98258789 0.98315185 0.98556885 0.98572999 0.98258788
 0.98242676 0.98307129 0.98387696 0.98597167 0.98645508 0.98339355
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 0.98742188 0.98339356 0.98355468 0.98436036 0.98524657 0.98669677
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 0.98355468 0.98395752 0.98500488 0.98701904 0.98871094 0.9835547
 0.98363525 0.98379639 0.98484375 0.98718019 0.9890332  0.98452148
```

```
       0.98379638 0.98339356 0.98436034 0.98701904 0.98887207 0.98508545
       0.98379638 0.98371583 0.98444091 0.98645508 0.98863037 0.98492431
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       0.98653564 0.98355469 0.98339356 0.98363525 0.98581054 0.98814697
       0.98718018 0.98436036 0.98315185 0.98387696 0.98500488 0.98774413
       0.98822754 0.98355469 0.98291015 0.98387695 0.98564942 0.98758301
       0.9883081  0.98436036 0.98291015 0.98347413 0.98484374 0.98701905
       0.9890332  0.98476319 0.98299072 0.98331299 0.98492431 0.98701904
       0.98863037 0.98444092 0.98258789 0.98339356 0.98468262 0.98645509
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       0.9885498  0.98645508 0.98363525 0.98323242 0.98427978 0.98637452
       0.9885498  0.98661621 0.98355468 0.98331299 0.98379639 0.98629395
       0.98822755 0.98734131 0.98427979 0.98299072 0.98403808 0.98556886
       0.98790526 0.98750243 0.98403809 0.98266845 0.98379639 0.98556886
       0.98774413 0.9885498  0.98476318 0.98250732 0.98371583 0.98532715
       0.98750244 0.98911377 0.98532715 0.98250732 0.98444092 0.98419922
       0.98669678 0.98927491 0.98468261 0.98258789 0.98379639 0.98460205
       0.98669677]
```

- plot V_{out}

```python
In [ ]:  import matplotlib.pyplot as plt
         import numpy as np

         Vout_ex_4_2_2 = np.loadtxt('data_ex_4_2_2.csv', delimiter=',')
         t=np.arange(0,len(Vout_ex_4_2_2))*0.00002
         plt.plot(t[:100], Vout_ex_4_2_2[:100])
         plt.xlabel('$t$ [s]')
         plt.ylabel('$V_{out}$ [V]')
         plt.legend(['$V_{out}$'],prop={'size': 14})
         plt.title('Fig. 1: Step response of the follower-integrator for small signals.')
         plt.grid()
         plt.show()
```

Fig. 1: Step response of the follower-integrator for small signals.
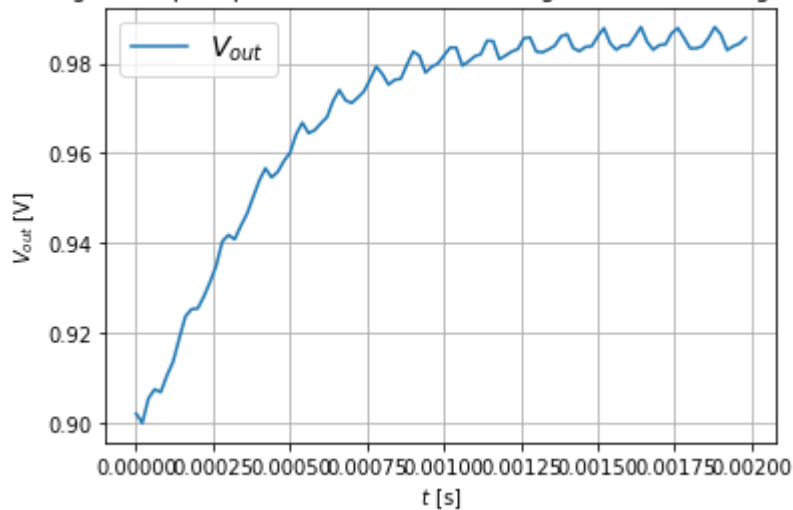
- save data

```
In [ ]:   # if the data looks nice, save it!
          data_ex_4_2_2= [Vout_ex_4_2_2]
          # save to csv file
          np.savetxt('data_ex_4_2_2.csv', data_ex_4_2_2, delimiter=',')
```

4.2.3 Compute the time constant τ by reading from the decay in the curve

- Assume the input step happens at exactly $t = 0$, what is the expression of V_{out} at $t = \tau$?

In the prelab, the transfer function of the follower-integrator was derived to be

$$H(s) = \frac{V_{out}}{V_{in}} = \frac{1}{\tau s + 1}.$$

It is known that the unit step can be described in the s-plane as

$$U(s) = \frac{\Delta V_{in}}{s}.$$

The unit step response can thus be evaluated to

$$V(s) = H(s)U(s) = \frac{\Delta V_{in}}{s(\tau s + 1)}.$$ Transformed back into the time domain, the output voltage

can now be described by

$$V_{out}(t) = \Delta V_{in} \left(1 - e^{-\frac{t}{\tau}} \right) + V_{out}(t = 0).$$

Thus

$$V_{out}(t = \tau) = ?$$

- Compute τ by "reading" this point from the curve:

```
In [ ]:  import scipy.interpolate as interpolate

         f = interpolate.interp1d(Vout_ex_4_2_2,t) # Interpolate t vs. Vout
         v_1tau = dVi*(1-np.exp(-1))+Vout_ex_4_2_2[0]
         tau1 = f(v_1tau) # Get tau = t(Vout=1-e^(-1))
         print(tau1)
```

0.0004879444451121427

- Compute the actual κ by comparing the measured τ and the estimated value in 4.2.1 with $\kappa = 1$.

```
In [ ]:  Ibe = 200
         Ibm = 199.5
         taue = 250
         taum = tau1*10**6
         kappa1 = (taue*Ibe)/(taum*Ibm)
         print(kappa1)
```

0.5136375030531178

4.2.5 Analysis

- Is the "small signal" assumption validated by the measurement? Why or why not?

- Is the assumption "input step happens at exactly $t = 0$" validated by the measurement? How can you get the actual time it takes place?

4.3 Time-domain response of large signal

4.3.1 Set parameters

- What does "large signal" mean? What should the magnitude of the step input (ΔV_{in}) be so that it can be treated as "large"?

```
In [ ]:  dVi = 0.3
```

Large signal behavior can be assumed when $|V_{in} - V_{out}| > 4U_T$, which translates to the step input

$$|\Delta V_{in}| > 4U_T \approx 100\text{mV}.$$

Thus, it is reasonable to assumed that e.g. $\Delta V_{in} = 300\text{mV}$.

- What should the input before the step $(V_{in}(t < 0))$ be (Hint: common-mode voltage of the (wide-range) transamp)? What is the corresponding V_{out} in steady state $\left(V_{out}(t = 0^-)\right)$?

In [ ]: `Vi_pre = 0.9`

In stead state, the value of V_{in} should be high enough that the transamp is capable of operating, e.g.

$$V_{in}(t < 0) = 0.9\text{V}.$$

The value of the output voltage will then also be

$$V_{out}(t < 0) = 0.9\text{V}.$$

- Let's still set ADC sampling rate as 100 kHz (10 μs), and the maximum number of samples as 250. If we want the linear part to be about 40% of the whole sampled period, what should the slew rate be (Hint: in [V/s])?

Assuming that $f = 100 \cdot 10^3 \text{Hz}$ and that $n = 250$, the total sample period will be

$$t_{tot} = \frac{n}{f} = 2.5 \cdot 10^{-3}\text{s} = 2.5\text{ms}.$$

Therefore, the linear part should be approximately

$$t_{lin} = 0.4 t_{tot} = 1\text{ms}$$

long.

The slew rate limits the circuit as long as the $\tanh$ in I_{out} is more or less saturated. In terms of $V_{in} - V_{out}$, this is given when the extrapolated linear section of I_{out} intercepts I_b. Since the linear section of I_{out} is given by

$$I_{out} = g_m\left(V_{in} - V_{out}\right) = I_b \frac{\kappa}{2U_T}\left(V_{in} - V_{out}\right).$$

Thus (assuming that $\kappa = 1$)

$$V_{in} - V_{out} = \frac{2U_T}{\kappa} = 50\text{mV}$$

when the circuit is no longer limited by the slew rate. Since $\Delta V_{in} = 300\text{mV}$, the corresponding change in output voltag thus has to reach

$$\Delta V_{out} = 250\text{mV}.$$

The corresponding slew rate would thus be

$$sr = \frac{\Delta V_{out}}{t_{lin}} = \frac{250\text{mV}}{1\text{ms}} = 250\frac{V}{s}.$$

- To get this slew rate, what is the value of I_b (Hint: C = 1 pF)?

For large signals, the output current of the transamp is saturated and the transamp can thus be treated as a constant current source with $I_{out} = I_b$. This current now charges the capacitor linearly. With $t_{lin} = 1\text{ms}$, $\Delta V_{out,lin} = 250\text{mV}$ and $C = 1\text{pF}$, it can be determined what value I_b must have in order to achieve the slew rate determined in the previous exercise

$$I_{out} = I_b = \frac{C}{t_{lin}}\Delta V_{out,lin} = C \cdot sr = 250\text{pA}.$$

- What `MasterCurrent` and `fine` value should we use for I_b? Please set below.

```
In [ ]: p.send_coach_events([pyplane.Coach.generate_biasgen_event(
            pyplane.Coach.BiasAddress.FOI_VB_N,
            pyplane.Coach.BiasType.N,
            pyplane.Coach.BiasGenMasterCurrent.I460pA, 46)])
```

For a given value of I_b, it is sensible to choose the next largest value for $I_{BG_{master}}$. In the case of $I_b = 250\text{pA}$, this is

$$I_{BG_{master}} = 460\text{pA}.$$

With $I_b = 250\text{pA}$ and $w = 3$, it can be calculated that

$$BG_{fine} = \frac{256 I_b}{w I_{BG_{master}}} \approx 46.38,$$

which has to be rounded down to

$$BG_{fine} \approx 46$$

as BG_{fine} must be an integer. This yields the bias current

$$I_b = w\frac{BG_{fine}}{256}I_{BG_{master}} \approx 248.0\text{pA}.$$

4.3.2 Data aquisition

- Verify the steady state

```
In [ ]:
```

- There is a small offset of the DAC of around -10 mV, but the ADC is correct, so we want to correct the DAC accordingly.

In [ ]:

- Acquire the step response

In [ ]:

- plot V_{out}

In [ ]:

- save data

In [ ]: `# if the data looks nice, save it!`

`# save to csv file`

4.3.3 Data processing

- Compute the slew rate by fitting the linear part of the curve

In [ ]:

- Calculate the bias current I_b from the slew rate and compare it with the set value. Comment on possible reason of any discrepancy.

In [ ]:

- Compute the time constant τ and the κ by fitting the exponential part of the curve. Do they make sense?

In [ ]:

4.3.4 Analysis

- Is the assumption "input step happens at exactly $t = 0$" validated by the measurement? How can you get the actual time it takes place?

- Could you give a marginal value of ΔV_{in} between "small" and large signal"? (Hint: assume for the transamp, $I_{out} \propto (V_1 - V_2)$ if $g_m |V_1 - V_2| < I_b$)

4.4 Frequency-domain response

4.4.1 Methodology

In the prelab we have computed the magnitude of the transfer function $H(s)$. In this exercise we are going to measure this curve.

- What will the output signal V_{out} look like if the input V_{in} is a sine wave in steady state (after several cycles)?

The output signal will be phase shifted and scaled depending on the frequency of the input sine wave.

- In order to make the measurement more accurate, we set I_b to minimum (about 0.7 pA):

```
In [ ]:  p.send_coach_events([pyplane.Coach.generate_biasgen_event(
             pyplane.Coach.BiasAddress.FOI_VB_N,
             pyplane.Coach.BiasType.N,
             pyplane.Coach.BiasGenMasterCurrent.I60pA,100)])
```

- With this I_b, what is the cutoff frequency approximately?

It is known from the prelab that at the cutoff frequency,

$$\omega = 2\pi f = \frac{1}{\tau}.$$

As

$$\tau = \frac{C}{g_m} = \frac{\kappa}{2I_b U_T} C$$

the cut of frequency can thus be calculated (using $\kappa = 0.9616$ as determined in exercise 4.2.3)

$$f_c = I_b \frac{\kappa}{4\pi U_T C} \approx 2.152 \text{Hz}.$$

4.4.2 Observe the input and output waveforms

- set a sine wave
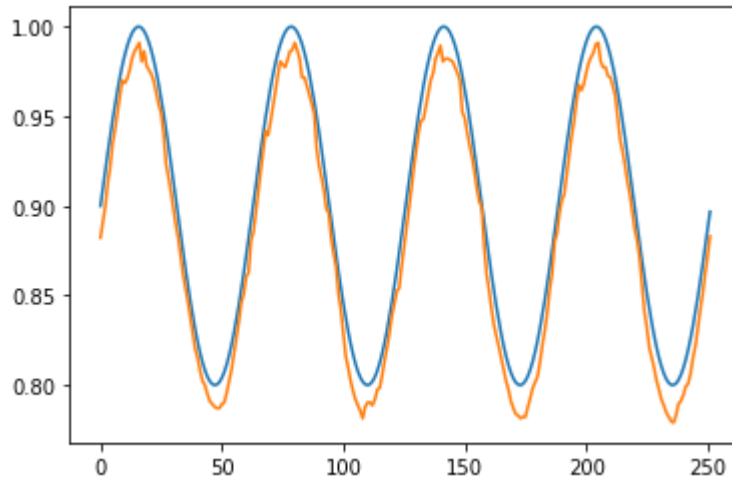
```
In [ ]:  wf = np.sin(np.arange(0, 8*3.14, 0.1))
```

- set the input voltage (offset of the input sine wave is 0.9, amplitude of the input sine wave is 0.1)

In [ ]: `p.set_voltage_waveform(0.9 + 0.1 * wf)`

- Plot V_{in} and V_{out} in the same figure

In [ ]:
```
plt.plot(0.9 + 0.1 * wf)
Vout_ex_4_4_2 = p.acquire_waveform(pyplane.DacChannel.AIN9, pyplane.AdcChannel.AOUT10,
plt.plot(Vout_ex_4_4_2)
```

Out[ ]: `[<matplotlib.lines.Line2D at 0x7ffab7747670>]`

- Save data

In [ ]:
```
# if the data looks nice, save it!
# save to csv file
```

- Is the circuit "following" or "integrating"?

- Can you change the frequency to make it operate in the other regime and plot V_{in} and V_{out} in the same figure

In [ ]:

- Save data

In [ ]:
```
# if the data looks nice, save it!
# save to csv file
```

- How to compute the transfer function at one frequency and extract the cutoff frequenc? Can you briefly give the basic method? (Optional)