

Diogo Aguiam

Systems Integration Engineer

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EXPERIENCE

Microwave Diagnostics Developer - Invited Researcher

Max-Planck-Institut für Plasmaphysik

November 2015 – Ongoing Garching, Germany

- Installation and commissioning of microwave reflectometry diagnostics for Nuclear Fusion research
- Development of density profile reconstruction codes for distributed computing cluster

Research Engineer, PhD Student

Instituto de Plasmas e Fusão Nuclear

February 2013 – Ongoing Lisbon, Portugal

- Design and assembly of RF electronics systems
- Testing and validation of new microwave diagnostics

LabVIEW Developer

Department of Physics, Instituto Superior Técnico

February 2012 – July 2012 Oeiras, Portugal

- Development of LabVIEW interfaces for physics laboratories

Ciência Viva Monitor

INESC-ID Lisboa

June 2011 Oeiras, Portugal

- Teaching electronics and PCB fabrication to high school students

TRAINING

Radiation Safety Instruction

Max-Planck-Institut für Plasmaphysik

2015, 2016, 2017 Garching, Germany

- Working in a nuclear fusion research facility including in-vessel access

Nanotechnologies - Athens Programme

École Nationale Supérieure de Techniques Avancées

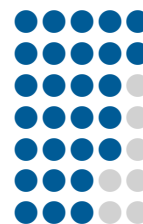
November 2011 Paris, France

- Fundamentals of nano-structures, nanophotonics, spintronics and microfabrication techniques

Hardware and signal processing engineer with a passion for building complex new systems from scratch

TECHNICAL SKILLS

C/C++
Python
PCB Design
Signal Processing
Software Engineering
MATLAB
RF



Experience with:

Altium Embedded Systems ADS
LabVIEW Cadence Xilinx FPGA
Solidworks Git Neural Networks

LANGUAGES

Portuguese
English (C2)
French (B1)
German (B1)



EDUCATION

Double Ph.D. in Technological Physics Engineering

University of Ghent & Universidade de Lisboa

2014 – 2018

Thesis: Implementation of a X-mode multichannel edge density profile reflectometer for the new ICRH antenna on ASDEX Upgrade

M.Sc. in Electronics Engineering

Instituto Superior Técnico, U. Lisboa

2011 – 2013

Thesis: Heterodyning Non Destructive Testing Electronic System Based on Eddy Currents

B.Sc. in Electronics Engineering

Instituto Superior Técnico, U. Lisboa

2008 – 2011

Best in class scholarships for three years

PUBLICATIONS