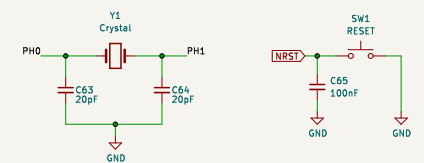


The image displays three separate pin connection diagrams for different interfaces:

- CAN:** Shows a 4-pin connector (J27) with pins 4 (VDD), 3 (CAN_TX), 2 (CAN_RX), and 1 (GND). The corresponding PCB pins are P14 (CAN_TX) and P10 (CAN_RX).
- I2S:** Shows a 6-pin connector (J28) with pins 6 (VDD), 5 (I2S2_WS), 4 (I2S2_CK), 3 (I2S2_eS1D), 2 (I2S2_S1), and 1 (GND). The corresponding PCB pins are P10 (I2S2_WS), P11 (I2S2_CK), P12 (I2S2_eS1D), and P13 (I2S2_S1).
- USB:** Shows a 5-pin connector (J20) with pins 5 (USB_B_Micro), 4 (VBUS), 3 (+5V +3V3), 2 (D-), and 1 (D+). The corresponding PCB pins are PA12 (D-), PA11 (D+), PA17 (VBUS), PA15 (GND), and PA14 (GND). Resistor values are specified: R5B (10R) for VBUS, R61 (1K) for D-, and R59 (22R) for D+.



J14
Conn_02x05_Odd_Even

VDD 1 2 GND
PC4 3 4 P0
PD5 5 6 PD1
P8 7 8 PD4
VDD 9 10 GND

J15
Conn_02x05_Odd_Even

VDD 1 2 GND
P0 3 4 PG11
PD7 5 6 PG12
P8 7 8 PG13
VDD 9 10 PG14

J16
Conn_02x05_Odd_Even

VDD 1 2 GND
PG15 3 4 P8
P8 5 6 P8
P8 7 8 P8
VDD 9 10 GND

J17
Conn_02x05_Odd_Even

VDD 1 2 GND
PC4 3 4 PC4
PC4 5 6 PC4
PC4 7 8 PC4
VDD 9 10 GND

J22
Conn_02x05_Odd_Even

VDD 1 2 GND
PC4 3 4 PC5
P8 5 6 P8
P8 7 8 P8
VDD 9 10 GND

J23
Conn_02x05_Odd_Even

VDD 1 2 GND
P11 3 4 P17
P17 5 6 P17
P17 7 8 P17
VDD 9 10 GND

J24
Conn_02x05_Odd_Even

VDD 1 2 GND
P11 3 4 P17
P17 5 6 P17
P17 7 8 P17
VDD 9 10 GND

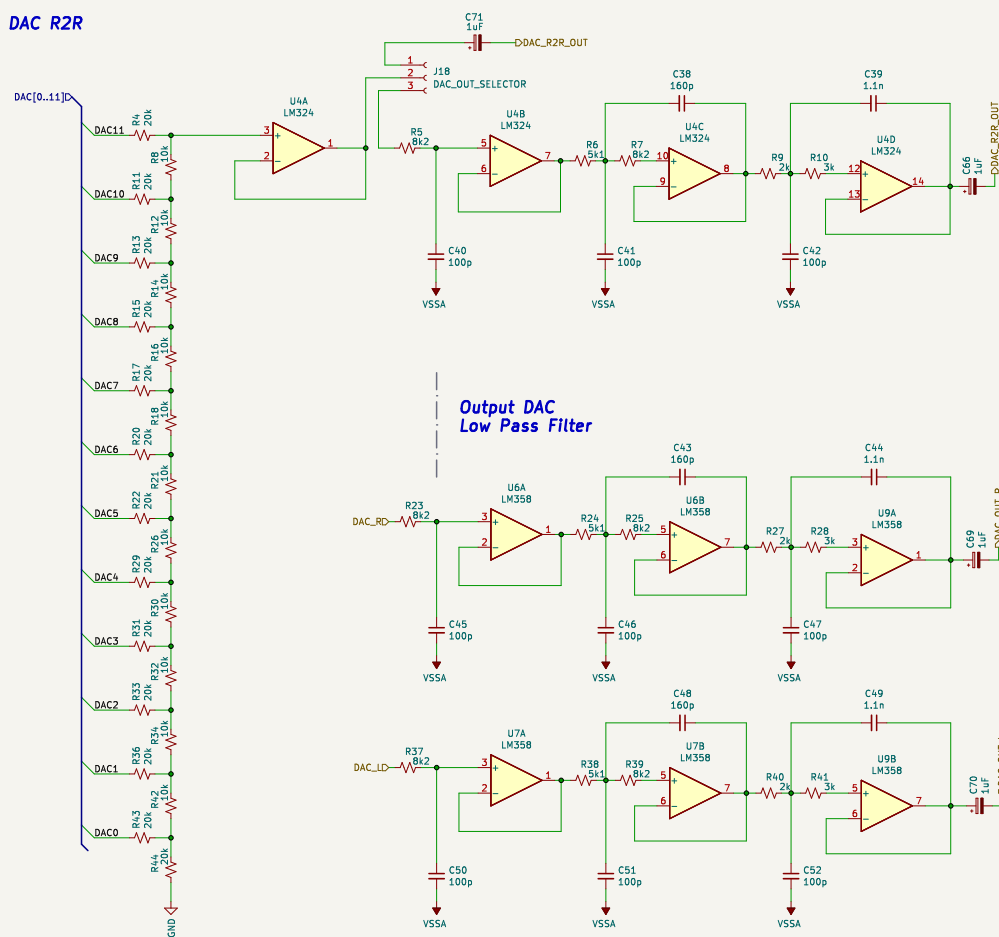
J25
Conn_02x05_Odd_Even

VDD 1 2 GND
P11 3 4 P17
P17 5 6 P17
P17 7 8 P17
VDD 9 10 GND

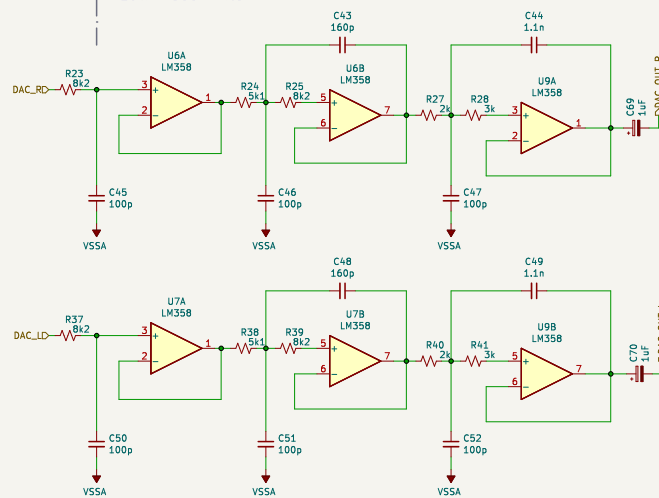
J29
Conn_02x05_Odd_Even

VDD 1 2 GND
P11 3 4 P17
P17 5 6 P17
P17 7 8 P17
VDD 9 10 GND

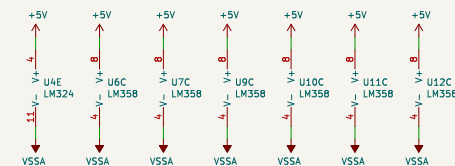
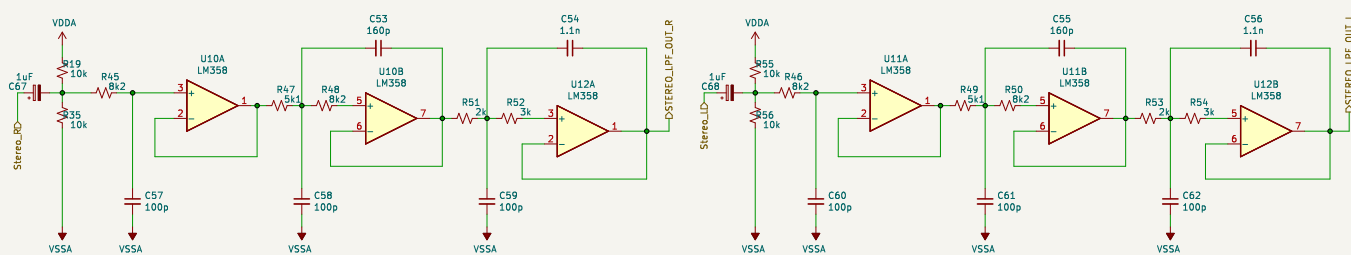
DAC R2R



Output DAC Low Pass Filter



Input ADC Low Pass Filter



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Sheet:

File: analgo.kicad_sch

Title: User Interface DSP Board

Size: A3 Date: 2023-06-30

KiCad E.D.A. kicad 7.0.1



Rev: d0.3.1
Id: 3/2