Weekly Project Status Report

From: Pratheep Joe Siluvai
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Project: Design and Verification of Multi-Channel ADPCM CODEC

Project Overview
The class design project is to design a complex system design of a Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

Work Completed
02/29/2014: I perceived the background information about the design project by gaining some knowledge from the web about ADPCM, its working and implementation. (45 minutes)
02/30/2014: With a good insight about the working of the ADPCM, the core architecture of the MCAC was studied. (30 minutes)
02/31/2014: After gathering an outline of the system, I tried to interpret the design specification by analyzing the G.726 and G.711 spec sheets from ITU. (60 minutes)
02/02/2014: Later towards the weekend I followed the EDA tutorial to access the sample project database from git repository. While cloning the database files to the remote server, I faced a public key access issue, which was later resolved by adding SSH key to git account. (45 minutes)
02/02/2014: Brushed up through the Verilog tutorials. (60 minutes)
02/03/2014: Project groups where created based on everyone's skillset. I happened to be a part of the C model group. (10 minutes)

Work Awaiting Completion
02/05/2014: The group will discuss about the project database, its organization and structure.
02/06/2014: After the study of the database structure, the group will be assigned with specific system components for each member to start working on the development of the RTL database.

Complications
The project had a good start and it is going on smoothly.