Weekly Project Status Report

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Date: 04/22/2014
Project: Design and Verification of Multi-Channel ADPCM CODEC

Project Overview
The class design project is to design a complex system design of a Bit-Serial Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

Work Completed
04/10/2014: All lower level modules synthesizes, their areas and time delays were noted.
04/20/2014: Worked on encoder rtl code and test bench. Made encoder rtl code to simulate and synthesis. Further worked on the test bench the output was not right for most of the cases though. Looks like the output oscillates for every other sample between two adjacent values.

Work Awaiting Completion
04/24/2014: Make encoder/decoder to pass all vector test.
04/25/2014: Work on the mcac module.

Complications
Understanding the control unit and the clock generator for the encoder was crucial.