

# Weekly Project Status Report

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**Project:** Design and Verification of Multi-Channel ADPCM CODEC

## **Project Overview**

The class design project is to design a complex system design of a Bit-Serial Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

## **Work Completed**

04/03/2014: FMULT/ACCUM module synthesis and passes vector tests.

04/04/2014: Working and helping teammates to complete other hierarchical blocks.

04/07/2014: Encoder and Decoder modules were created and made to synthesis. Corrected some errors. Waiting for other modules to be completed so that the ENC and DEC modules can be verified.

## **Work Awaiting Completion**

04/10/2014: Lower level modules have to be completed and tested so that upper level modules can be tested.

04/11/2014: Make all modules to be vector test verified for bit-serial implementation.

## **Complications**

Since lower hierarchical modules weren't tested its difficult to proceed on and work on the higher level modules.