Weekly Project Status Report

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Project: Design and Verification of Multi-Channel ADPCM CODEC

Project Overview
The class design project is to design a complex system design of a Bit-Serial Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

Work Completed
03/19/2014: Three different architectural proposals were made and the class was divided into 3 groups. I was into the bit-serial implementation group.
03/20/2014: Separate projects for the three implementations were created along with hierarchical modules.
03/21/2014: Started working on the bit-serial implementation of the FMULT module along with David.
03/30/2014: Made all lower level modules to work and pass the test vectors. I worked on the EXPAND and G711 for vector verification. EXPAND passed all the test vectors after G711 was fixed by Levs.
03/31/2014: Worked on the TON_TRAN_DET module, created rtl and vector test bench. Verified to pass all the test vectors.

Work Awaiting Completion
04/03/2014: Work on the FMULT, FMULT/ACCUM and verify it.
04/04/2014: Work on the ENC and DEC module to create rtl code as well as vector test bench and verify it.

Complications
Some of the rtl code for low level modules were created with logical errors, hence implementation of upper level modules got delayed.