

Weekly Project Status Report

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Project: Design and Verification of Multi-Channel ADPCM CODEC

Project Overview

The class design project is to design a complex system design of a Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

Work Completed

02/07/2014: The ANSI standard C model was studied and individual modules were assigned for each member in the project group, which is updated in the module roster.

02/08/2014: I then analyzed the functions in my modules and gathered the changes required to be done as per the ITU standard and the status was updated in the module roster. (30 minutes)

02/08/2014: Discussed with RTL Design team regarding their implementation of the same module and gathered the requirements so as to match both RTL and C model design. (60 minutes)

02/09/2014: Later towards the weekend I continued going through the Verilog tutorials. (45 minutes)

Work Awaiting Completion

02/14/2014: Convert each modules assigned to ITU Standards as per the specifications in G.726 spec file.

02/16/2014: Work on the C model to get identical results for a-law and u-law.

Complications

Figuring out how to handle incoming data in each module for different bit-rate.