Weekly Project Status Report

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Project: Design and Verification of Multi-Channel ADPCM CODEC

Project Overview
The class design project is to design a complex system design of a Multi-Channel Adaptive Differential Pulse Code Modulator and Demodulator (MCAC) compatible with G.726 & G.711 Specifications.

Work Completed
02/12/2014: Six modules where assigned for me which includes, adda.c, addb.c, addc.c, functf.c, functw.c, synch.c. Each module was studied and the changes required for each module in ITU standard was analyzed.
02/13/2014: The first 3 modules where modified to handle 16-bit data and then pushed back to the repository by upending the revision history.
02/14/2014: The functf.c and functw.c involves the bit-rate, hence both the modules where modified to handle all the four different bit-rate using switch cases.
02/15/2014: The final module synch.c again had to handle different bit rates, the required modification where done and four different rate variables where included in this module.
02/16/2014: The enc.c and dec.c was modified in the function calls (my module functions) to include the rate variable parameter.
02/17/2014: The whole c-model was compiled and executed for its correctness. Final committed files are yet to be pushed.

Work Awaiting Completion
02/19/2014: Push all my files and check for the working of the MCAC system's C-model design for ITU standards.
02/20/2014: Work on the C model to get identical results for a-law and u-law.

Complications
Had no difficulties faced so far, since the changes where much simpler.