NOP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 指令格式 | NOP | | | | | | | | | | | | | | | |
| 指令功能 | 空操作 | | | | | | | | | | | | | | | |
| 功能说明 | 什么也不做 | | | | | | | | | | | | | | | |

R型

ADDU

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 0 | rx | | | ry | | | rz | | | 0 | 1 |
| 指令格式 | ADDU rx ry rz | | | | | | | | | | | | | | | |
| 指令功能 | R[z]←R[x] + R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx与寄存器ry的值求和，结果保存到寄存器rz中 | | | | | | | | | | | | | | | |

SUBU

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 0 | rx | | | ry | | | rz | | | 1 | 1 |
| 指令格式 | SUBU rx ry rz | | | | | | | | | | | | | | | |
| 指令功能 | R[z]←R[x] - R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 用寄存器rx的值减去寄存器ry的值，结果保存到寄存器rz中 | | | | | | | | | | | | | | | |

AND

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 1 | 1 | 0 | 0 |
| 指令格式 | AND rx ry | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←R[x] & R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx的值与寄存器ry的值逐位逻辑与，结果保存到寄存器rx中 | | | | | | | | | | | | | | | |

OR

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 1 | 1 | 0 | 1 |
| 指令格式 | OR rx ry | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←R[x] | R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx和寄存器ry的值求逻辑或后赋给寄存器rx | | | | | | | | | | | | | | | |

NOT

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 1 | 1 | 1 | 1 |
| 指令格式 | NOT rx ry | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←~ R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器ry的值求逻辑反后赋给寄存器rx | | | | | | | | | | | | | | | |

CMP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 1 | 0 | 1 | 0 |
| 指令格式 | CMP rx ry | | | | | | | | | | | | | | | |
| 指令功能 | If ( R[x] = R[y] ) then T←0  If ( R[x] != R[y] ) then T←1 | | | | | | | | | | | | | | | |
| 功能说明 | 比较rx和ry的值，若相等，则给标志寄存器T置0；否则置1 | | | | | | | | | | | | | | | |

SLTU

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 0 | 0 | 1 | 1 |
| 指令格式 | SLTU rx ry | | | | | | | | | | | | | | | |
| 指令功能 | If ( R[x] < R[y] ) then T←1 (无符号比较)  If ( R[x] >= R[y] ) then T←0 (无符号比较) | | | | | | | | | | | | | | | |
| 功能说明 | 无符号比较寄存器rx的值和ry的值并根据结果对标志寄存器T赋值 | | | | | | | | | | | | | | | |

SLL

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 1 | 1 | 0 | rx | | | ry | | | Immediate | | | 0 | 0 |
| 指令格式 | SLL rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( immediate = 0 ) then R[x]←R[y] << 8  If ( immediate != 0 ) then R[x]←R[y] << immediate(unsigned) | | | | | | | | | | | | | | | |
| 功能说明 | 对寄存器ry的值逻辑左移immediate位后赋值给寄存器rx（注意：immediate为无符号数，当其为0时，左移8位） | | | | | | | | | | | | | | | |

SRA

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 1 | 1 | 0 | rx | | | ry | | | Immediate | | | 1 | 1 |
| 指令格式 | SRA rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( immediate = 0 ) then R[x]←R[y] >> 8 (arithmetic)  If ( immediate != 0 ) then R[x]←R[y] >> immediate(arithmetic) | | | | | | | | | | | | | | | |
| 功能说明 | 对寄存器ry的值算术右移immediate位后赋值给寄存器rx（注意：immediate为无符号数，当其为0时，右移8位） | | | | | | | | | | | | | | | |

SRAV

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | ry | | | 0 | 0 | 1 | 1 | 1 |
| 指令格式 | SRAV rx ry | | | | | | | | | | | | | | | |
| 指令功能 | R[y]←R[y] >> R[x] (arithmetic) | | | | | | | | | | | | | | | |
| 功能说明 | 对寄存器ry的值算数右移R[x]位后赋给寄存器ry | | | | | | | | | | | | | | | |

SRL

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 1 | 1 | 0 | rx | | | ry | | | Immediate | | | 1 | 0 |
| 指令格式 | SRL rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( immediate = 0 ) then R[x]←R[y] >> 8 (logical)  If ( immediate != 0 ) then R[x]←R[y] >> immediate(logical) | | | | | | | | | | | | | | | |
| 功能说明 | 对寄存器ry的值逻辑右移immediate位后赋值给寄存器rx（注意：immediate为无符号数，当其为0时，右移8位） | | | | | | | | | | | | | | | |

MFPC

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 |
| 指令格式 | MFPC rx | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←PC | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器PC的值赋给寄存器rx | | | | | | | | | | | | | | | |

MFIH

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 1 | 0 | rx | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 指令格式 | MFIH rx | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←IH | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器IH的值赋给寄存器rx | | | | | | | | | | | | | | | |

MTIH

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 1 | 0 | rx | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 |
| 指令格式 | MTIH rx | | | | | | | | | | | | | | | |
| 指令功能 | IH←R[x] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx的值赋给寄存器IH | | | | | | | | | | | | | | | |

MTSP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 1 | 0 | 0 | 1 | 0 | 0 | rx | | | 0 | 0 | 0 | 0 | 0 |
| 指令格式 | MTSP rx | | | | | | | | | | | | | | | |
| 指令功能 | SP←R[x] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx的值赋给寄存器SP | | | | | | | | | | | | | | | |

I型

ADDIU3

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 0 | 0 | 0 | rx | | | ry | | | 0 | Immediate | | | |
| 指令格式 | ADDIU3 rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | R[y]←R[x] + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 将立即数immediate进行符号扩展后与寄存器rx的值求和，结果保存到寄存器ry中 | | | | | | | | | | | | | | | |

LW

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 0 | 0 | 1 | 1 | rx | | | ry | | | Immediate | | | | |
| 指令格式 | LW rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | R[y]←MEM[R[x] + Sign\_extend(immediate)] | | | | | | | | | | | | | | | |
| 功能说明 | 从内存中读取数据到寄存器ry中，内存地址为寄存器rx的内容与立即数(进行符号扩展后)immediate之和 | | | | | | | | | | | | | | | |

SW

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 0 | 1 | 1 | rx | | | ry | | | Immediate | | | | |
| 指令格式 | SW rx ry immediate | | | | | | | | | | | | | | | |
| 指令功能 | MEM[R[x] + Sign\_extend(immediate)]←R[y] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器ry的值写入到内存中，内存地址为寄存器rx的内容与立即数（进行符号扩展之后）immediate之和 | | | | | | | | | | | | | | | |

ADDIU

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 0 | 0 | 1 | rx | | | Immediate | | | | | | | |
| 指令格式 | ADDIU rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←R[x] + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 对立即数immediate进行符号扩展之后与寄存器rx的值求和，结果保存到寄存器rx中 | | | | | | | | | | | | | | | |

CMPI

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 1 | 1 | 0 | rx | | | Immediate | | | | | | | |
| 指令格式 | CMPI rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( R[x] = Sign\_extend(immediate) ) then T←0  If ( R[x] != Sign\_extend(immediate) ) then T←1 | | | | | | | | | | | | | | | |
| 功能说明 | 将立即数immediate进行符号扩展后，与rx的值比较。若相等，则给标志寄存器T置0；否则置1 | | | | | | | | | | | | | | | |

LI

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 1 | 0 | 1 | rx | | | Immediate | | | | | | | |
| 指令格式 | LI rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←Zero\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 将立即数immediate的值（进行零扩展之后）存储到寄存器rx中 | | | | | | | | | | | | | | | |

LW\_SP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 0 | 0 | 1 | 0 | rx | | | Immediate | | | | | | | |
| 指令格式 | LW\_SP rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | R[x]←MEM[SP + Sign\_extend(immediate)] | | | | | | | | | | | | | | | |
| 功能说明 | 从内存中读取数据到寄存器rx中，内存地址为寄存器SP的内容与立即数（进行符号扩展后）immediate之和 | | | | | | | | | | | | | | | |

SW\_SP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 0 | 1 | 0 | rx | | | Immediate | | | | | | | |
| 指令格式 | SW\_SP rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | MEM[SP + Sign\_extend(immediate)]←R[x] | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器rx的值写入到内存中，内存地址为寄存器SP的内容与立即数（进行符号扩展后）immediate之和 | | | | | | | | | | | | | | | |

ADDSP

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 1 | 0 | 0 | 0 | 1 | 1 | Immediate | | | | | | | |
| 指令格式 | ADDSP immediate | | | | | | | | | | | | | | | |
| 指令功能 | SP←SP + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 对立即数immediate进行符号扩展后与寄存器SP的值求和，结果保存到寄存器SP中 | | | | | | | | | | | | | | | |

B型

B

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 0 | 1 | 0 | Immediate | | | | | | | | | | |
| 指令格式 | B immediate | | | | | | | | | | | | | | | |
| 指令功能 | PC←PC + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 将寄存器PC与立即数immediate符号扩展后的值求和，结果保存到寄存器PC中，即程序无条件跳转到目的地址执行 | | | | | | | | | | | | | | | |

BEQZ

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 1 | 0 | 0 | rx | | | Immediate | | | | | | | |
| 指令格式 | BEQZ rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( R[x] = 0 ) then PC←PC + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 若寄存器rx的值为0，则跳转到目的地址执行；否则顺序执行下一条指令 | | | | | | | | | | | | | | | |

BNEZ

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 0 | 1 | 0 | 1 | rx | | | Immediate | | | | | | | |
| 指令格式 | BNEZ rx immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( R[x] != 0 ) then PC←PC + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 若寄存器rx的值不为0，则跳转到目的地址执行；否则顺序执行下一条指令 | | | | | | | | | | | | | | | |

BTEQZ

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 0 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | Immediate | | | | | | | |
| 指令格式 | BTEQZ immediate | | | | | | | | | | | | | | | |
| 指令功能 | If ( T = 0 ) then PC←PC + Sign\_extend(immediate) | | | | | | | | | | | | | | | |
| 功能说明 | 若标志寄存器T的值为0，则跳转到目的地址执行；否则顺序执行下一条指令 | | | | | | | | | | | | | | | |

J型

JR

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| 指令编码 | 15 | 14 | 13 | 12 | 11 | 10 | 9 | 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 | 0 |
| 1 | 1 | 1 | 0 | 1 | rx | | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 指令格式 | JR rx | | | | | | | | | | | | | | | |
| 指令功能 | PC←R[x] | | | | | | | | | | | | | | | |
| 功能说明 | 程序无条件跳转到rx地址单元执行，用于长地址跳转 | | | | | | | | | | | | | | | |