

54321

VERSION HISTORY

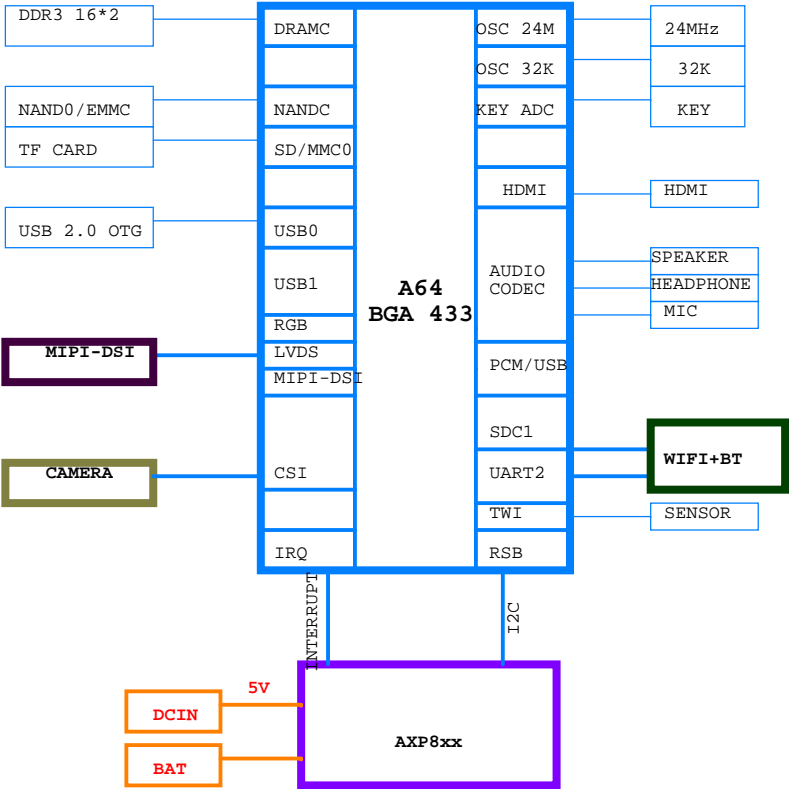
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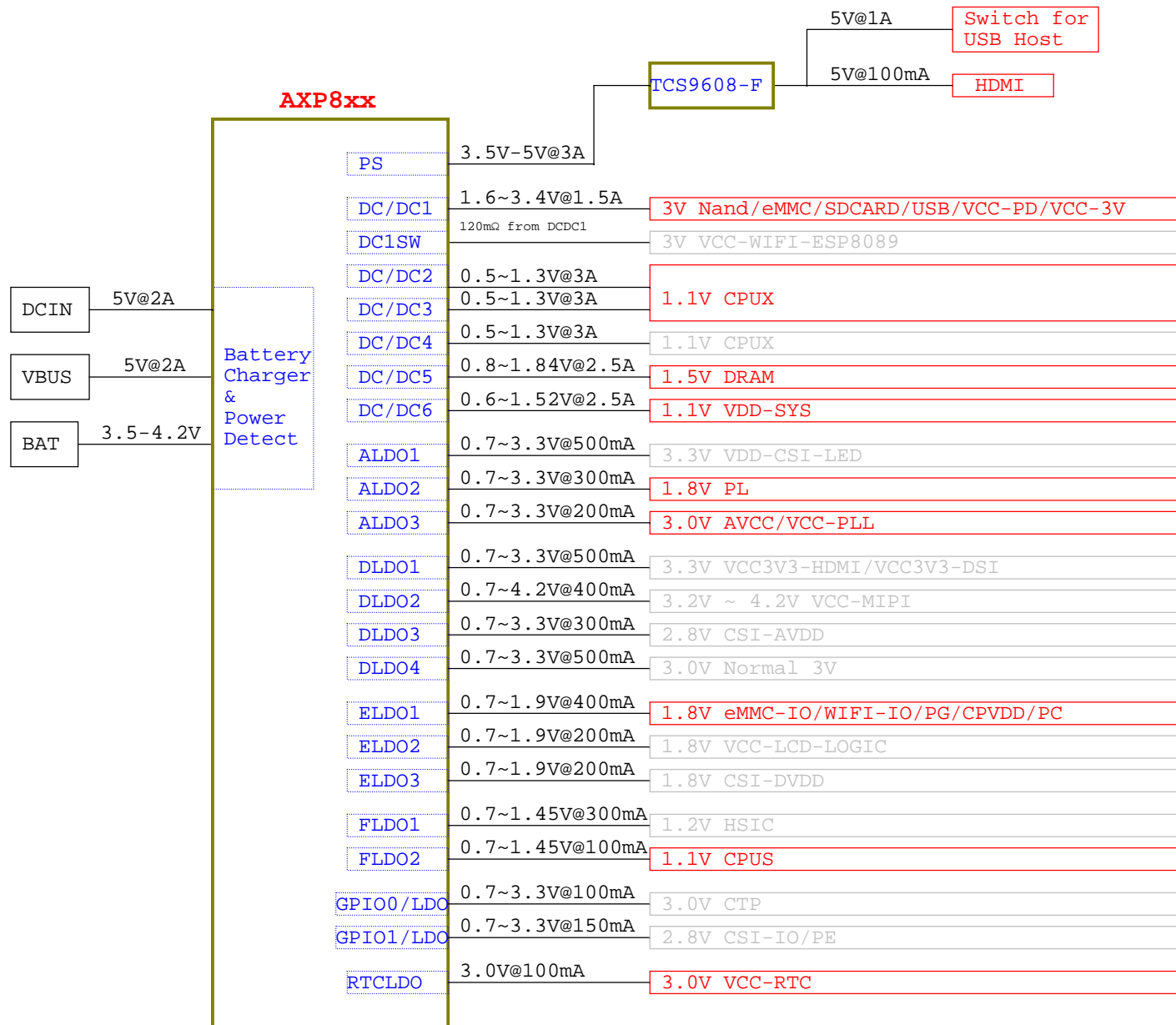
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03 POWER TREE
04 GPIO ASSIGNMENT
05 DDR3 16X2
06 CPU
07 POWER
08 FLASH
09 HDMI
10 CSI
11 DSI/CTP
12 AUDIO/SENSOR
13 CARD/KEY/USB
14 WIFI-BT-GPS
- C
- B
- A

Revision	Description	Date	Drawn	Checked	Approved
Ver 0.1	Inittial	2014-12-29	Prince		
Ver 0.1	Add Pin assignment	2015-01-20	Prince		

BLOCK DIAGRAM



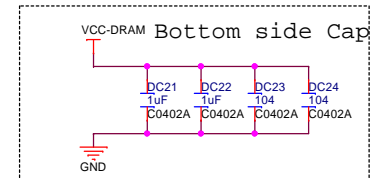
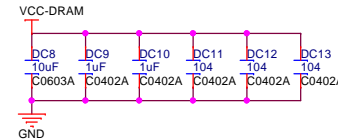
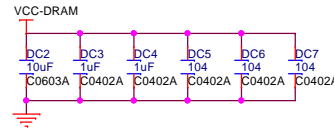
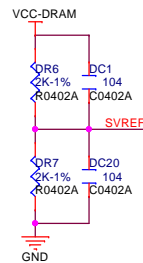
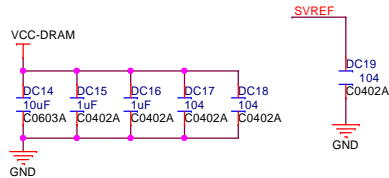
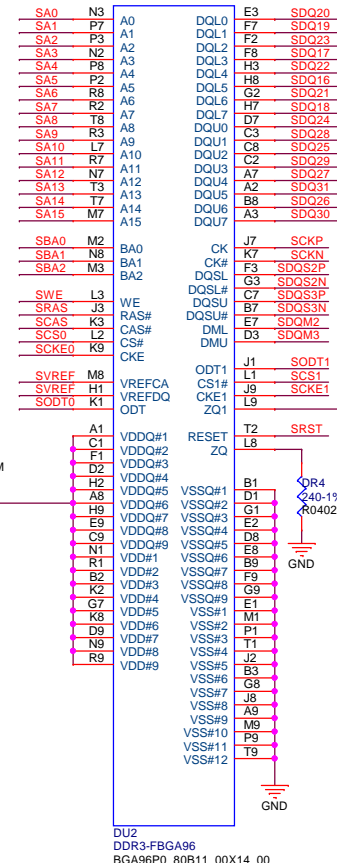
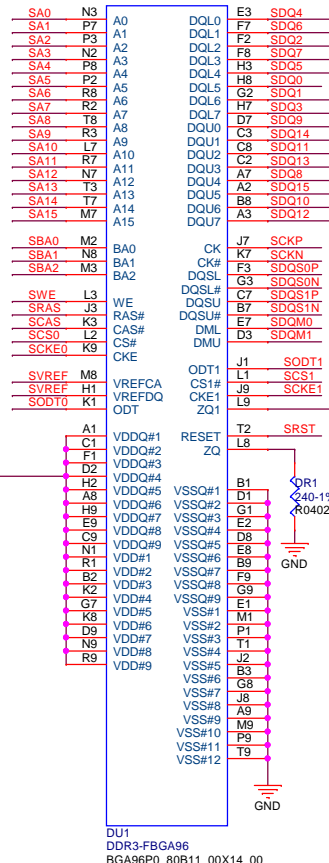
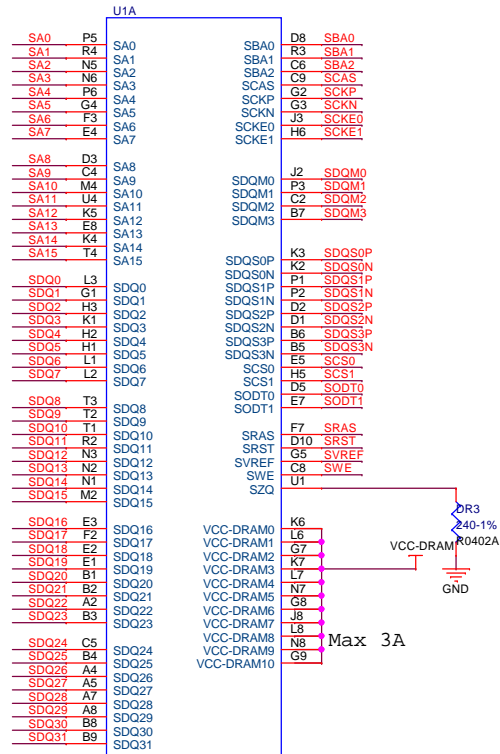
POWER TREE

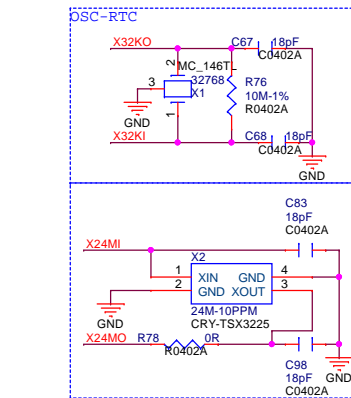
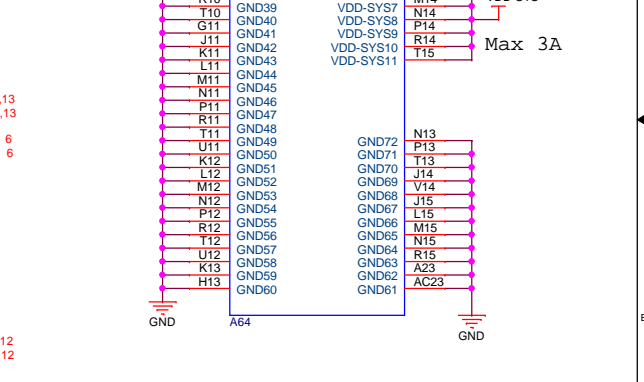
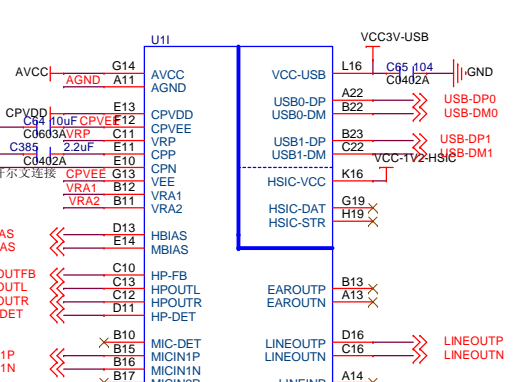
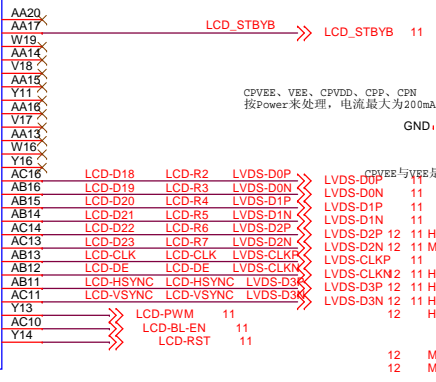
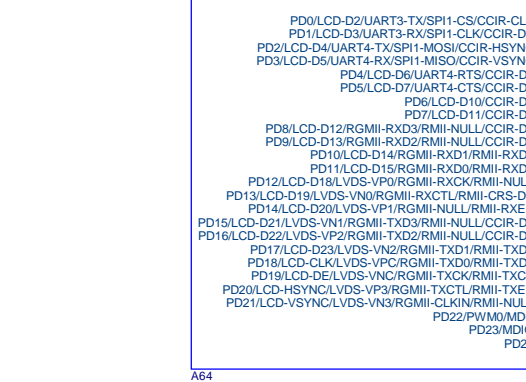
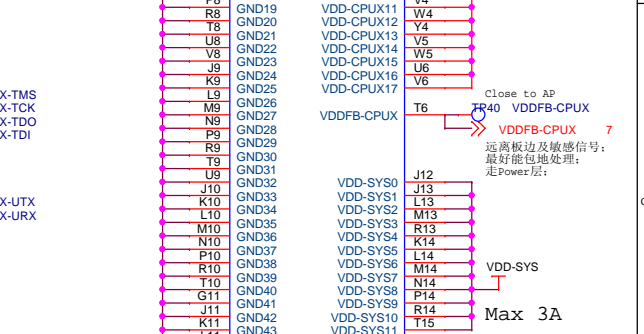
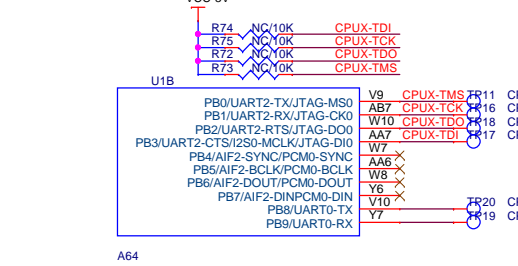
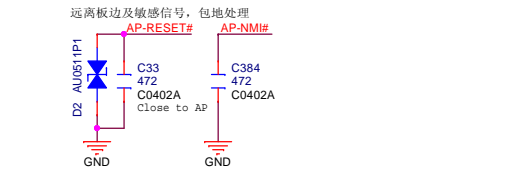
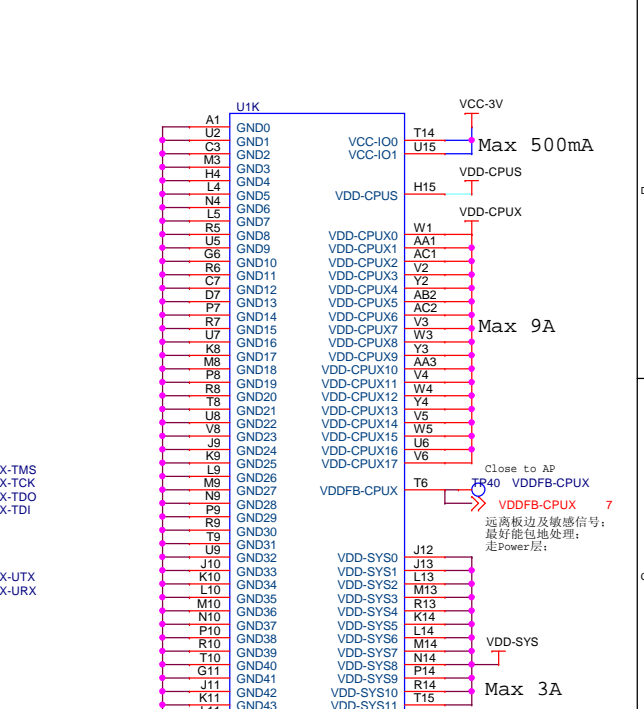
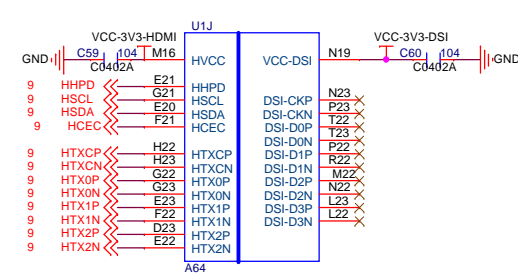
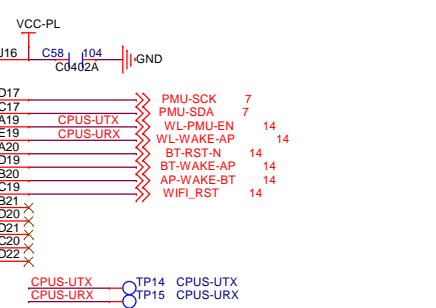
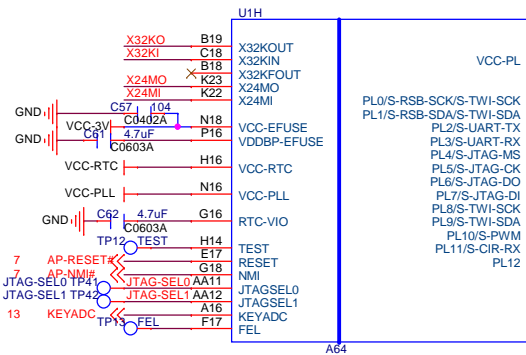


灰色：上电默认关
红色：上电默认开

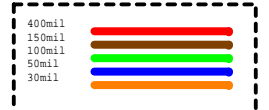
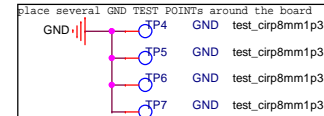
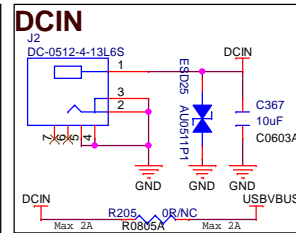
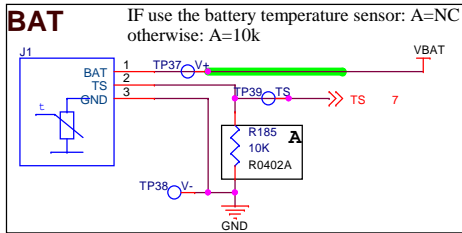
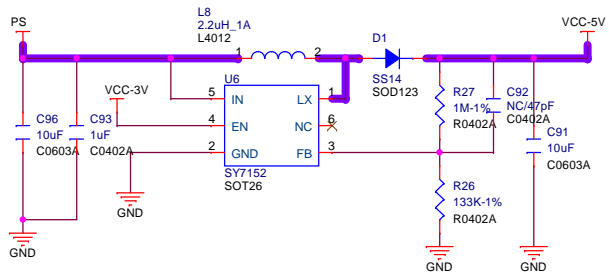
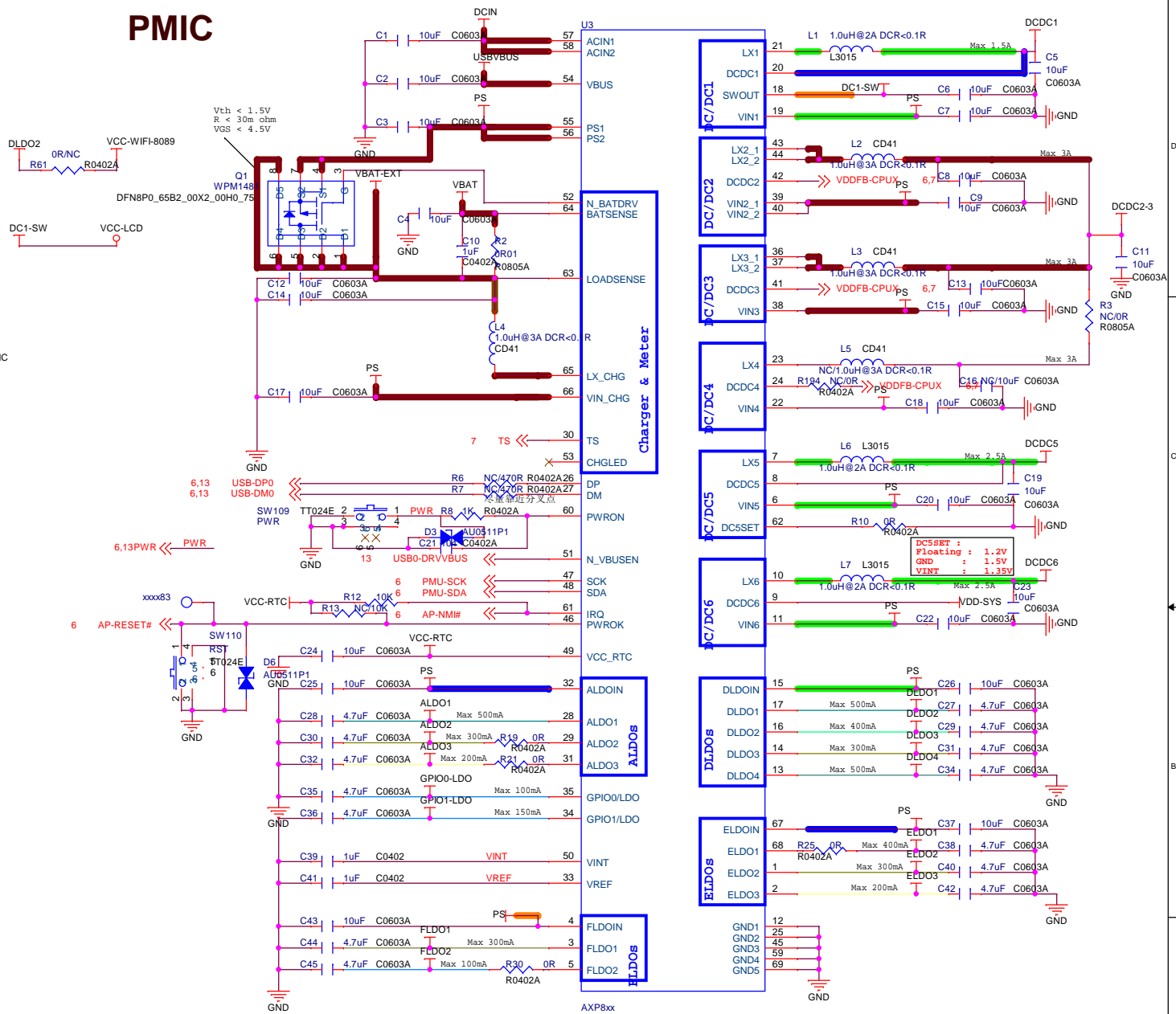
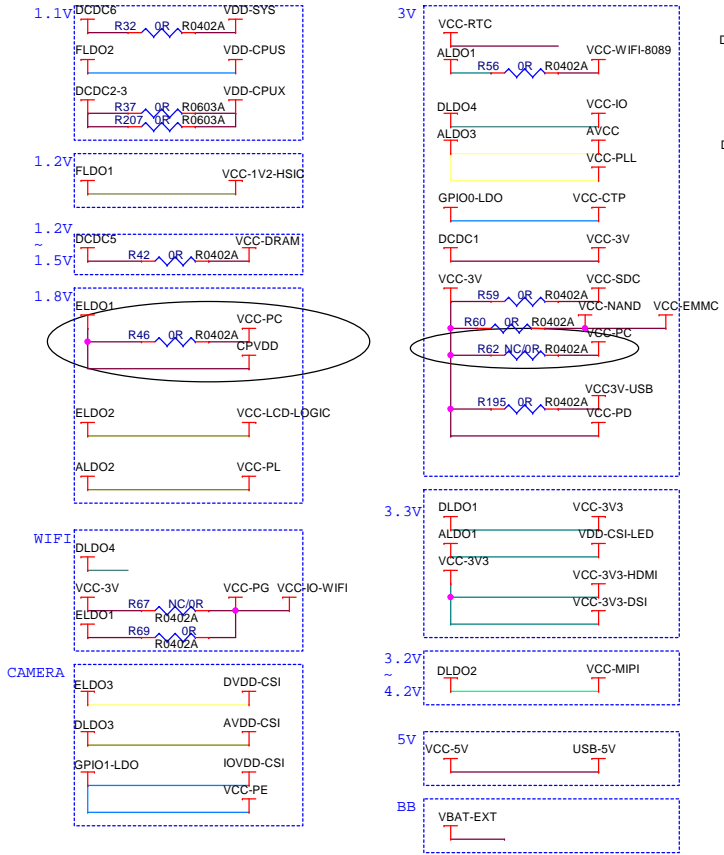
GPIO ASSIGNMENT

DDR3

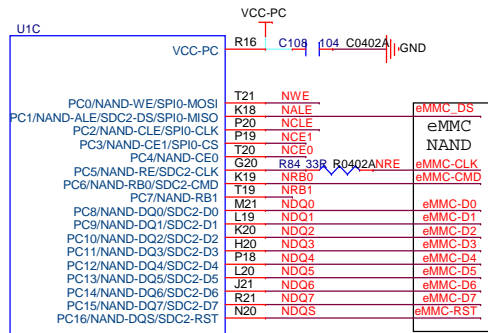
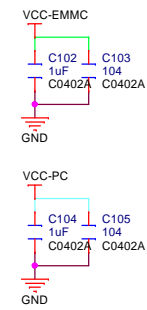
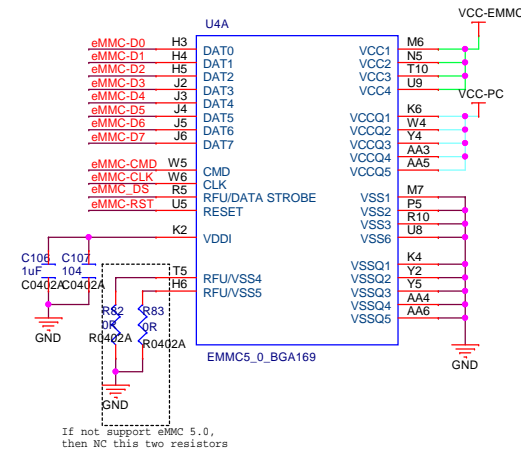
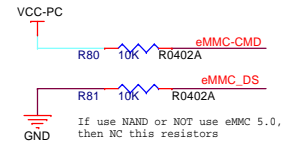




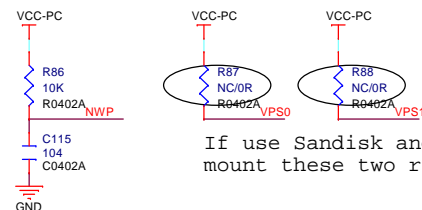
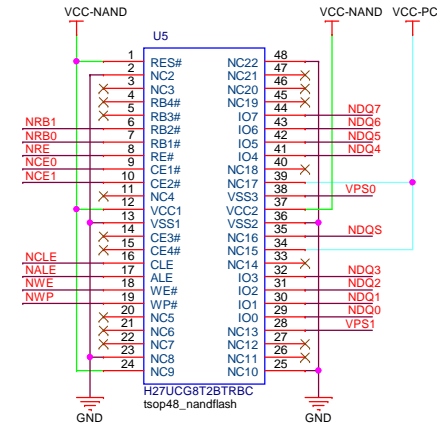
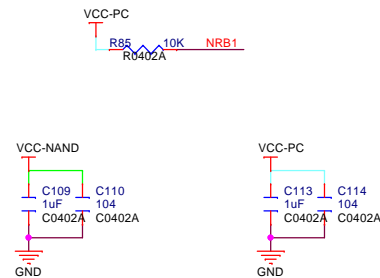
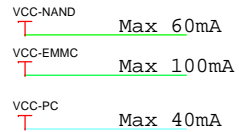
POWER

PMIC

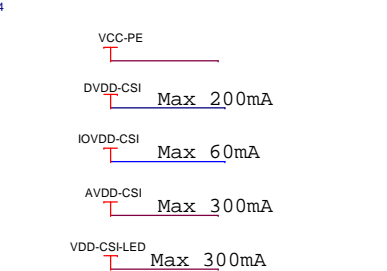
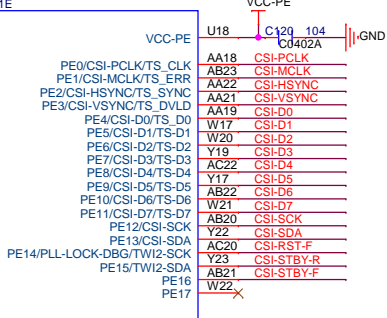
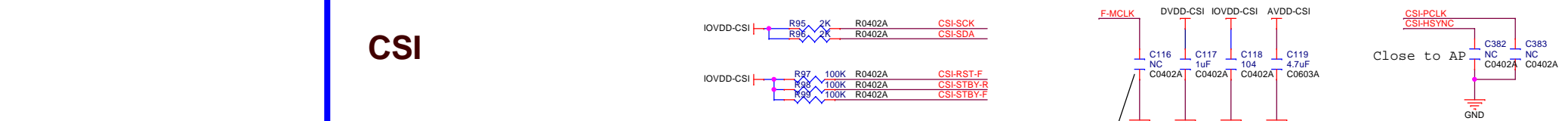
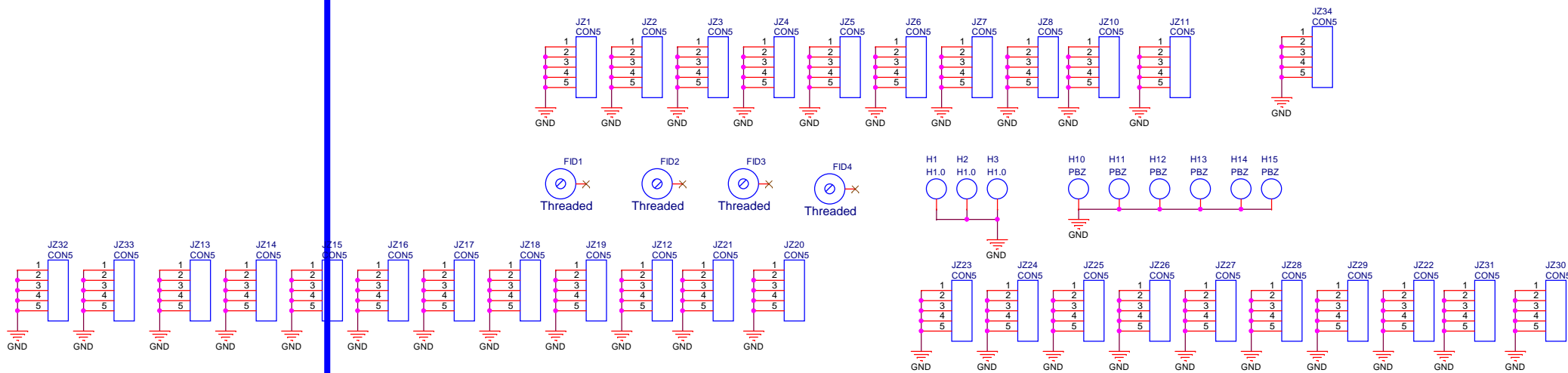
NAND/eMMC



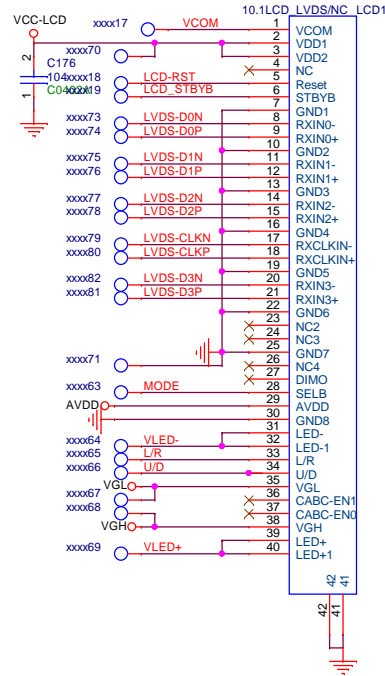
Both eMMC NAND and first TSOP NAND layout together



If use Sandisk and Toshiba NAND flash,
mount these two resister



LVDS



6 LCD-PWM
6 LCD-BL-EN
6 LCD-RST

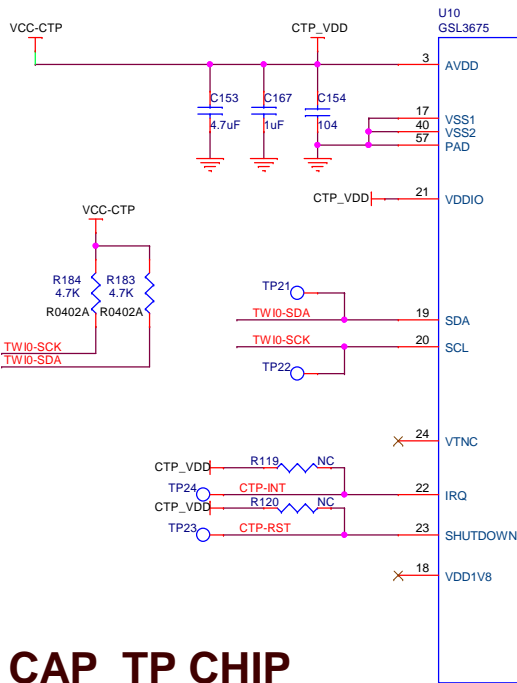
Differential pairs
Z0= 100 ohm

VCC-MIPI Max 200mA
VCC-LCD-LOGIC Max 200mA
PS Max 600mA

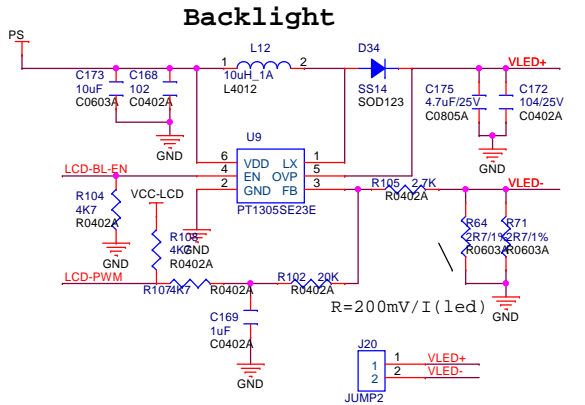
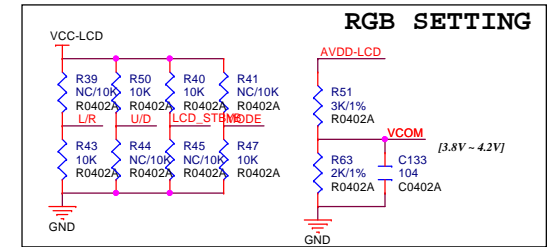
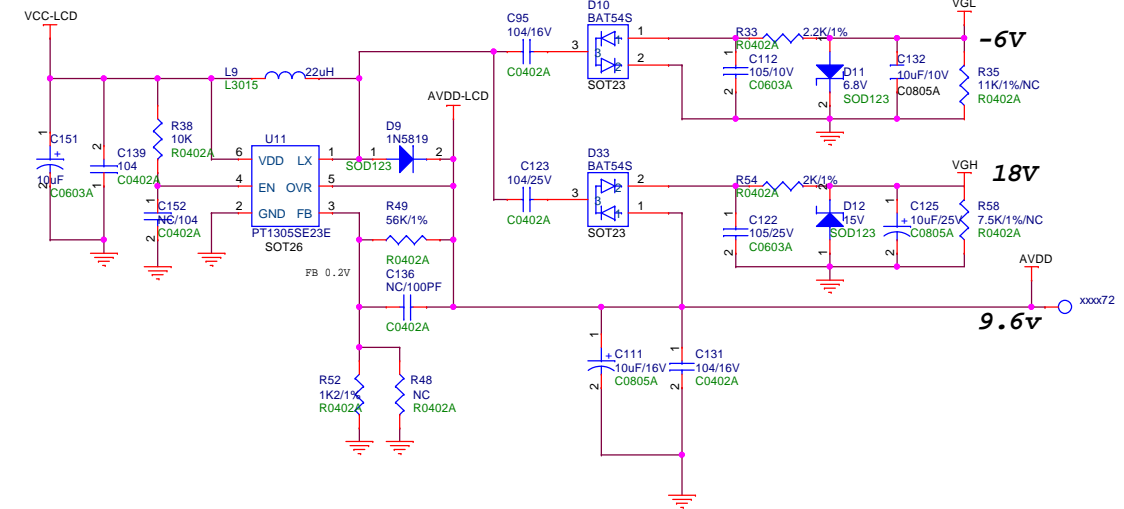
LCD_STBYB >>> LCD_STBYB 11
LCD-D18 LCD-R2 LVDS-D0P >>> LVDS-D0P 11
LCD-D19 LCD-R3 LVDS-D0N >>> LVDS-D0N 11
LCD-D20 LCD-R4 LVDS-D1P >>> LVDS-D1P 11
LCD-D21 LCD-R5 LVDS-D1N >>> LVDS-D1N 11
LCD-D22 LCD-R6 LVDS-D2P >>> LVDS-D2P 11
LCD-D23 LCD-R7 LVDS-D2N >>> LVDS-D2N 11
LCD-CLK LCD-CLK LVDS-CLKP >>> LVDS-CLKP 11
LCD-DE LCD-DE LVDS-CLKN >>> LVDS-CLKN 11
LCD-HSYNC LCD-HSYNC LVDS-D3P >>> LVDS-D3P 11
LCD-VSYNC LCD-VSYNC LVDS-D3N >>> LVDS-D3N 11

VCC-IO Max 1mA
VCC-CTP Max 100mA

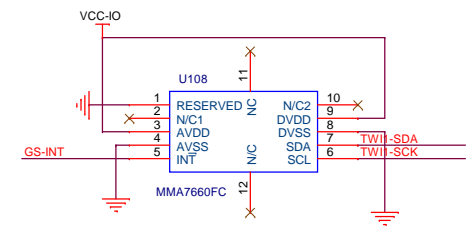
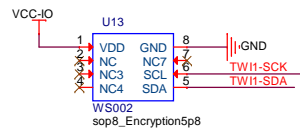
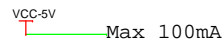
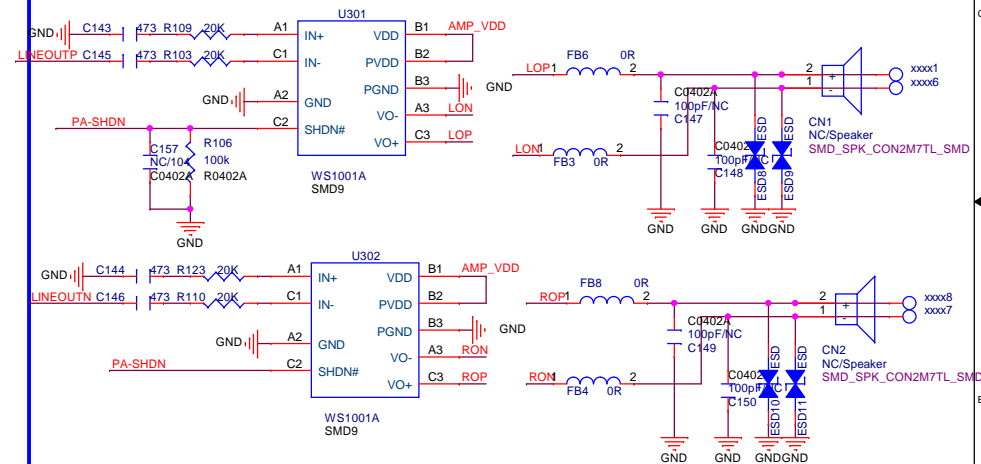
13 TWI0-SDA
13 TWI0-SCK
13 CTP-INT
13 CTP-RST



CAP_TP CHIP



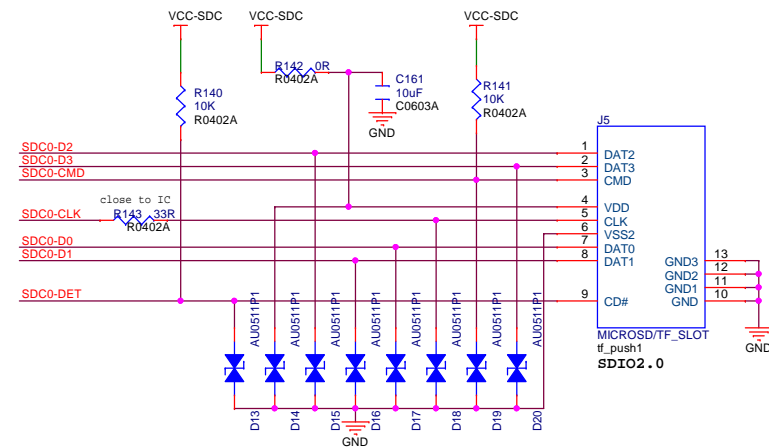
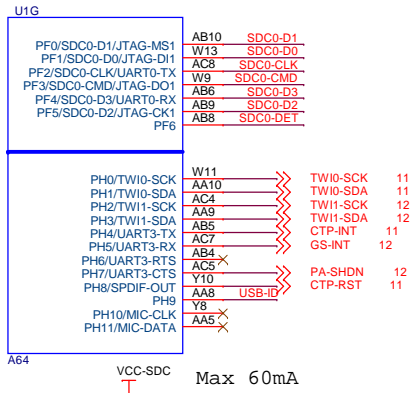
G-SENSOR



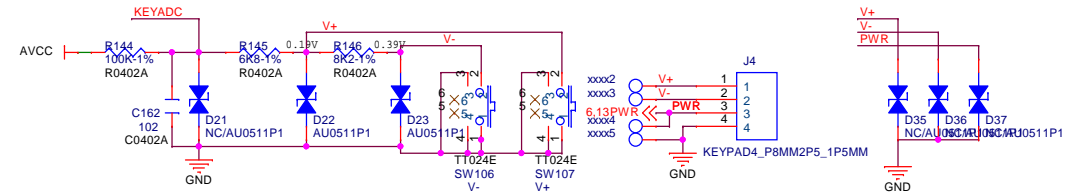
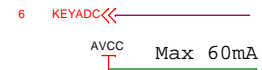
A64-Prototype

Name	Rev
CARD-SENSOR-VIBRATOR-KEY-OTG	

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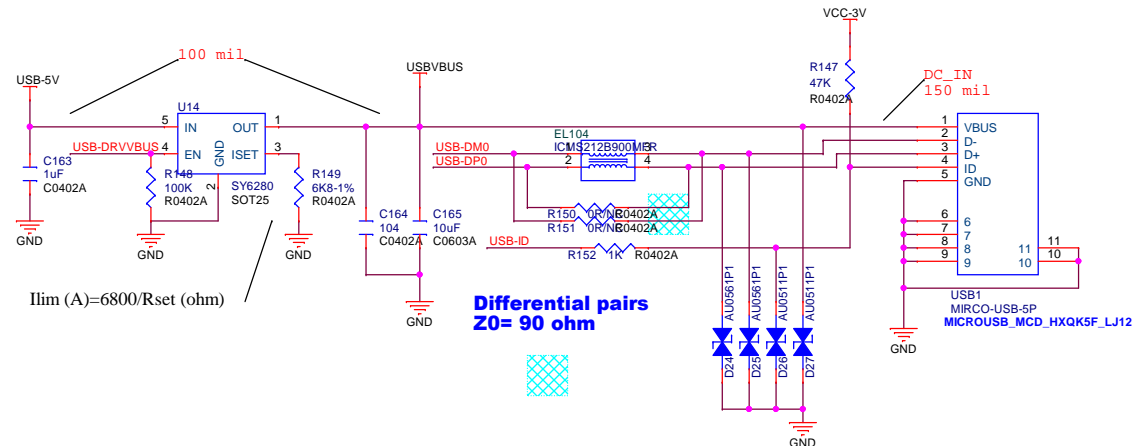
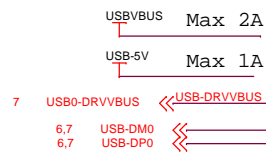


KEY



USB OTG

note: Make sure the routing between the ESD and the USB connectors should be on the same PCB side



AllWinner Technology Co.,Ltd

Design Name

A64-Prototype

Size
A3

	Page Name
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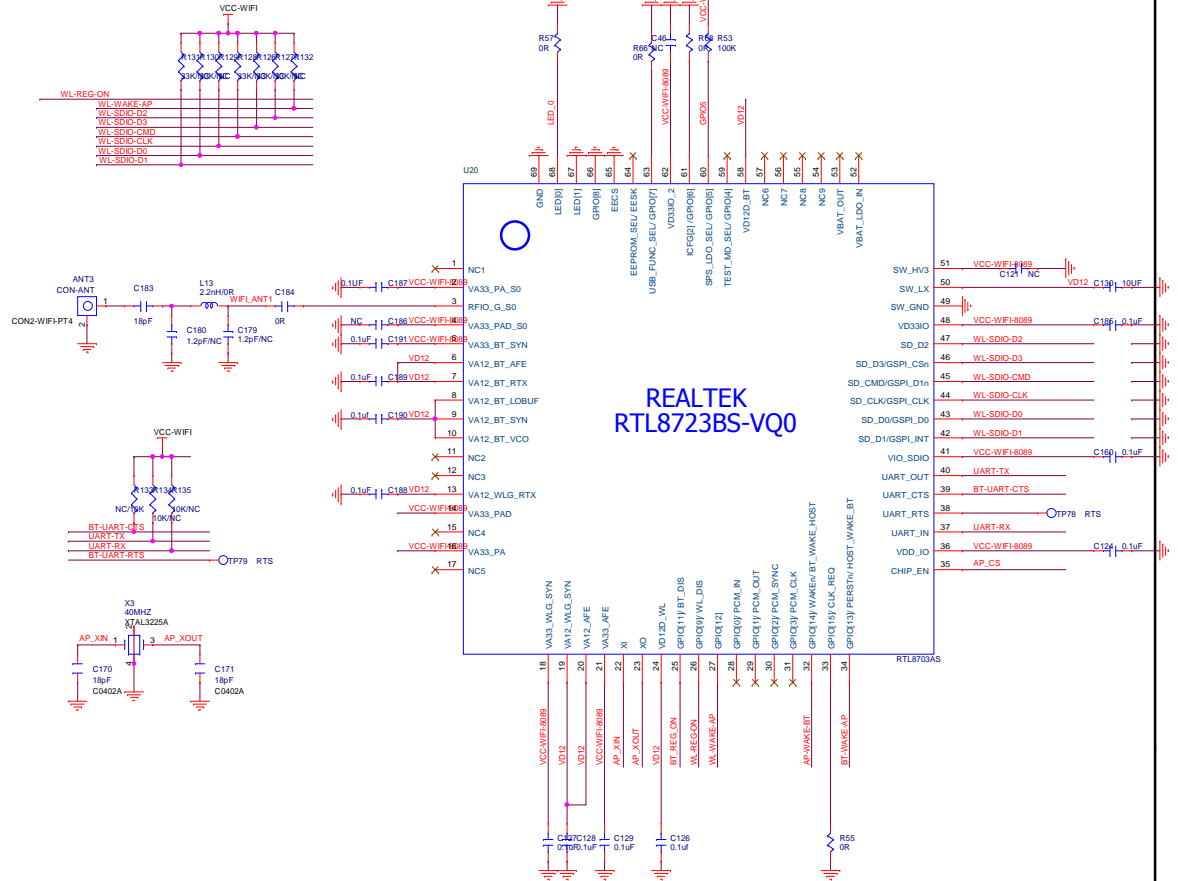
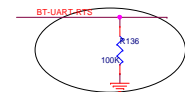
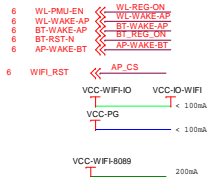
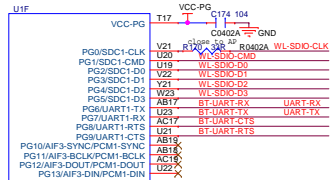
Name CARD-SENSOR-VIBRATOR-KEY-OTG Rev

Date:

Wednesday, June 17, 2015

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WIFI+BT+GPS



8188ETV

OPTION

