# Massachusetts Institute of Technology Department of Electrical Engineering and Computer Science

# Proposal for Thesis Research in Partial Fulfillment of the Requirements for the Degree of Doctor of Philosophy

TITLE: Parallel Processor Architecture

Submitted by: Peter Nuth

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(SIGNATURE OF AUTHOR)

Date of Submission: May 14, 2016
Expected Date of Completion: September 1990

LABORATORY: Artificial Intelligence Laboratory

BRIEF STATEMENT OF THE PROBLEM:

The proposed research is a study of processor architecture for large scale parallel computer systems. The thesis introduces mechanisms for fast context switching, synchronization between tasks, and run-time binding of variable names to processor memory. Various design tradeoffs are evaluated through simulation of a processor running a typical load. This work contains estimates of the speed and complexity of the different alternatives as implemented in VLSI.

## Doctoral Thesis Supervision Agreement

From: Professor William J. Dally	
The program outlined in the proposal:	
TITLE: Parallel Processor Architecture AUTHOR: Peter Nuth DATE: May 14, 2016	
is adequate for a Doctoral thesis. I believe that thesis would be:	t appropriate readers for this
Reader 1: Professor Arvind Reader 2: Professor Thomas Knight	
Facilities and support for the research outlined I am willing to supervise the thesis and evaluate t	
Signed:	
	Associate Professor of Electrical Engineering and Computer Science
Date:	
Comments:	

## Doctoral Thesis Reader Agreement

To: Department Graduate Committee From: Professor Arvind				
The program outli	ned in the proposal:			
DATE: SUPERVISOR:	Parallel Processor Architecture Peter Nuth May 14, 2016 Professor William J. Dally Professor Thomas Knight			
is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.				
	Signed:	Professor of Electrical Engineering AND Computer Science		
	Date:			
Comments:				

## Doctoral Thesis Reader Agreement

	nent Graduate Committee or Thomas Knight		
The program outli	ned in the proposal:		
DATE: SUPERVISOR:	Parallel Processor Architect Peter Nuth May 14, 2016 Professor William J. Dally Professor Arvind	ture	
is adequate for a Doctoral thesis. I am willing to aid in guiding the research and in evaluating the thesis report as a reader.			
	Signed:	Assistant Professor of Electrical Engineering and Computer Science	
	DATE:		
Comments:			

## Doctoral Thesis Reader Agreement

To: Departn	nent Graduate Committee	
From: Professo	or William J. Dally	
The program outli	ned in the proposal:	
TITLE:	Parallel Processor Architec	eture
AUTHOR:	Peter Nuth	
DATE:	May 14, 2016	
	Professor William J. Dally	
	Professor Arvind	
Other Reader:	Professor Thomas Knight	
_	Octoral thesis. I am willing thesis report as a reader.	to aid in guiding the research
	SIGNED:	
		Associate Professor of Electrical Engineering and Computer Science
	Date:	
Comments:		