SERVICE MANUAL

A590

AUGUST, 1989

PN-314899-01



SECTION 1 SPECIFICATIONS

A590 Specifications

Features

20 MB Hard Drive — SCSI

Expansion sockets for 2MB of fast RAM (256K x 4)

Auto-Boot ROMS

SCSI — ANSI X3T9.2 compatible

DB-25 SCSI connector

50-PIN internal SCSI connector

Host Interface - connects to 86 PIN edge connector of A500 computer.

Dimensions

6 x 10.5 x 2.75 inches, assembly

Weight

4.5 lbs, assembly

Power Requirements

117/220/240 VAC, 50 Watts (max)

SECTION 2

- THEORY OF OPERATIONS
- SCSI BUS INTERFACE CONTROLLER

NOTE

PLEASE REFERENCE A590 USER MANUAL PN-363026-01 FOR DETAILED OPERATIONS.

Theory of Operation

The Host Power supply provided +5 volts to this Assembly through an 86-pin connector where it enables the A590 external power supply.

The Assembly as described herein shall consist of three independent subsystems:

- [1] MEMORY
- [2] DMA/SCSI CONTROLLER
- [3] AUTO-CONFIG

AUTO-CONFIG for all on-board devices (RAM, ROM, DMA controller) shall be executed by the DMA controller chip.

The DMA chip provides "_RAMSEL" and "_ROMCS" signals to indicate an access to the on board RAM and ROM, respectively. During auto-config time the DMA chip must indicate how much RAM is on board. It determines this by reading its "RAMSZ" line. The following chart shows the relationship between the signal on RAMSZ and the amount of RAM that should be present:

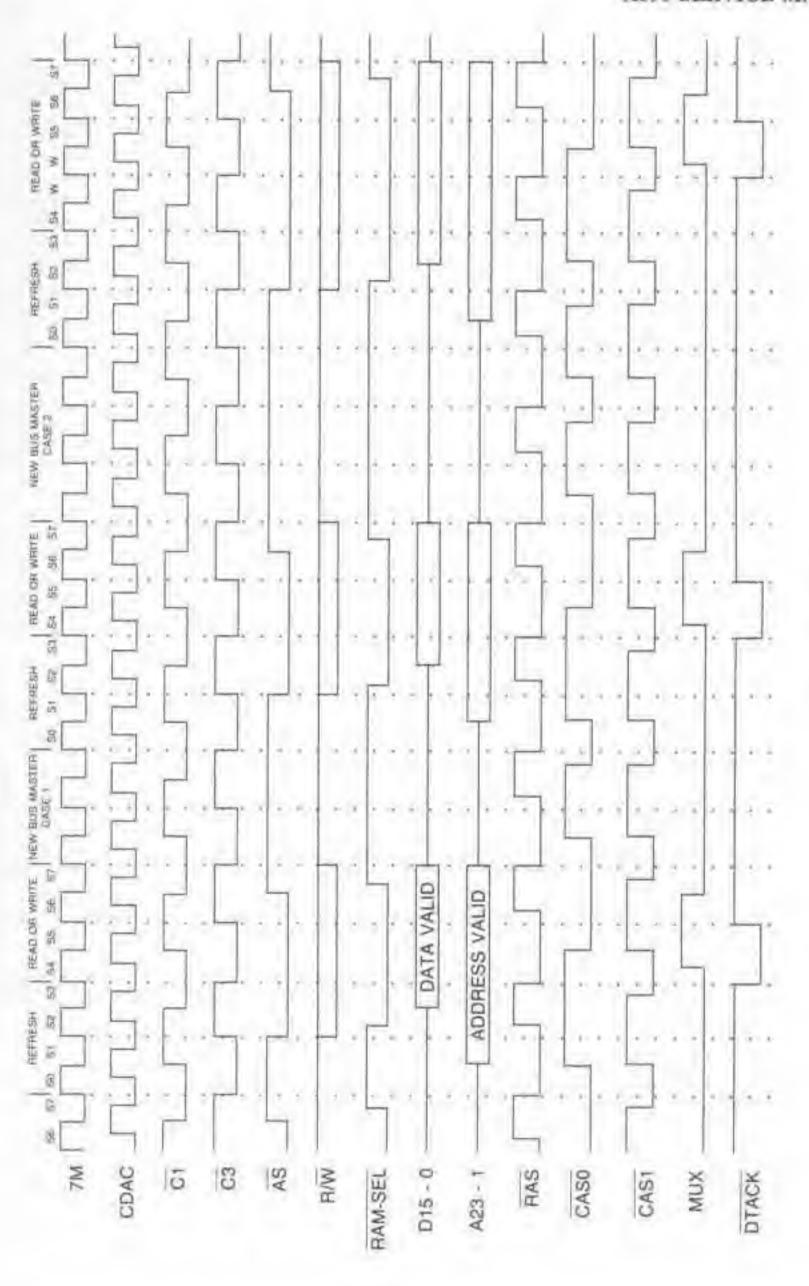
Signal	Amount of RAM		
Ground	None		
Inverse of CDAC	512K		
CDAC	1M		
Vec	2M		

The signal applied to "RAMSZ" is determined by JP1. The DMA controller chip also generates the signal "_SLAVE" whenever something within the auto config address space is being accessed. This is tied directly to "_OVR" to allow for generation of "_DTACK" on our board.

The auto-boot ROMS simply connect to the bus with the data outputs being enabled by "_ROMCS". "_DTACK" is generated automatically by the DMA chip when the ROM is accessed.

The RAM section is controlled by U5. (See Timing Diagram.) U3 generates RAS and CAS for the RAMs as well as DTACK. The RAM is set up in 4 separate banks, each with its own CAS, but a common RAS. The CAS on the bank that is being accessed has a special access waveform, while the other three CAS lines will just perform a refresh. After the access, a hidden refresh is performed. If the RAM is not being accessed, all of the RAM is just continually refreshed. On writes, the byte(s) (upper, lower or both) to write shall be determined by gating and write signal to the RAMs with UDS and LDS. Byte control of reads is gated by applying LDS or UDS to the output enable inputs of the RAMs. The multiplexed address that DRAMS need is generated by U8 and U9, which is controlled by the mux signal from U5. There are two possible relationships between processor timing and the C1 and C3 clocks. U5 only understands one of these (Agnus normally synchronizes the processor to this relationship, but this relationship can be upset by DMA operations, for instance). If U5 sees the other relationship, it will insert a single wait state to realign the 68000 to what it considers correct timing. This special cycle is shown on the timing diagram in addition to normal cycles.

The DMA controller (U1) attaches to the bus and to the WD33C93 SCSI controller chip (U4). U4 attaches to an internal 50 pin SCSI connector and goes out to a DB25 connector on the back of the board which allows attachment of SCSI peripherals. The DMA controller also provides a special XT interface for IBM PC XT bus type drives (this is NOT ST-506). This connects to the internal drive. The 7-MHz clock required by the DMA controller is generated by U6. The special SCSI bus reset signal is generated whenever the Amiga is reset.



390206-01 SCSI Bus Interface Controller U4-WD33C93

The SCSI protocol between the Host/Initiator and a Target/Controller is:

- . Host selects the SCSI controller
- . Controller requests from the Host a command specifying the task. (Such as disk read, tape write or printer etc.)
- Host sends command and controller interprets it and executes. (Reads data from disk and asks Host to accept data, prepares tape for write operation or conditions printer for printing and then requests date from Host, etc.)
- . After completion of data transfer the controller requests that the Host accept the Status Byte
- . After status is accepted by the Host the controller disconnects from the bus, leaving it free for next operation

The SBIC when used in an initiator system is connected to both the Host type bus and the SCSI bus and waits for a command from the Host to select a specific target. If the bus is busy serving a higher priority Iniciator, the SBIC waits for the bus to become available and then attempts to select the Target. When successful the SBIC generates an interrupt to the Host to indicate that the selection has been completed.

The Target then requests a command byte, the SBIC interrupts the Host and passes on the request. The Host responds by giving a TRANSFER INFO command to the SBIC, along with the command byte requested by the Target, and passes it on to the Target. This process continues until all command data and status bytes have been transferred. During a data transfer phase, the data can be transferred between the SBIC and Host memory via DMA. To relieve the Host of some of the interrupt-handling responsibilities, the SELECT and TRANSFER INFORMATION commands may be chained together by using the special SELECT-AND TRANSFER commands.

When the SBIC is used in a peripheral controller system it communicates with the local processor and SCSI bus in the same way as used in the initiator system. The SBIC is also capable of operating as a bus master on the controllers local data bus. Therefore, the SBIC can, during a data transfer, issue read and write enables to access an external buffer without requiring DMA or programmed I/C transfers.

The SBIC implements arbitration, parity and synchronous transfers as well as full standard SCSI physical path definition for use with either differential or single ended interfacing option.

Refer to Figure 1 for block diagram, Figure 2 for pin configuration and Table 1 for pin description.

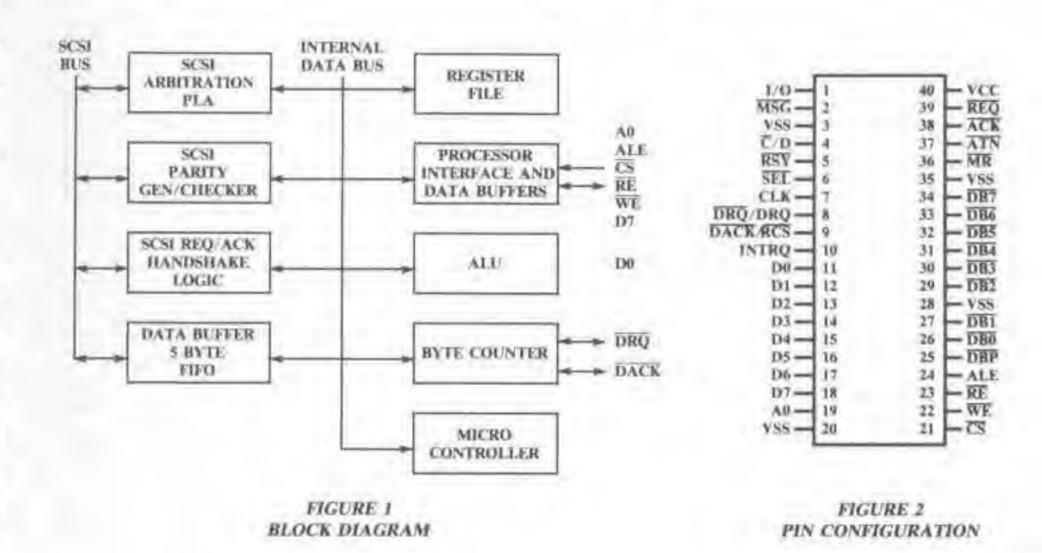


TABLE 1

District			Action of the control
NUMBER	MNEMONIC	SIGNAL NAME	I/O FUNCTION
	Sietzmient.	SACROANT LANGUED	THE PLANT SHOW
DIP/QSM		- ALL SCSI plus have op	
1/2	1/0	INPUT/OUTPUT	I/O L/O controls the direction of data unoversion on the SCSI has with respect to the fulfator. When asserted, data is output to the Initiator. When de-asserted, data is output from the Initiator. 10 is an input signal when the harmonic of the Initiator.
2/3	MSG	MESSAGE	WD33C93 is operating as an Initiator and an output signal when operating as a Target. I/O MSC is exerted during a message phase. MSC is an input when the WD33C93 is operating as an Initiatio and an output when operating as a Target.
A/5	6.0	CONTROL/DATA	1/O C/D is amorted when there is Control information on the SCSI data has and de-excepted for data. C/D is an input when the WDDC93 is operating as at lettlator and an output when operating as a Target.
5/6	BSY	BUSY	1/6) BSV is asserted by the WD33CV3 as an output when attempting to arbitrate for the SCSI has or when con- outied as a target. When the WD33CV3 is connected as an Initiator, BSV operates as an imput.
9/1	SEC	SELECT	1/O The WD33C93 asserts SET as an output when trying to select or resoluci another SCSI slevice. The WD33C93 receives SET as an input when it is being selected.
25/28	DEF	SCEI DATA PARITY	I/O 6CSI bus dute parity.
36/29	DBO	SCSI DATA 0	1/O SCSI bus data bit ft.
27/30	DBI	SCSI DATA T	1/O SCSI buy data bil 1;
29/32	DHE	SCSI DATA 2	L/O SCSI bos data bit 2.
thra	faru	Ourse	theu
32/35	DINS	SCSI DATA S	1/O SCS) bus data 5.
33/37	DB6	SCSI DATA 6	1/O SCS) bus data till 6.
34/38	087	SCSL DATA 7	1/O SCSI bue data bit 7.
37/41	ATN	ATTENTION	1/O ATN signals that the totalistor has a message to transfer. ATN is an output signal when the WE33C93-SBIC is operating as so fulfator and no input signal when connected as a Target.
38/43	ACK	ACKNOWLEDGE	I/O ACK acknowledges a REQ/ACK data transfer hundshake, ACK is an output signal when the WD33C93 is operating as an initiator and an input when operating as a Target.
39/43	REO	REQUEST	UO REQ requests an REQ/ACK data transfer REQ is an input signal when the WD33C93 is operating as an initiator and an output signal when operating as a Target.
DIP/QSM	PROCESSOR/DMA	INTERFACE	
7/8	CLS	CLOCK	1 10 MHz square wave cluck.
8/9	DRQ/DRQ	DATA REQUEST	O DRQ interfaces with no external DMA controller (eq. \$257) and forms the DRQ/DACK handshake for data byte transfers.
		DATA REQUEST	I DRQ interfaces with an extremal buffer. When unserted, data burst transfers are enabled using Direct Buffer Access (DRA). This algorit is open strain.
9/10	DACK/RCS	ACKNOWLEDGE RAM CHIP	 DACK interfaces with an external DMA controller (eg. 8347). When asserted, all has transfers are to or from the Data Register regardless of the contents of the Address Register. RCS interfaces with an external buffer. When RCS is asserted. WE and RE are enabled as output signals, making it possible for the WD33C93 to access the Intifer directly.
			This signal is open drain.
10/32	INTRO	INTERBUPT REQUEST	O INTRO signals a local microprocessor or Hon that a WD33C93 command has terminated or the SCSI interface needs service.
11/13	100	DATA 0	L/O Local data fea ful 0.
Ora	Oreic	10cm	tirv
t6/28	D1	DATA 7	Local data has bit 7.
19/21	Aff	ADDRESS 0	1 A0 is used to access an internal register during the indirect addressing mode of operation. During direct addressing, A0 is ignored. A0 = 0. The address of the desired register is loaded into the Address Register during a write cycle (WE superied).
			Att = 1. The register selected by the Adulerss Register is accessed.
21/24 12/25	W.E.	WRITE ENABLE	1 When CS is asserted. WE used RE are enabled as imput signals for accessing registers within the WIESSE'93. 1/O WE is no imput signal and enables writing to an internal register when used with CS. WE is no output signal and enables writing to the external buffer when used with ECS.
			WE is a tri-state algoral.
13/26	RE	READ ENABLE	I/O RE is an imput signal and enables reading an internal register when used with CS. RE is an noticel signal and enables reading the external buffer when used with RCS.
24/17	ALU	ADDRESS	RE is a tri-state signal. With the training edge of ALE, the address on the local data has is betched into the Address Register, When
26/40	MR	MASTER RESET	indirect subtreasing is used, as in non-emittiplexed busins the ALE pin must be grounded. 1 When americal, MR places the WIGSC93 into a disconnected state. All SCSI signals are placed in a passive state.
DIP/QSM	MISCELLANEOUS		
d	TEST	FACTORY TEST	TEST pins for inctory use only and should not be connected.
CHI	TEST	FACTORY TEST	rear him for rectors use outle and prouds not be connected.
3/4	Visi	GROUND	
20/33	VSS	GROUND	
28/41	V88	GROUND	
35/39	VSS		
40	TEST	CHOUND PACTORY TEXT	
		FACTORY TEXT	
736	TEST	FACTORY TEST	
40/+4	VCC	+ 5 VOLTS	

SECTION 3

- TROUBLESHOOTING GUIDE
- INSTALLING DRAMS

TROUBLESHOOTING

With the power off, make sure that all cables are connected correctly, and that the A590 is properly connected to the Amiga.

SYMPTOM	CAUSE	SOLUTION	
Power light is not on.	Power supply not plugged into wall. Power supply not plugged into A590. A500 power supply not turned on.	Check connections to wall and A590.	
A500 power light blinks, or system fails with the A590 connected.	A500 is running with Kickstart 1.2 and DIP switch 1 is set to autoboot enabled.	Set DIP switch 1 to OFF.	
System cannot find the A590,	A500 is running Kickstart 1.3 and DIP switch is set to autoboot disabled.	Set DIP switch I to ON.	
System cannot find the A590.	A500 is running Kickstart 1.2 and the device driver was not copied to expansion drawer.	Boot with disk cremed with MakeBootDisk.	
Not a DOS disk in Unit 1. Unit appears as NDOS on Workbench screen:	Hard drive Prepped but not formatted.	Format the hard disk from CLI/Shel or initialize hard disk from Workbench.	
Read/Write error.	Bad block on hard drive.	Run Verify Data on Drive from HDToolbox. For more information refer to the section Backing Up You Hard Disk.	
During format system reports Can't find handler.	System not using most recent Fast File System from the A590 Setup disk.	Copy Fast File System from the L directory on the A590 Setup disk to your boot disk.	
You have an external Seagate drive attached and when you boot from the A590 the system displays the Workbench request	If you can reboot the system and it functions normally after reboot, the time-out is too short.	Set DIP switch 3 to the on position	
An external hard disk appears multiple times on the screen.	The hard disk responds to all logical addresses.	Set DIP switch 2 to the off position	
When you attempt to access the hard disk, the hard disk light turns on and stays on, but the system locks up and permits no further actions.	Drive does not generate a standard parity signal.	Set DIP switch 3 to the on position	

INSTALLING RANDOM ACCESS MEMORY (RAM) CHIPS

You can install RAM chips in the A590 to increase the memory available to your Amiga 500. You can install 512KB, 1MB or 2MB of additional memory.

Note: Use only CMOS 256k x 4 DRAMs, with an access speed of 120 ns or faster. Slower DRAMs will not work properly. Do not use NMOS DRAMs. The use of NMOS DRAMs will damage the A590 and void your warranty.

RAM chips are sensitive to static electricity. Contact with a chip when high levels of static electricity are present could ruin a chip. Touching a nearby grounded metal surface before touching the chip can help reduce static levels.

Installation of RAM chips should be performed by an authorized Commodore Service Center, or by your Commodore dealer. Commodore will not be responsible or liable for any damages caused by improper installation of RAM chips.

APPROVED VENDORS

100 NS NEC UPD424256C-10

MATSUSHITA MN414256-10

120 NS TOSHIBA TC514256P-12

FUJITSU MB81C4256-12P

Note: Commodore will stock 256k x DRAMs, availability will be announced in Techtopies parts section.

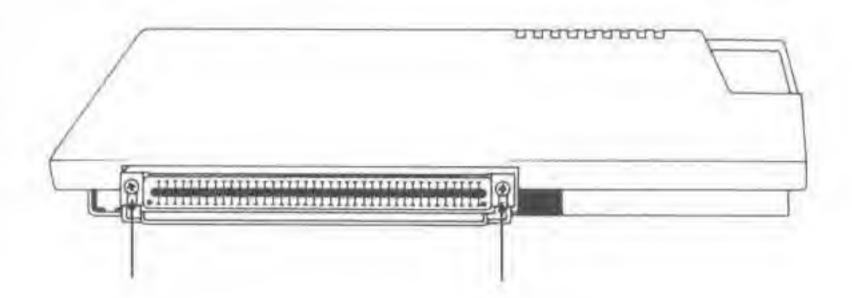
WARNING: If your A590 is connected to the computer, you must first turn off the power, disconnect all cables and peripherals, and detach the A590 from the computer by carefully pulling the A590 directly away from the A500,

The following steps, required to install RAM chips, are explained in detail below:

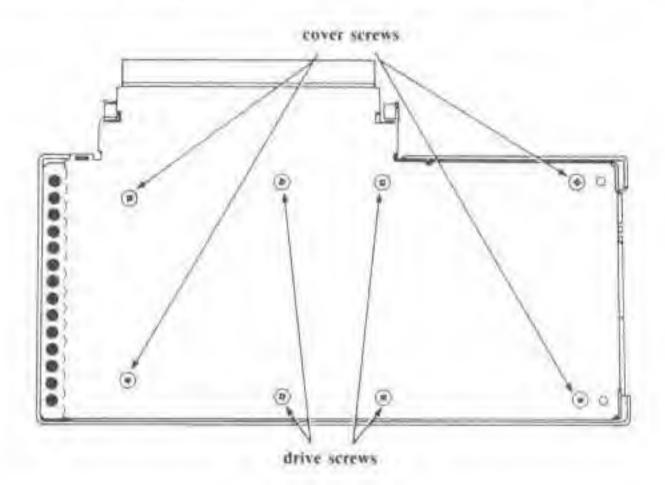
- 1. Remove the A590's cover.
- 2. Remove the drive and the drive shield:
- 3. Insert the RAM chips.
- 4. Set the RAM size jumper.
- 5. Replace the drive and the cover.

1. Removing the A590's cover.

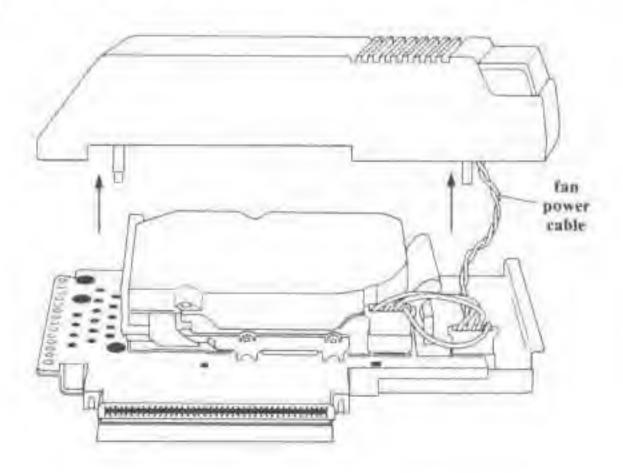
Locate the two screws on the connector side of the A590. Loosen these screws, but do not remove them.



Locate the eight screws on the bottom of the A590. The outer four connect the cover to the base and the inner four connect the drive and the drive shield to the base. Remove the outer four and set these screws aside.

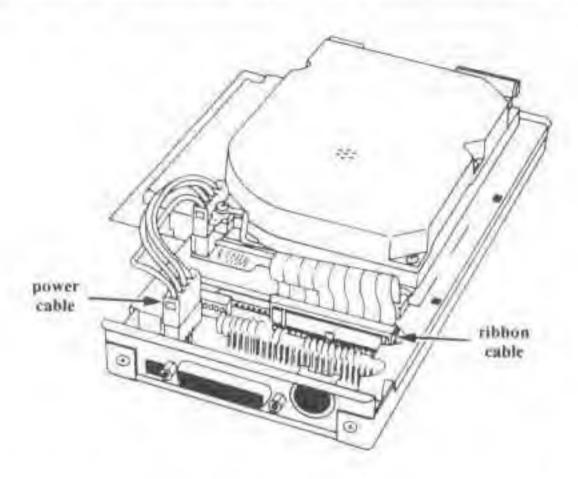


Place the A590 on a flat surface and carefully lift the cover straight off. Disconnect the fan power cable from the printed circuit board in the base of the A590. Note the alignment of the LED cable if there is one, so that it can be reconnected properly. Disconnect the LED cable and put the cover aside.

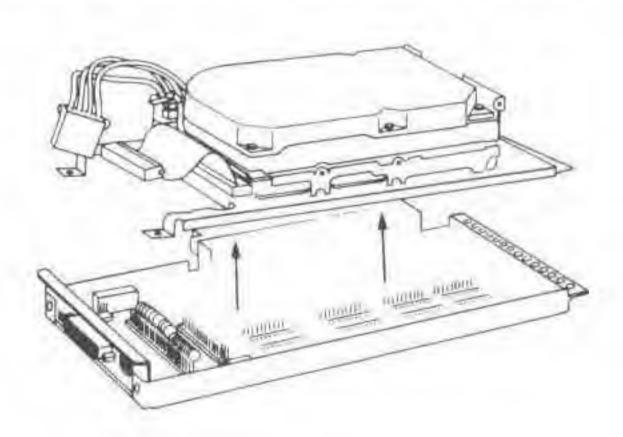


2. Removing the drive and the drive shield.

Locate the ribbon cable connector and the power cable connector and note how they connect to the board, so you can reconnect them properly. Disconnect them from where they attach to the board. Always handle each cable by the plastic connector.



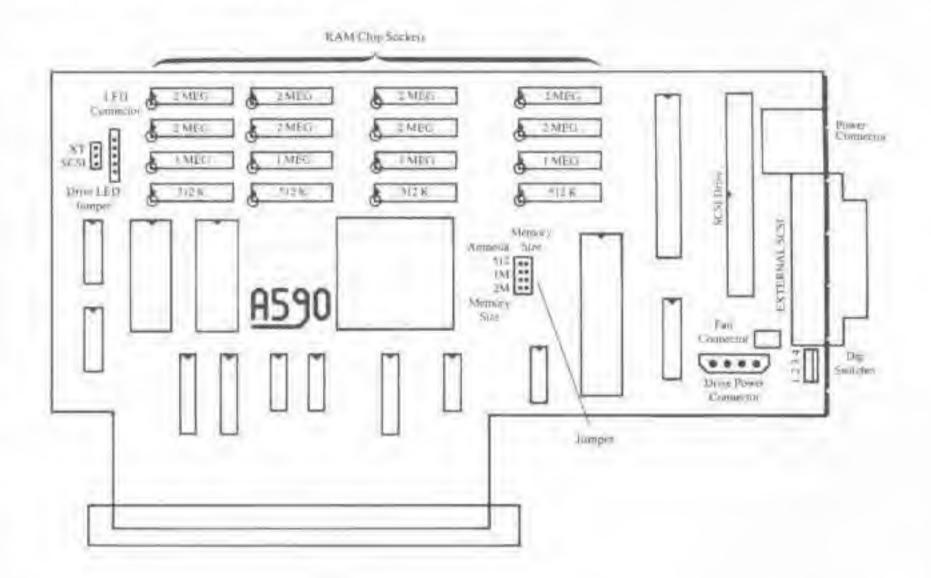
Remove the four remaining screws, and put them aside, separate from the first four screws. Lift the drive and shield off and put them aside in a safe place. Place the drive on a soft, non-conductive surface. Do not subject the drive to shock.



3. Inserting the RAM chips.

Note: Be careful to properly align the chips before inserting them. Do not force them or bend the pins.

Turn the A590's board so that it matches the illustration below. It is important that the chips be inserted properly. Each chip has a notch or dot on one end to show the location of pin 1. When the end with the notch or dot is held to the left, pin 1 will be in the lower left corner. The chip should then be inserted so that the notched end is towards the left of the board. The location for pin 1 is circled in the illustration below. Align the chip with the socket and insert it with slight pressure. If you are installing 512 kilobytes of memory (4 chips), insert them in the four sockets labled 512K. If you are installing 1 megabyte of memory (8 chips), insert them in the eight sockets labled 512K and 1 MEG. If you are installing 2 megabytes of memory (16 chips), insert them in all 16 sockets.



4. Setting the jumper.

You must set a jumper on the board to match the amount of RAM installed. As shipped, the jumper is set to "Amnesia", or no RAM installed. Lift the jumper straight off and replace it on the set of pins labeled the same as the amount of RAM installed.

5. Replacing the drive and the cover.

Replace the drive and the drive shield. Reconnect the ribbon cable and the power cable. Make certain that the connectors and the pins are aligned properly. The power cable connector is shaped so that is can only fit the correct way. Replace the four screws to hold the drive and shield in place. Reconnect the fan's power cable to the board. Reconnect the LED cable if there is one. Replace the cover and install the four screws. Tighten the two screws on the connector side of the A590.

SECTION 4
PARTS SECTION

A590 HDD INTERNATIONAL SHIPPING ASSEMBLIES (312641)

312641-01 SHIPPING ASSY A590 SCSI (US)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-01 PACKING ASSY (US)

312641-08 SHIPPING ASSY A590 SCSI (GERMANY)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (GERMANY)

312641-02 SHIPPING ASSY A590 SCSI (CANADA)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-02 PACKING ASSY (CANADA)

312641-09 SHIPPING ASSY A590 SCSI (FRANCE)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (FRANCE)

312641-03 SHIPPING ASSY A590 SCSI (AUSTRALIA)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-03 PACKING ASSY (AUSTRALIA)

312641-10 SHIPPING ASSY A590 SCSI (NETHERLANDS)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (NETHERLANDS)

312641-04 SHIPPING ASSY A590 SCSI (UK)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-04 PACKING ASSY (UK)

312641-11 SHIPPING ASSY A590 SCSI (SWEDEN)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (SWEDEN)

312641-05 SHIPPING ASSY A590 SCSI (ITALY)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-05 PACKING ASSY (ITALY)

312641-12 SHIPPING ASSY A590 SCSI (DENMARK)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (DENMARK)

312641-06 SHIPPING ASSY A590 SCSI (SPAIN)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-06 PACKING ASSY (SPAIN)

312641-13 SHIPPING ASSY A590 SCSI (NORWAY)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (NORWAY)

312641-07 SHIPPING ASSY A590 SCSI (SWITZERLAND)

363146-01 SHIPPING BOX A590

363147-01 PROTECTION SHEET, SIDE

363148-01 PROTECTION SHEET TOP & BOTTOM

312642-07 PACKING ASSY (SWITZERLAND)

A590 HDD INTERNATIONAL PACKING ASSEMBLIES (312642)

312642-01 PACKING ASSY A590 HDD (US)	312642-04 PACKING ASSY A590 HDD (UK)
312643-01 MAIN ASSY A590 SCSI	312643-01 MAIN ASSY A590 SCSI
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318203-01 BOTTOM FOAM	318203-01 BOTTOM FOAM
316645-01 UPC LAREL (US)	112639-07 POWER SUPPLY - BSI
312639-DI POWER SUPPLY - LIT /CSA	363026-01 USER MANUAL EFFOR
318203-01 BOTTOM FOAM 316645-01 UPC LABEL (US) 312639-01 POWER SUPPLY - UL/CSA 318904-01 WARRANTY CARD - US 90 DAYS	112568 OF EXPANSION PORT CROUND OF ID
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318896-01 SOFTWARE LCENSE AGREEMENT	112642 AS BACKING ARRY ARROUND OFFICE
(US, CANADA, AUSTRL)	312642-05 PACKING ASSY A590 HDD (ITALY)
312341-01 DISKETTE REPLACEMENT CARD (US)	312643-01 MAIN ASSY A590 SCS1
251006-05 ANTI-STATIC BAG	312643-02 MAIN ASSY SUB: 312643-01
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	318202-01 TOP FOAM
312642-02 PACKING ASSY A590 HDD (CANADA)	318203-01 BOTTOM FOAM
	312639-03 POWER SUPPLY - VDE
312643-01 MAIN ASSY A590 SCSI 312643-02 MAIN ASSY SUB: 312643-01	363026-01 USER MANUAL - ITALY
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318202-01 TOP FOAM	
318203-01 BOTTOM FOAM	
312639-01 POWER SUPPLY - UL/CSA	312642-06 PACKING ASSY A590 HDD (SPAIN)
318882-01 WARRANTY REGISTRATION - CANADA	312643-01 MAIN ASSY A590 SCS1
363026-01 USER MANUAL - EFIGS	312643-02 MAIN ASSY SUB: 312643-01
312568-01 EXPANSION PORT GROUND CLIP	363143-01 INDIVIDUAL PACKING BOX (EFIGS)
318896-01 SOFTWARE LCENSE AGREEMENT	318202-01 TOP FOAM
(US, CANADA, AUSTRL)	318203-01 BOTTOM FOAM
318556-02 DISKETTE REPLACEMENT CARD	312639-03 POWER SUPPLY - VDE
(CANADA)	363026-02 USER MANUAL - SPANISH
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312643-02 MAIN ASSY SUB: 312643-01	312642-07 PACKING ASSY A590 HDD
363143-01 INDIVIDUAL PACKING BOX (EFIGS)	(SWITZERLAND)
318202-01 TOP FOAM	312643-01 MAIN ASSY A590 SCSI
318203-01 BOTTOM FOAM	312643-02 MAIN ASSY SUB: 312643-01
312639-05 POWER SUPPLY - SAA	363143-01 INDIVIDUAL PACKING BOX (EFIGS)
318884-01 WARRANTY CARD - AUSTRALIA	318202-01 TOP FOAM
363026-01 USER MANUAL - EFIGS	318203-01 BOTTOM FOAM
312568-01 EXPANSION PORT GROUND CLIP	312639-04 POWER SUPPLY - SEV
318896-01 SOFTWARE LCENSE AGREEMENT	363026-01 USER MANUAL - GERMAN
(US, CANADA, AUSTRL)	380933-01 WARRANTY CARD (SWITZERLAND)
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SUB: 312643-01

A590 HDD INTERNATIONAL PACKING ASSEMBLIES (312642) (continued)

312642-08 PACKING ASSY A590 HDD (GERMANY) 312642-11 PACKING ASSY A590 HDD (SWEDEN) 312643-01 MAIN ASSY A590 SCS1 312643-01 MAIN ASSY A590 5CS1 SUB: 312643-01 312643-02 MAIN ASSY 312643-02 MAIN ASSY 363143-01 INDIVIDUAL PACKING BOX (EFIGS) 363143-01 INDIVIDUAL PACKING BOX (EFIGS) 318202-01 TOP FOAM 318202-01 TOP FOAM 318203-01 BOTTOM FOAM 318203-01 BOTTOM FOAM 312639-03 POWER SUPPLY - VDE 312639-03 POWER SUPPLY - VDE 363214-01 USER MANUAL - SWEDISH 363211-01 USER MANUAL - GERMAN 320046-06 WARRANTY CARD (GERMANY) 312568-01 EXPANSION PORT GROUND CLIP 312568-01 EXPANSION PORT GROUND CLIP 251006-05 ANTI-STATIC BAG 251006-05 ANTI-STATIC BAG

312642-09	PACKING ASSY A590 HDD (FRANCE)	
312643-01	MAIN ASSY A590 SCS1	
312643-02	MAIN ASSY SUB: 312643-01	
363143-01	INDIVIDUAL PACKING BOX (EFIGS)	
318202-01	TOP FOAM	
318203-01	BOTTOM FOAM	
312639-03	POWER SUPPLY - VDE	
363027-01	USER MANUAL - FRENCH	
325254-01	WARRANTY CARD (FRANCE)	
312568-01	EXPANSION PORT GROUND CLIP	

251006-05 ANTI-STATIC BAG

251006-05 ANTI-STATIC BAG

312642-10 PACKING ASSY A590 HDD (NETHERLANDS) 312643-01 MAIN ASSY A590 SCSI 312643-02 MAIN ASSY SUB: 312643-01 363143-01 INDIVIDUAL PACKING BOX (EFIGS) 318202-01 TOP FOAM 318203-01 BOTTOM FOAM 312639-03 POWER SUPPLY - VDE 363213-01 USER MANUAL - DUTCH 312568-01 EXPANSION PORT GROUND CLIP

	PACKING ASSY A590 HD	DE THINKS
312643-01	MAIN ASSY A590 SCSI	
312643-02	MAIN ASSY	SUB: 312643-01
363143-01	INDIVIDUAL PACKING I	BOX (EFIGS)
318202-01	TOP FOAM	
318203-01	BOTTOM FOAM	
312639-03	POWER SUPPLY - VDE	
363215-01	USER MANUAL - DANIS	H
312568-01	EXPANSION PORT GROU	JND CLIP
251006-05	ANTI-STATIC BAG	

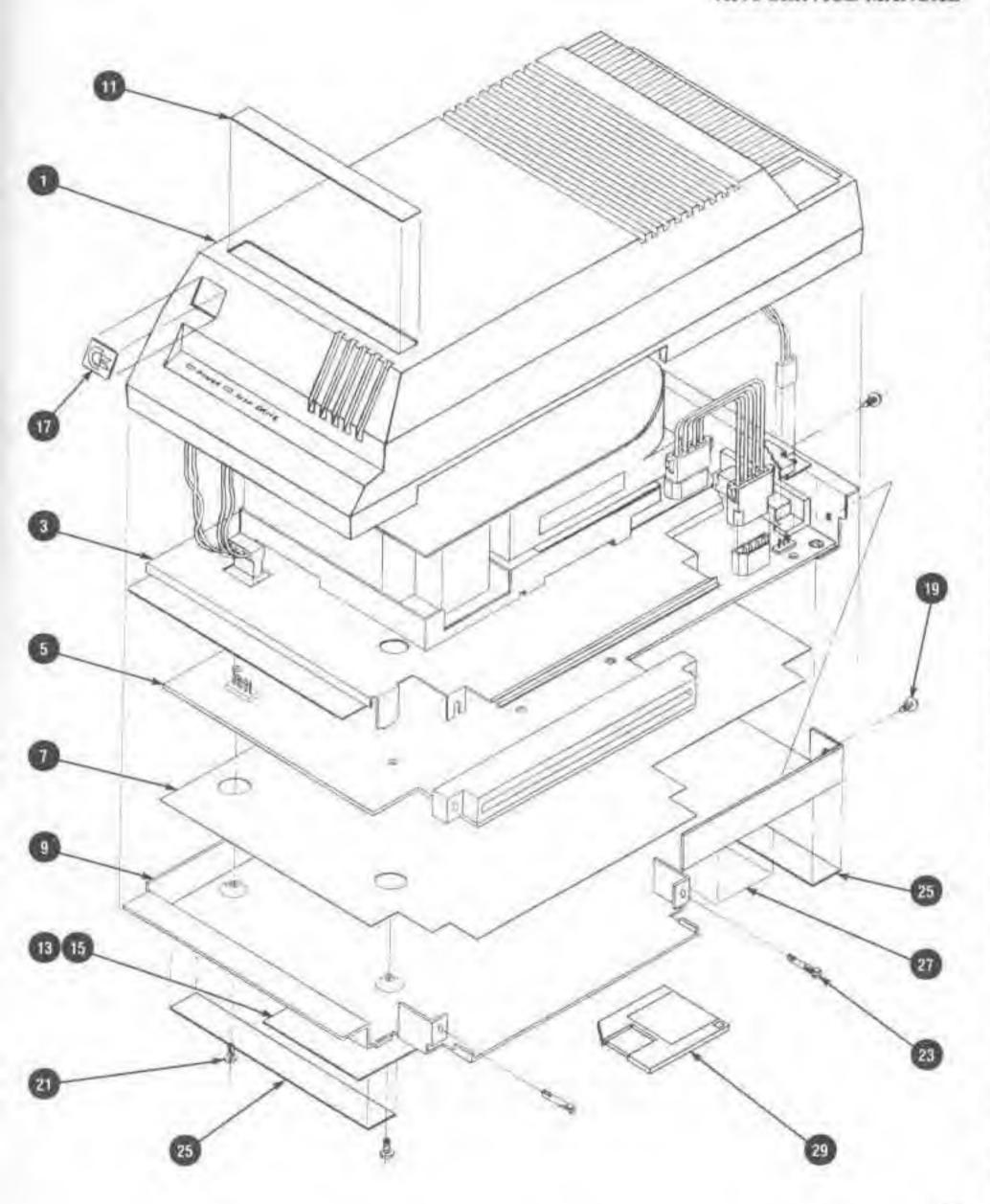
314044-13	PACKING ASST ASSU HDD INDRWAT
312643-01	MAIN ASSY A590 SCSI
312643-02	MAIN ASSY SUB: 312643-01
363143-01	INDIVIDUAL PACKING BOX (EFIGS)
318202-01	TOP FOAM
318203-01	BOTTOM FOAM
312639-03	POWER SUPPLY - VDE
363215-01	USER MANUAL - NORWEGIAN
312568-01	EXPANSION PORT GROUND CLIP
251006-05	ANTI-STATIC BAG

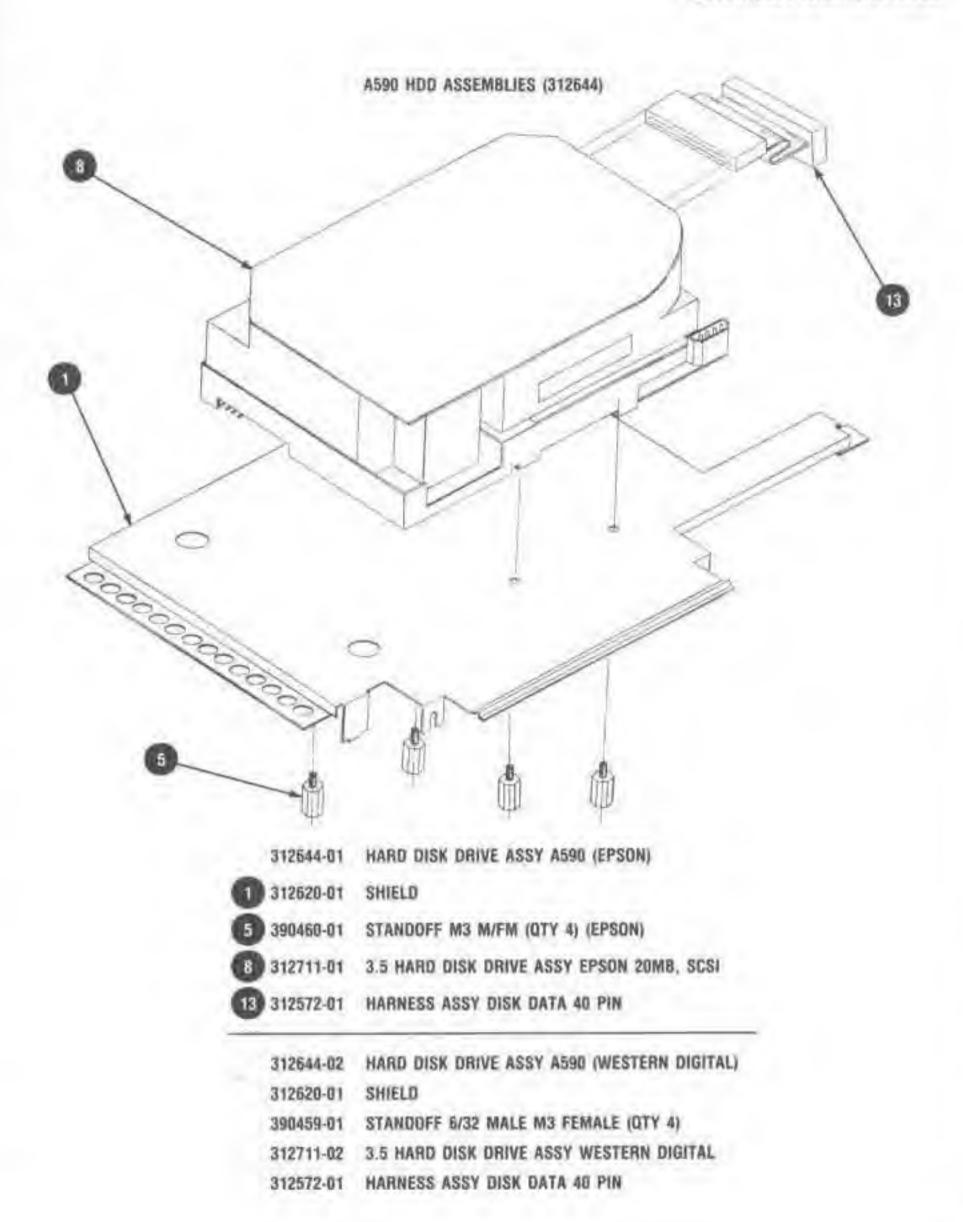
SELD TO BACKTAC ASSV ASSA HIND MODWAY

A590 HDD MAIN ASSEMBLIES (312643)

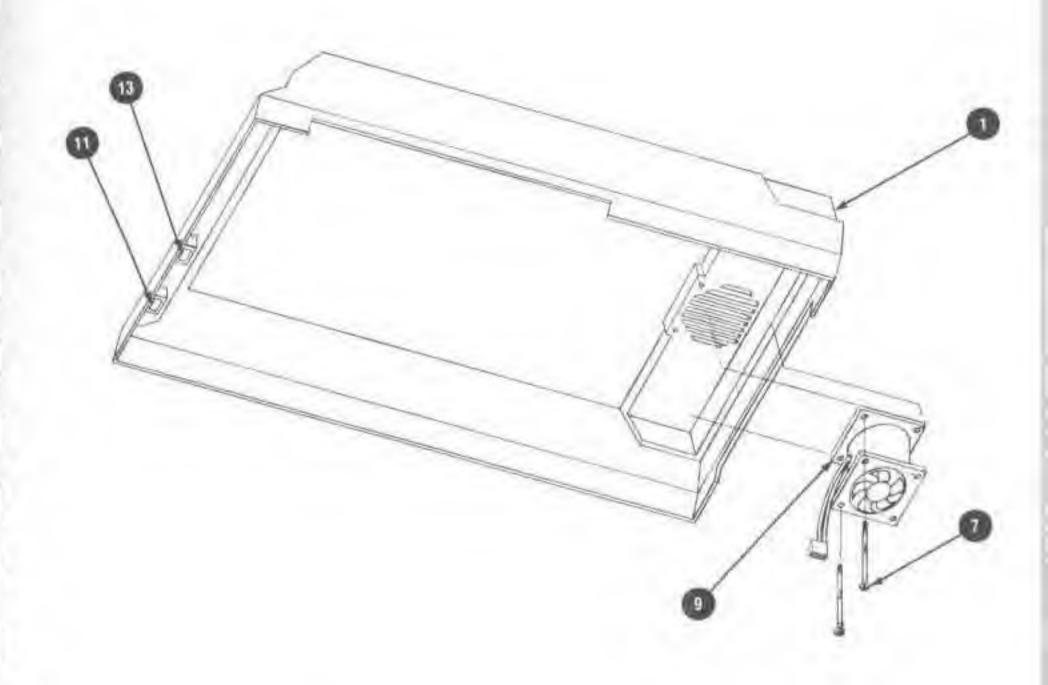
312643-01 MAIN ASSY A590 SCSI HARD DISK (SK (EPSON)
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- 312647-D1 TOP COVER ASSY
- 312644-01 HARD DISK DRIVE ASSY EPSON 20MB
- 312875-01 PCB & CONNECTOR PANEL ASSY (W/ EPSON)
- 312621-01 INSULATION SHEET
- 312622-01 BASE
- 312623-01 NAMEPLATE A590 SCSI HD
- 312636-02 RATING LABEL (MADE IN HK)
- 15 312636-01 RATING LABEL (MADE IN US) SUB: 312636-02
- 17 380133-03 LOGO
- 19 906800-07 SCREW, METRIC, M3 X 5.0 LONG (QTY 4)
- 21) 906883-81 SCREW CROSS RECESSED M3 X 8 (QTY 4)
- 28 906800-02 SCREW METRIC M3 X 10.0 LONG (OTY 2)
- 25 312577-01 FOOT LABEL (GTY 2)
- 27 316661-01 HARD DISK INFORMATION LABEL EPSON 20MB
- 29 317734-01 CONTROLLER DISK ASSY (EPSON)
 - 312643-02 MAIN ASSY A590 SCSI HARD DISK (WESTERN DIGITAL)
 - 312647-01 TOP COVER ASSY
 - 312644-02 HARD DRIVE ASSY WESTERN DIGITAL
 - 312875-82 PCB & CONNECTOR PANEL ASSY (W/ WESTERN DIGITAL)
 - 312621-01 INSULATION SHEET
 - 312622-01 BASE
 - 312623-01 NAMEPLATE A590 SCSI HD
 - 312636-02 RATING LABEL (MADE IN HK)
 - 312636-01 RATING LABEL (MADE IN US) SUB: 312636-02
 - 380133-03 LOGO
 - 906800-07 SCREW, METRIC, M3 X 5.0 LONG (QTY 4)
 - 906883-01 SCREW CROSS RECESSED M3 X 8 (QTY 4)
 - 906808-02 SCREW METRIC M3 X 10.0 LONG (OTY 2)
 - 312577-01 FOOT LABEL (QTY 2)
 - 316661-02 HARD DISK INFORMATION LABEL WESTERN DIGITAL
 - 317734-02 CONTROLLER DISK ASSY (WESTERN DIGITAL)





A590 HDD TOP COVER ASSEMBLY (312647)



312647-01 TOP COVER ASSY, A590 SCSI / DISK DRIVE

312648-01 TOP COVER

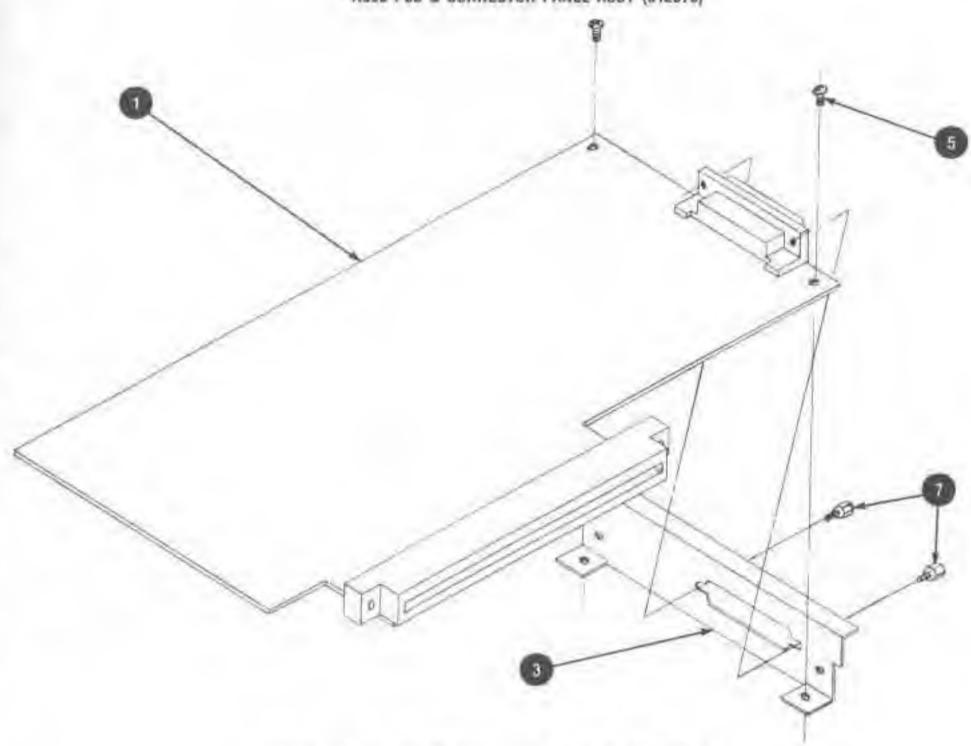
906883-17 SCREW, SELF - TAPPING, M3 X 35 MM LG.

3 312611-01 FAN ASSY, A590 SCSI

312160-02 LED LENS GREEN

18 312160-03 LED LENS YELLOW

A590 PCB & CONNECTOR PANEL ASSY (312875)



312875-01 PCB & CONNECTOR PANEL ASSY (LEFT DATA)

312615-01 PCB ASSY (EPSON)

312649-01 BASE CONNECTOR PANEL

906800-07 SCREW, METRIC, M3 X 5.0 LG. (OTY 2)

390251-01 STANDOFF HEX MALE/FEMALE 5MM

312875-02 PCB & CONNECTOR PANEL ASSY (RIGHT DATA)

312615-02 PCB ASSY (WESTERN DIGITAL)

312649-01 BASE CONNECTOR PANEL

905800-07 SCREW, METRIC, M3 X 5.0 LG. (QTY 2)

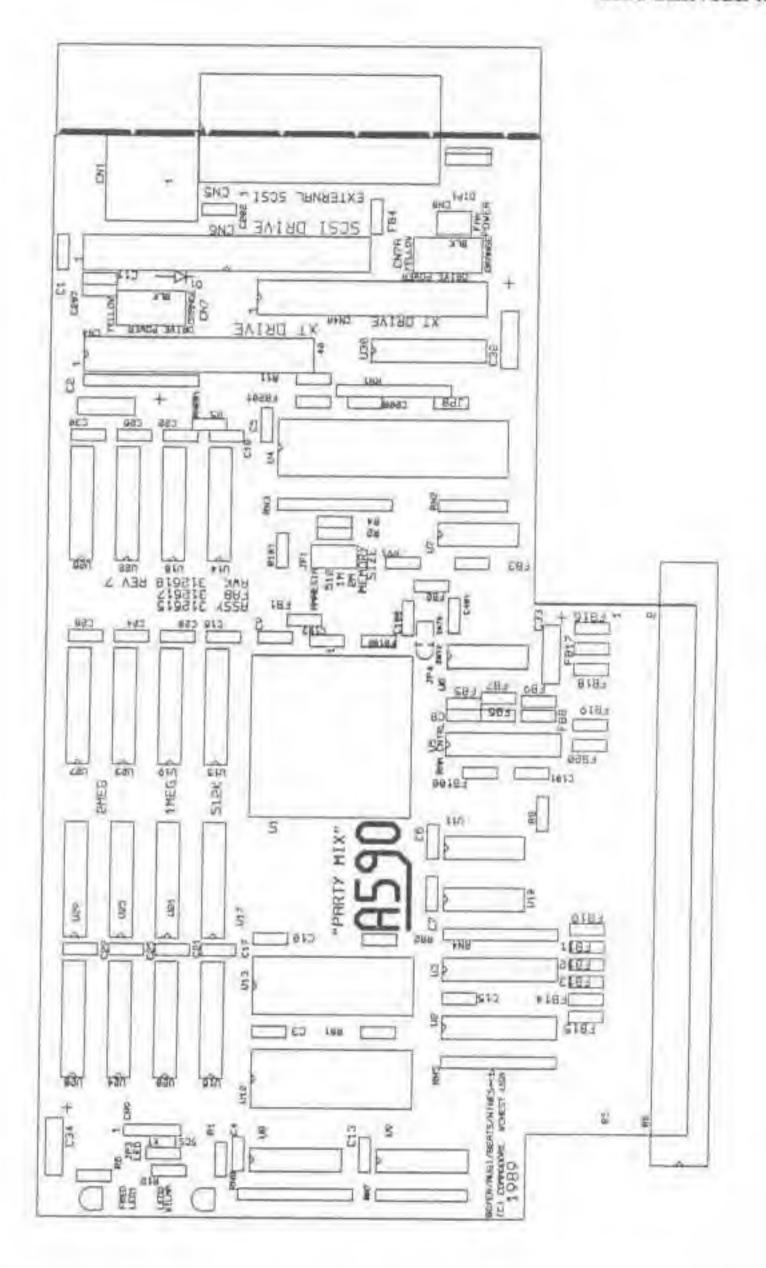
390251-01 STANDOFF HEX MALE/FEMALE 5MM

COMPONENT PARTS LIST PCB ASSEMBLY #312615, REV. F, 5/11/89

312615-01 — PCB ASSY A590 SCSI (HD LEFT CABLE) EPSON 312615-02 — PCB ASSY A590 SCSI (HD RIGHT CABLE) WESTERN DIGITAL

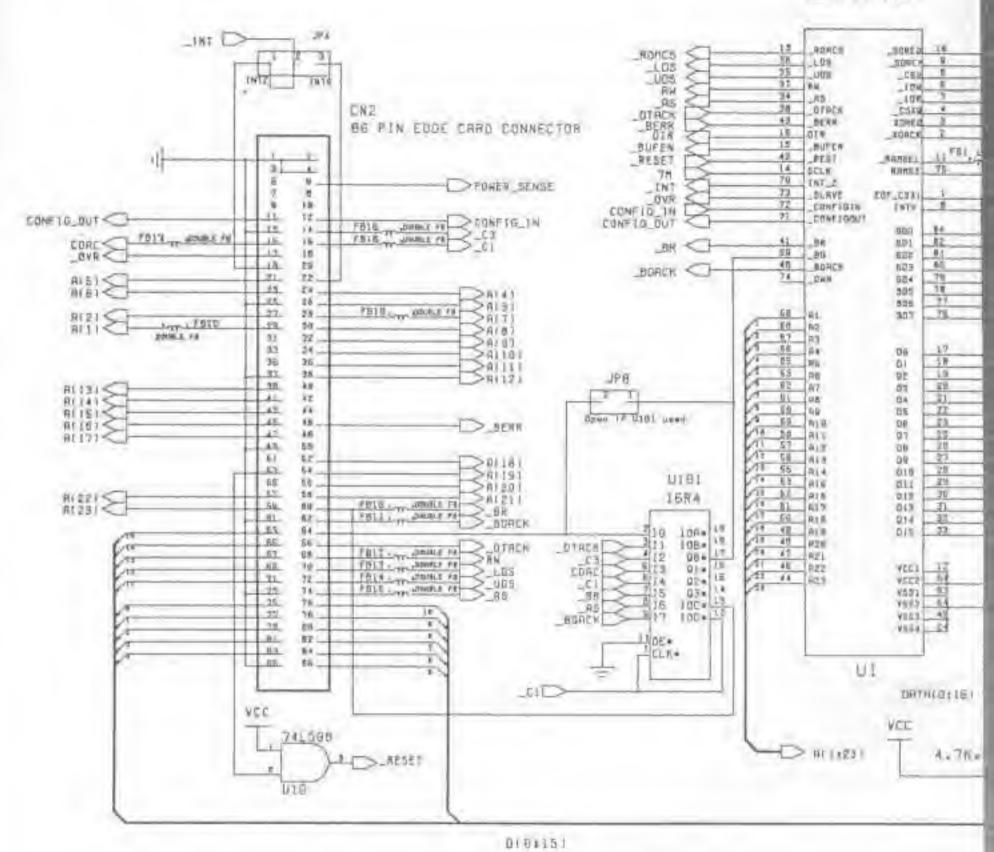
Commodore part numbers are provided for reference only and do not indicate the availability of parts from Commodore. Industry standard parts (Resistors, Capacitors, Connectors) should be secured locally. Approved cross-references for TTL chips, Transistors, etc. are available in manual form through the Service Department, order #314000-01.

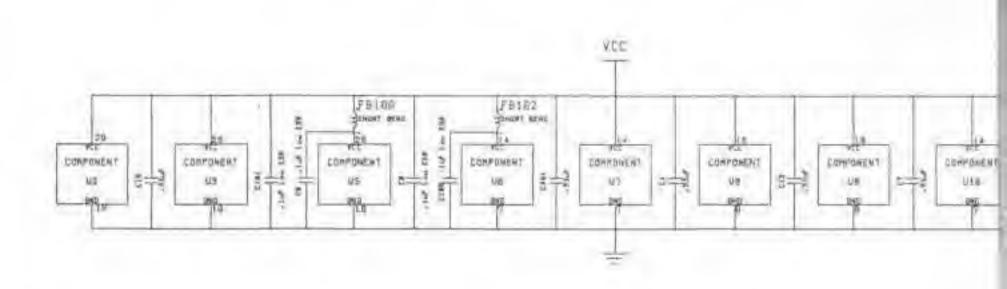
IC COMP	ONENTS		RESISTOR	NETWORKS (constraint)	
901323-46 390388-01 390389-01	IC, 74F1SRA IC, 74LS245 IC, ROM - ODD IC, BOM - EVEN	U) I U) I I U) I I I U) I I I I I I I I I I I I I I I I I I I	901550-105 901550-01 901550-131	130 EHM 1/4W 33 OHM 1/4W 1K, 1/4W 56 OHM 1/4W 0 OHM 1/4W	R6 R1_R5_R31 R2_R4_R4_R31 R12_R301 JPS_USE_W/390563-02 DNLY
901521-12	IC, 74L585 IC, DMAC	136	CAPACIT	ORS	
390363-02 901523-30 390206-03 390398-01	IC, DMAC ENHANCED IC, 7407 IC, WD33C93 IC, 16R4A PROGRAMMED	UI SUB UI U7 U/4 U/10) (USE W/390363-01 ONLV)	990188-03 900101-17 900020-09	22 UF 35VV 22 UF 35VV 33 UF RADIAL	SUB: C2,C33,C34 C2,C33,C34 (MAX LENGTH < -13 MM C1,C3,C4,C6,C7,C10, C11,C13,C15-10,C24
	IC. 74LS08	U10	390397-01	1 UF LOW ESR	22,C24-26,C28-30,C401 C5,C8,C9,C100,C101
MISCELL	ANEOUS.		THEOLETICAL TOTAL	THE LIGHT COR	C202,C207,C208
390017-01	DIODE, IN914	(13)	CONNECT	TORES	1 (1002/1-101/1/1-100)
904)50-06 390)83-01	SOCKET 84 PIN	FB4-FB20 2 INCH SPACING FB1-FB3 LED2 LED1 JP (U3,U14-28, (U10) W/390561-01 ONLY) U12,U13 U4		CARD EDGE, 86PIN 2 PIN 4 PIN POLARIZED 4 PIN POLARIZED CABLE W/DRV PWR CABLE W/DRV PWR HEADER 40 PIN DIL ROUND DIN 4 PIN	CNI, JP3 1 INCH CENTERS CN7 (USE W/312615-03) CN7A (USE W/312615-02) CN7 SUB: 390462-01 CN7A SUB: 390462-01 CN7A SUB: 390462-01 CN4A USE W/312615-02 CN1
	NETWORKS		312789-01	HARDNESS ASSY DRY PWR	USB W/390462-0)
380388-01 902422-03 390227-04	220/330 x # 220/330 x 4 33 OHM x 4 33 OHM x 5 SIP 4.7K x 9	RNI,RNI RNI RNI RNI RNA,RNS,RNIDI	390272-01 39024) 405 903345-20 390363-01 903345-25	ROUND DIN 4 FIN SHIELED DB25 FEMALE, RT ANGLE HEADER 40 PIN DIL SWITCH 4POS DIP RT ANGLE 50 PIN	CNI SUB: 359004-01 CNS CNA SWI CN6

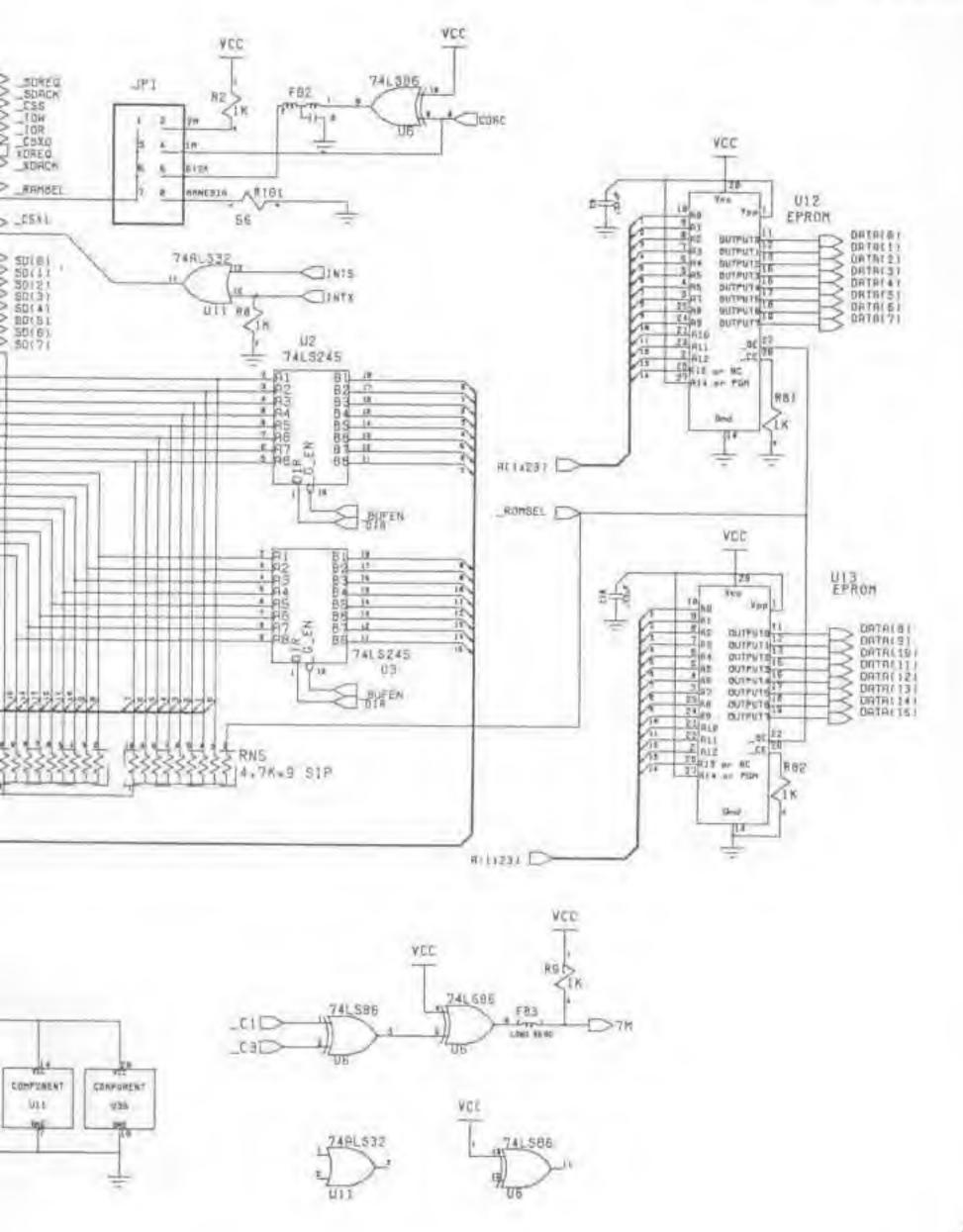


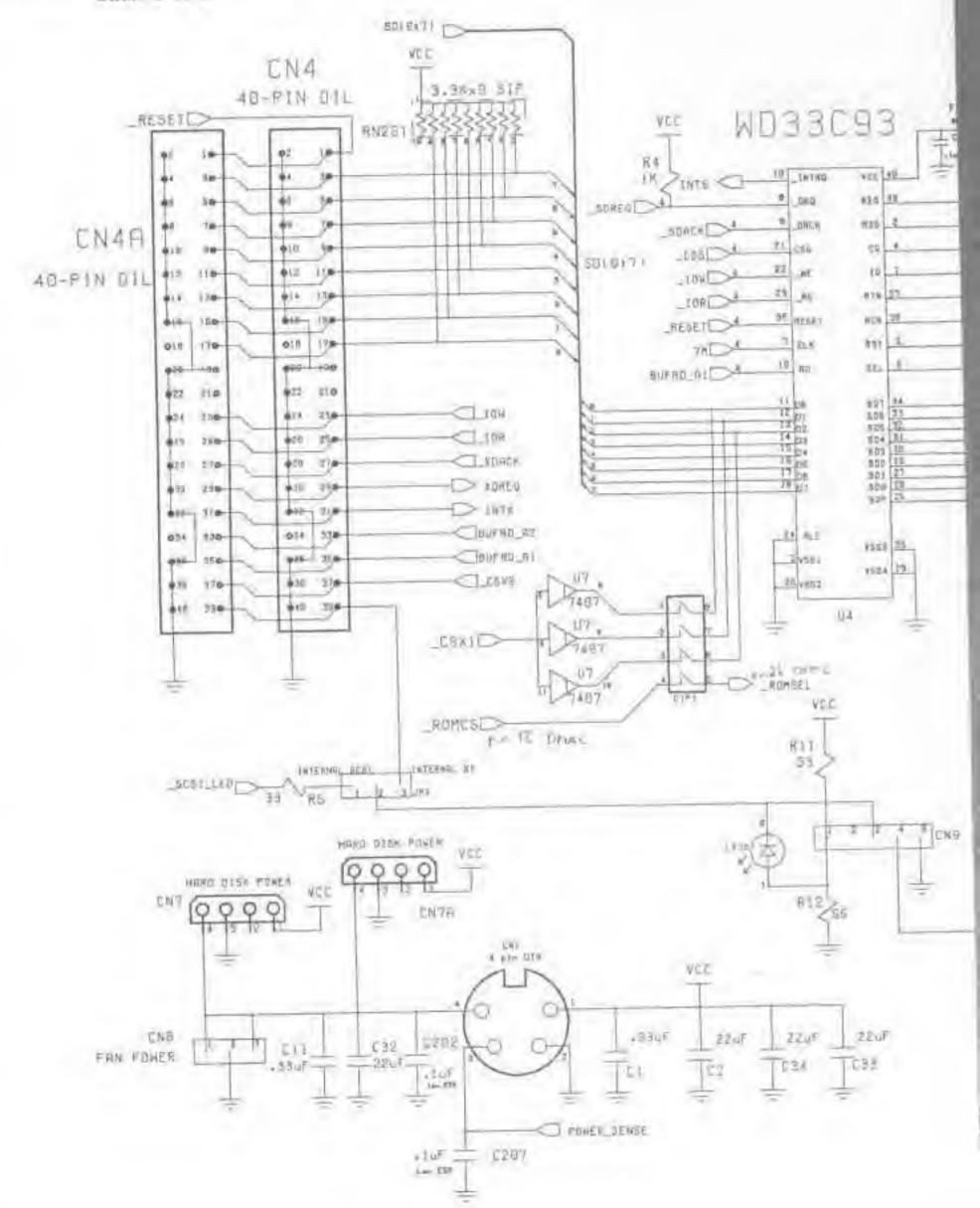
SECTION 5

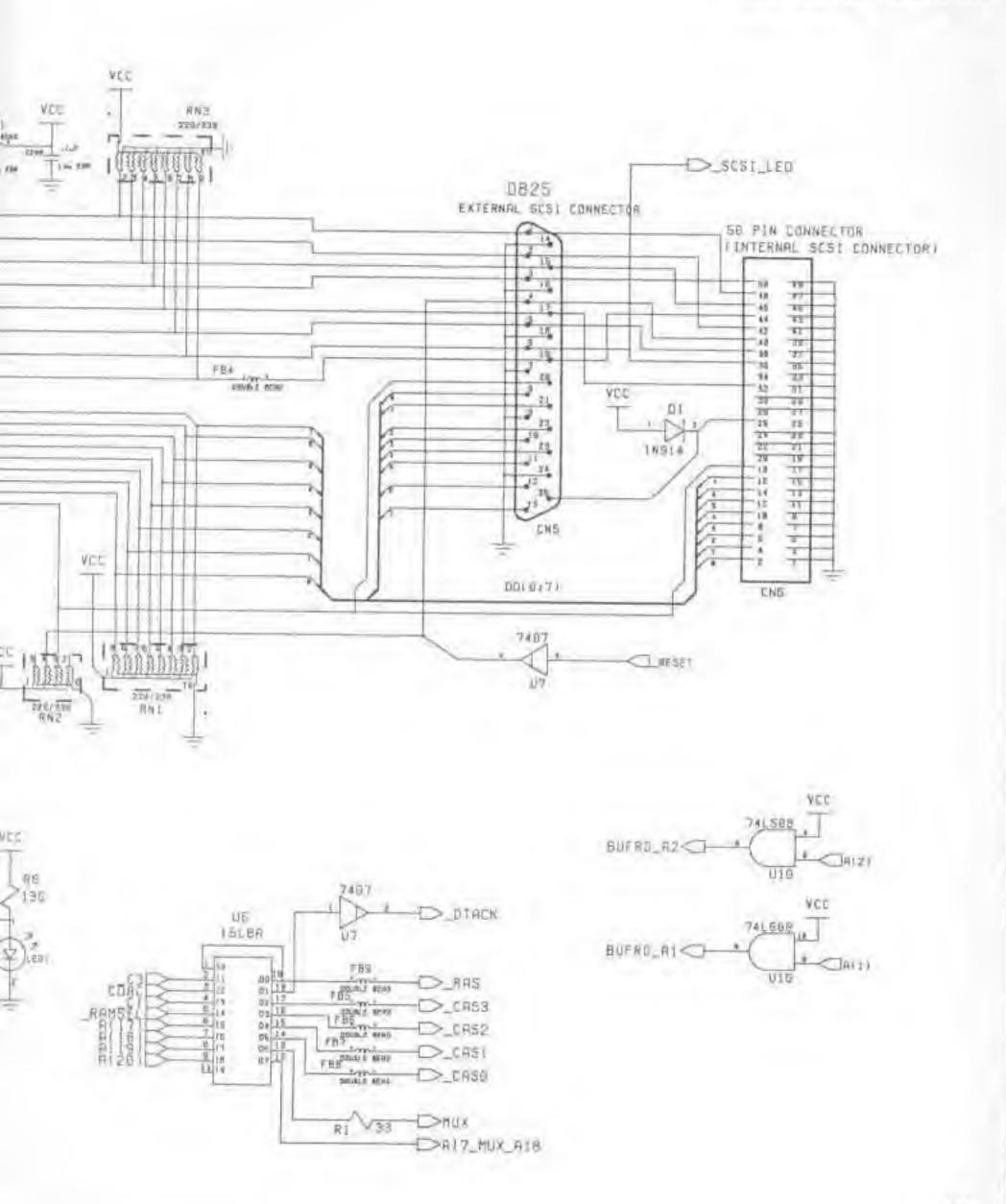
- SCHEMATICS
- CONNECTORS
- DIP SWITCHES
- JUMPERS

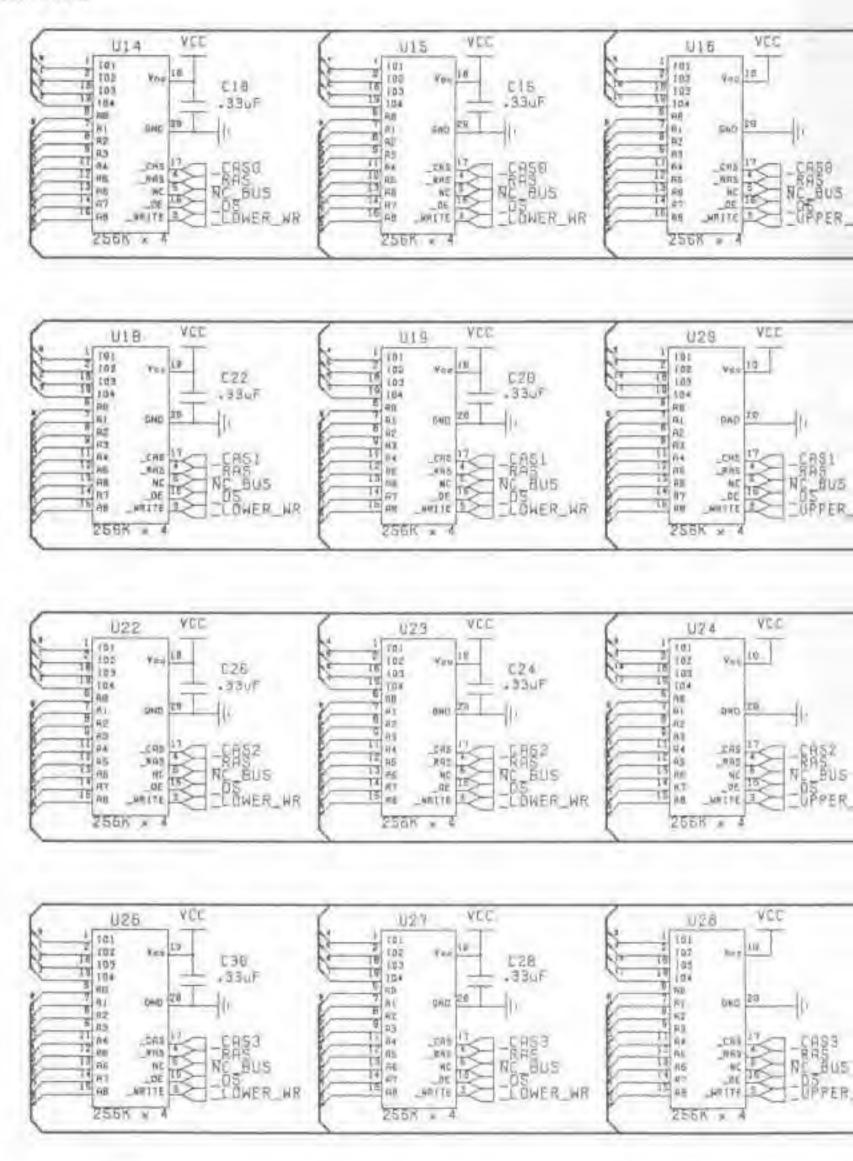


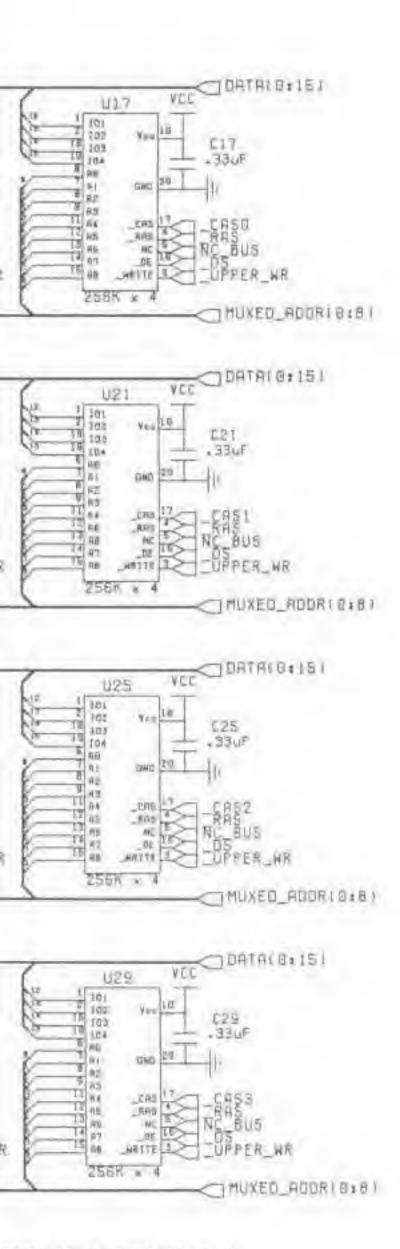


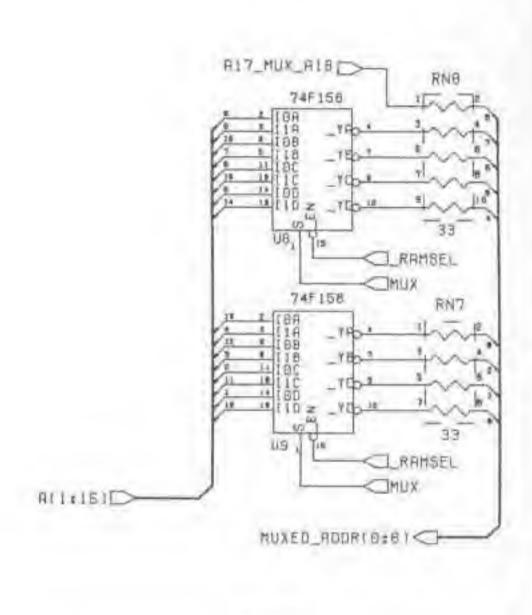


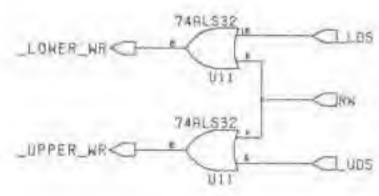










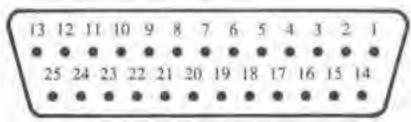


Internal SCSI Connector

_		-	Pin	Name
	50	■ 49	50	T/O
	48	■ 47	48	REQ
	46	o 45	46	C/D
	44	e 43	44	SEL.
	42	# 41	42	MSG
	40	• 39	40	RST
	38	o 37	38	ACK
	36	o 35	36	BSY
	34	o 33	34	N.C.
	32	e 31	32	ATN
	30	e 29	30	Ground
	28	■ 27	28	Ground
	26	e 25	26	Termination Power
	24	23	24	Ground
	22	o 21	22	Ground
	20	• 19	20	Ground
	18	• 17	18	Parity
	16	o 15	16	Data 7
	14	• 13	14	Data 6
	12	• 11	12	Data 5
	10	. 9	10	Data 4
	8	. 7	8	Data 3
	6	0 5	6	Data 2
	4	0 3	4	Data 1
	2	• 1	2	Data 0

All odd pins, except pin 25, are ground. Pin 25 is open.

External SCSI Connector (DB-25)



Female Connector

Pin	Name	Pin	Name
1	REQ	14	Ground
2	MSG	15	C/D
3	1/0	16	Ground
4	RST	17	ATN
5	ACK	18	Ground
6	BSY	19	SEL
7	Ground	20	Parity
8	Data 0	21	Data I
9	Ground	22	Data 2
10	Data 3	23	Data 4
1.1	Data 5	24	Ground
12	Data 6	25	Termination Power
13	Data 7		

Hard Disk Power Connector

.85 A Maximum Continuous Current at 12V 2.6 A Maximum Startup Current at 12V 1.0 A Maximum Continuous Current at 5V

Internal XT Connector

		Pin	Name
• 40	o 39	39	Not used
• 38	o 37	37	Select
• 36	e 35	35	Addr 1
34	o 33	33	Addr 0
• 32	· 31	31	Lat
• 30	o 29	29	Data-Req
o 28	e 27	27	Data-Ack
· 26	o 25	25	10Rend
· 24	e 23	23	10Write
• 22	e 21	21	Not used
20	o 19	19	Ground
8 IS	o 17	17	Data 0
B 16	0 15	15	Data 1
o. 14	· 13	13	Data 2
e 12	• 11	Ti	Data 3
• 10	0 9	9	Data 4
· 8	0 7	7	Data 5
. 6	0 5	5	Data 6
0 4	0 3	3	Data 7
. 2	0 1	1.	Reset

Pins 18 and 34 are not used. All other even pins are ground.

1. +5 VDC	
2. ground	0000
3. ground	1234

4. +12 VDC

4. +12 VDC

Fan Conn

Fan Conne	ofor	
Fan Conne		
	1. +12 VDC	Lane I
	2. ground	0 0 0
	3. +12 VDC	1 2 3
4 Pin Pow	er Connector	
	1. +5 VDC	1000
	2. ground	()
	3. power sense	1

DIP Switch Settings

Switch I off on	Disables Autoboot ROMs Enables Autoboot ROMs	Kickstart 1.2 Kickstart 1.3
Switch 2 off on	LUN disabled LUN enabled	One drive at each address. Multiple drives at each address.
Switch 3 off	Time-out disabled	Short wait period for drive, Parity checking enabled.
on	Time-out enabled	Long wait period for drive. Parity checking disabled on message-in phase.

Switch 4 Reserved for future enhancements.

Note: Parity checking is not supported on some Seagate drives. With these drives set the switch to the on position.

Switch 1: Autoboot Enable If you are using Rickstart 1.3, you can set Switch 1 to the on position. This will allow you to boot your system from the A590.

Switch 2: LUN Enable

This switch only affects SCSI drives. If you have more than one device at a physical address, set Switch 2 to on. When this switch is in the off position, the system only looks for one Logical Unit Number (LUN), or one drive, at each physical SCSI address. When this switch is in the on position the system will attempt to open Logical Units 0 through 7 at each physical SCSI address.

Note: Some SCSI drives such as certain SeagateTM and EpsonTM drives, respond to more than one logical address. Such drives will appear on the Workbench screen and the Hard Drive Preparation, Partitioning and Formatting screen in HDToolbox multiple times, at the same Address but at LUN 0 through 7. If this occurs, set switch 2 to off.

Switch 3: Time-out Length

If you are using a SCSI drive that takes longer than thirty seconds to start up when you turn the system on, such as some Seagate drives, you may wish to set Switch 3 to on. When the switch is on the off position the time-out period, (the time between the start-up and when the system checks the drive), is short. When the switch is in the on position the system will wait a longer time before checking the drive.

Switch 4: Reserved.

Jumper Settings

Note: The A590 is shipped with all jumpers set to 1.

JUMPER	SETTING	NOTES
JPt	1. Amnesia 2. 512K 3. IMB 4. 2MB	RAM memory installed on the A590 board.
JP3	1. XT drive 2. SCSI drive	Sets LED for XT or SCSI drive.
JP4	1. Interrupt 2 2. Interrupt 6	Interrupt level. Not user adjustable.