

San Diego State University
CS 370 Computer Architecture

Homework Assignment 1 [100 points, Weight 8%]

- You are required to write by hand legibly to solve each problem. When you plot a circuit, please make sure that you use a ruler. You are not allowed to use any software to draw a circuit or answer any question. An answer that is not written by your hand will receive zero credit for the corresponding problem.
- Please write down your full name and SDSU Red ID on the first scratch paper. After you finish writing all your answers on multiple scratch papers (print papers preferred), please take a photo for each of them. Next, please insert these image files into a Word file. Finally, convert the Word file into a single PDF file, and then, submit the PDF file from within Canvas.
- Please note that we only accept your PDF file submissions. A submission in any other format will not be graded and will receive zero credit. For example, submitting multiple image files will receive zero credit.
- Please pay special attention to the due date – a late submission will receive zero.
- For your convenience, the text of each question is appended to this document.
- Please do the following problems from the textbook, and submit solutions.

1. [6 points] Problem 1-9 {please ignore the “Octal” part} (each sub-question is 1 point)
2. [6 points] Problem 1-12 (each sub-question is 2 points)
3. [6 points] Problem 2-3 (a)
4. [10 points] Problem 2-6 (each sub-question is 2 points)
5. [10 points] Problem 2-11 {please ignore “maxterms of each function” in (a)} (each sub-question is 2 points)
6. [6 points] Problem 2-17 (each sub-question is 3 points; in each sub-question, a correct K-map is 1 point and a correct equation is 2 points)
7. [9 points] Problem 2-20 (each sub-question is 3 points; in each sub-question, finding all prime implicants deserves 1 point, finding all essential prime implicants deserves 1 point, final function after applying the selection rule deserves 1 point)
8. [11 points] Problem 3-9 (the truth table is 2 points, each **optimized** equation deserves 2 points, a K-map for each equation deserves 1 point). For each output, you need to draw a K-map and then write down an optimized equation based on your K-map. Providing a non-optimized equation will receive zero for that equation part.
9. [6 points] Problem 3-28 (**hint:** You can fully use one 3-8 decoder so that you can have 3 inputs and 8 outputs. Meanwhile, you can partially use the second 3-8 decoder so that you have 1 input and 2 outputs. For this second 3-8 decoder, you only need to use one of its inputs [you can connect the other two inputs to the ground so that these two inputs are always zero] and two of its 8 outputs [the other six outputs are simply not used]. Note that each of the two outputs of the second 3-8 decoder controls 8 2-input AND gates).
10. [6 points] Problem 3-36.

11. [6 points] Problem 3-37 (a) (**hint:** An 8x2 AND-OR means 8 AND gates [each of these 8 AND gates has two inputs] and one OR gate with 8 inputs. The 8 outputs of the 8 AND gates are the inputs of the OR gate).
12. [4 points] Problem 3-52 ([each sub-question is 1 point](#))
13. [4 points] Problem 3-53 ([each sub-question is 1 point](#))
14. [4 points] Problem 3-55 ([each sub-question is 1 point](#))
15. [6 points] Problem 3-57

- 1-9.** *Convert the following numbers from the given base to the other three bases listed in the table:

<u>Decimal</u>	<u>Binary</u>	<u>Hexadecimal</u>
369.3125	?	?
?	10111101.101	?
?	?	F3C7A

- 1-12.** Perform the following binary multiplications:

(a) 1010×1100 (b) 0110×1001 (c) 1111001×011101

- 2-3.** +Prove the identity of each of the following Boolean equations, using algebraic manipulation:

(a) $AB\bar{C} + B\bar{C}\bar{D} + BC + \bar{C}D = B + \bar{C}D$

- 2-6.** Simplify the following Boolean expressions to expressions containing a minimum number of literals:

(a) $\bar{A}\bar{C} + \bar{A}BC + \bar{B}C$

(b) $\overline{(A + B + C)} \cdot \bar{A}\bar{B}\bar{C}$

(c) $AB\bar{C} + AC$

(d) $\bar{A}\bar{B}D + \bar{A}\bar{C}D + BD$

(e) $(A + B)(A + C)(A\bar{B}C)$

2-11. For the Boolean functions E and F , as given in the following truth table:

X	Y	Z	E	F
0	0	0	0	1
0	0	1	1	0
0	1	0	1	1
0	1	1	0	0
1	0	0	1	1
1	0	1	0	0
1	1	0	1	0
1	1	1	0	1

- List the minterms of each function.
- List the minterms of \bar{E} and \bar{F}
- List the minterms of $E + F$ and $E \cdot F$.
- Express E and F in sum-of-minterms algebraic form.
- Simplify E and F to expressions with a minimum of literals.

2-17. Optimize the following Boolean functions, using a map:

(a) $F(W, X, Y, Z) = \sum m(0, 1, 2, 4, 7, 8, 10, 12)$

(b) $F(A, B, C, D) = \sum m(1, 4, 5, 6, 10, 11, 12, 13, 15)$

2-20. Optimize the following Boolean functions by finding all prime implicants and essential prime implicants and applying the selection rule:

(a) $F(A, B, C, D) = \sum m(1, 5, 6, 7, 11, 12, 13, 15)$

(b) $F(W, X, Y, Z) = \sum m(0, 1, 2, 3, 4, 5, 10, 11, 13, 15)$

(c) $F(W, X, Y, Z) = \sum m(0, 1, 2, 5, 7, 8, 10, 12, 14, 15)$

3-9. +Design a combinational circuit that accepts a 4-bit number and generates a 3-bit binary number output that approximates the square root of the number. For example, if the square root is 3.5 or larger, give a result of 4. If the square root is < 3.5 and ≥ 2.5 , give a result of 3.

3-28. Design a 4-to-16-line decoder using two 3-to-8-line decoders and 16 2-input AND gates.

- 3-36.** Derive the truth table of a decimal-to-binary priority encoder. There are 10 inputs I_0 through I_9 and outputs A_3 through A_0 and V . Input I_9 has the highest priority.
- 3-37.** (a) Design an 8-to-1-line multiplexer using a 3-to-8-line decoder and an 8×2 AND-OR.
- 3-52.** Perform the indicated subtraction with the following unsigned binary numbers by taking the 2s complement of the subtrahend:
- (a) $11010 - 10001$
 - (b) $11110 - 1110$
 - (c) $1111110 - 1111110$
 - (d) $101001 - 101$
- 3-53.** Repeat Problem 3-52, assuming the numbers are 2s complement signed numbers. Use extension to equalize the length of the operands. Indicate whether overflow occurs during the complement operations for any of the given subtrahends. Indicate whether overflow occurs overall for any of the given subtractions. When an overflow does occur, repeat the operation with the minimum number of bits required to perform the operation without overflow.
- 3-55.** The following binary numbers have a sign in the leftmost position and, if negative, are in 2s complement form. Perform the indicated arithmetic operations and verify the answers.
- (a) $100111 + 111001$
 - (b) $001011 + 100110$
 - (c) $110001 - 010010$
- 3-57.** Use contraction beginning with a 4-bit adder with carry out to design a 4-bit increment-by-2 circuit with carry out that adds the binary value 0010 to its 4-bit input. The function to be implemented is $S = A + 0010$.