



Performance analysis with BSC-Tools

Jesus Labarta, Judit Gimenez
BSC



Fly with instruments

Who can I blame?

Understand our systems

Insight
Identify most productive efforts

Help generate hypotheses

Help validate hypotheses

Qualitatively

Quantitatively

Our Tools

Since 1991
Based on traces
Open Source

<http://www.bsc.es/paraver>

Core tools:

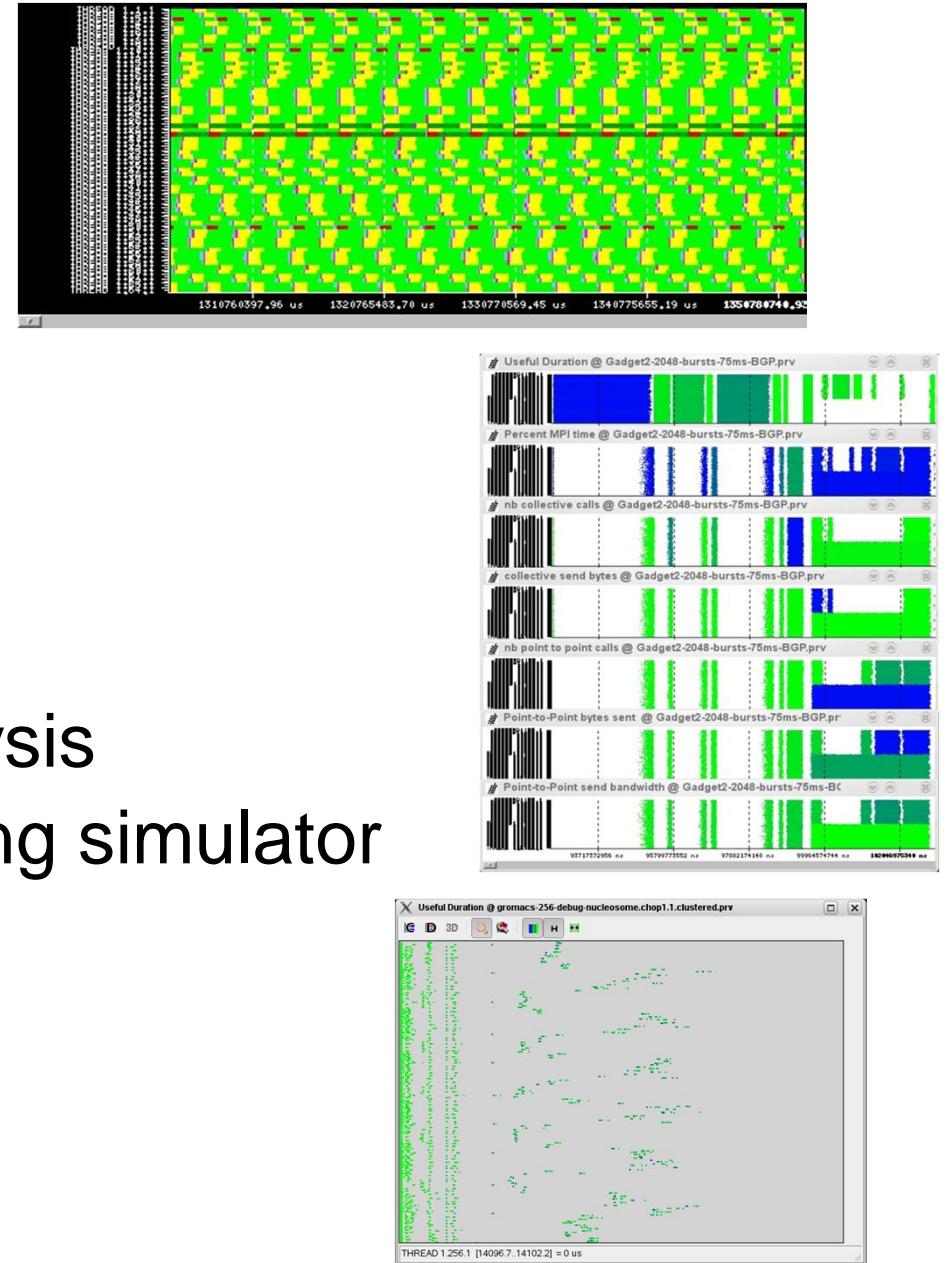
Paraver – offline trace analysis

Dimemas – message passing simulator

Exrae – instrumentation

Focus

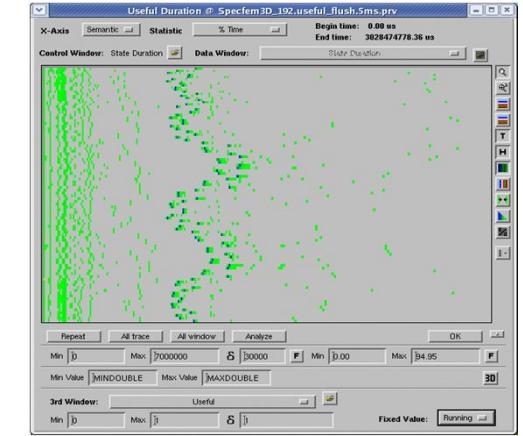
Detail, flexibility, intelligence



Why traces?

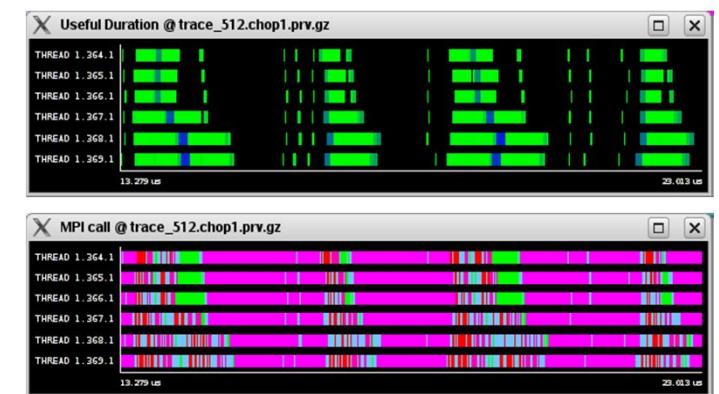
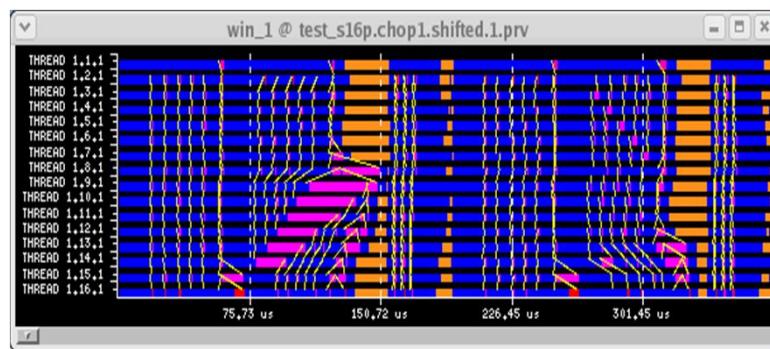


Detail and variability are important
along time, across processors



Highly non-linear systems
microscopic effects may have macroscopic impact

Extremely useful to develop/test analysis
techniques



Overview of capabilities of the environment



Analysis

Trace analysis

Scaling model

What if

Simple abstract model

Structure detection

HWC projection and CPI stack model

Extreme detail at low overhead

Scalability

Data selection and reduction

Online

Outline



Extrace

Paraver

Dimemas

Scaling model

Structure detection

HWC analyses

Projection and CPI Stack models

Folding: Instrumentation + sampling

Scalability

Outline



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Parallel programming model runtime

MPI, OpenMP, Pthreads, StarSs, ...

Counters

CPU counters – PAPI (standard + native)

Network counters (GM, MX) - at flushes

OS counters

Links to source

Callstack at MPI calls

User functions selected – default none

Periodic samples

PAPI counters + callstack

User events

Outline



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Structure detection

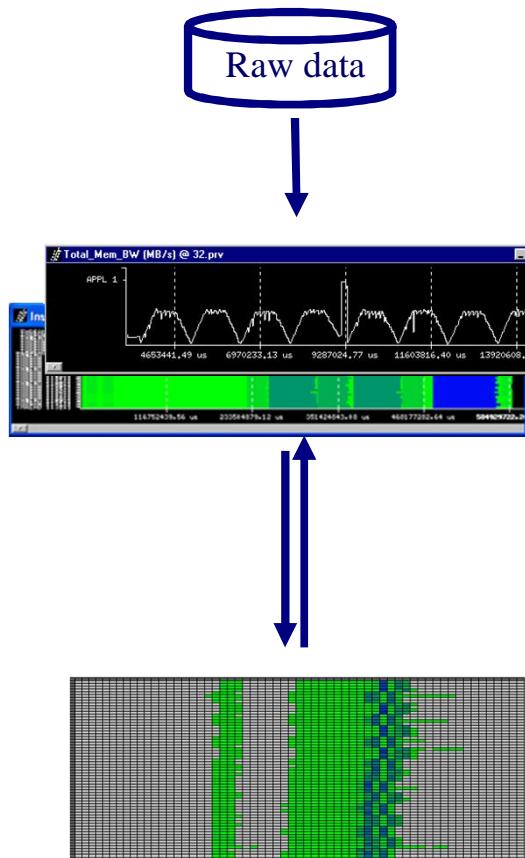
HWC analyses

Projection and CPI Stack models

Folding

Scalability

Paraver – Performance data browser



Timelines

2/3D tables
(Statistics)

Goal = Flexibility
No semantics
Programmable

Configuration files
Distribution
Your own

Comparative analyses
Multiple traces
Synchronize scales

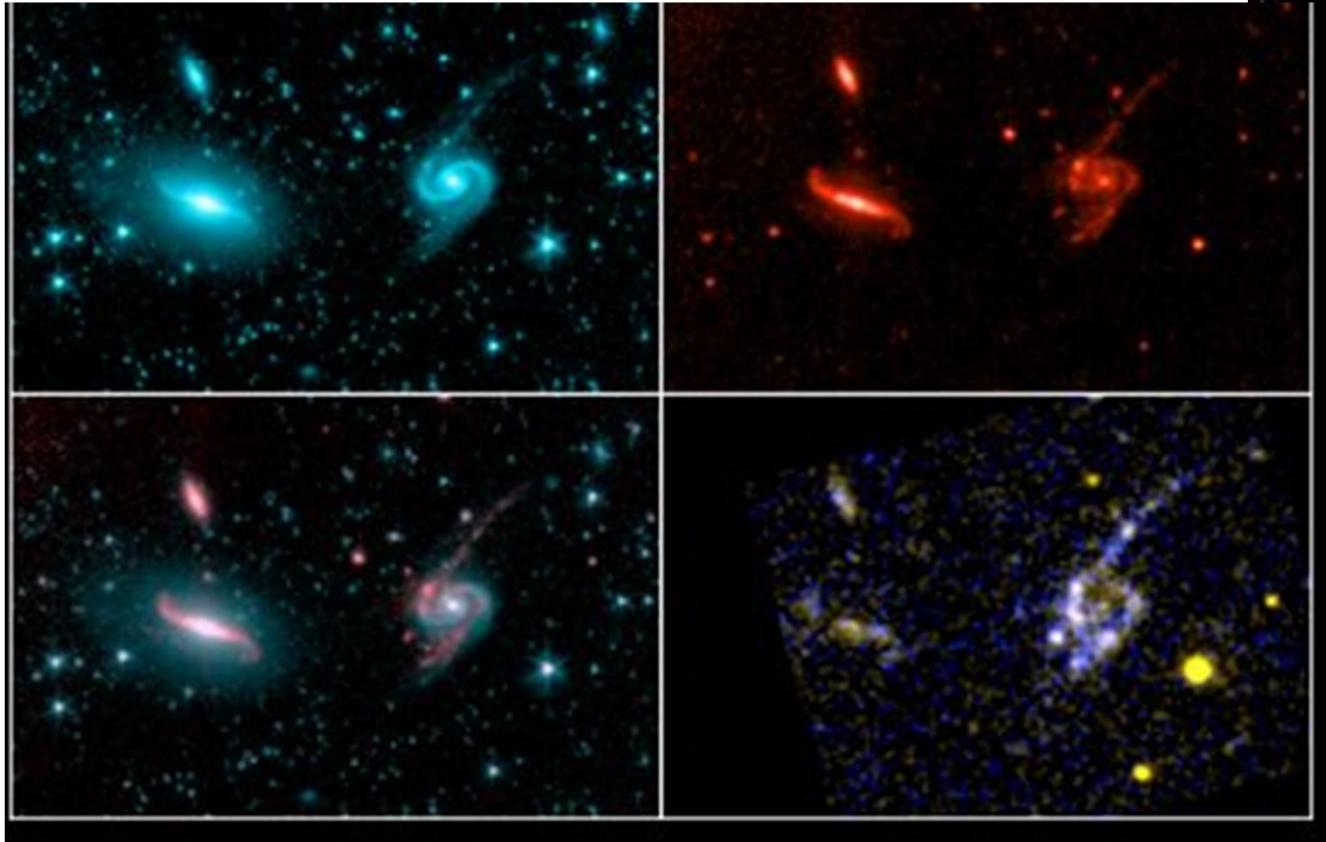
Multispectral imaging



Different looks at one reality

Different spectral bands (light sources and filters)

Highlight different aspects

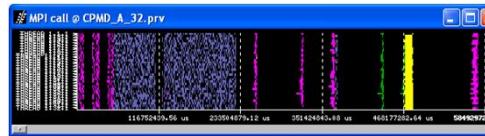


Views: Timelines

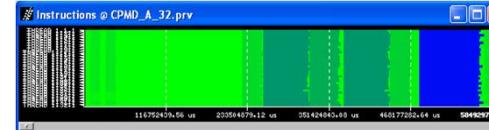


Raw events → Piece-wise constant functions of time → plots / colors

- Basic metrics

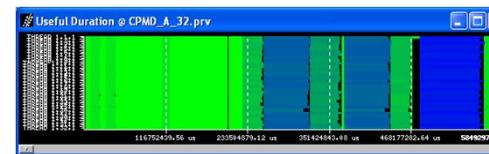
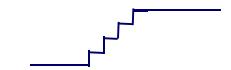


MPI calls



Instructions

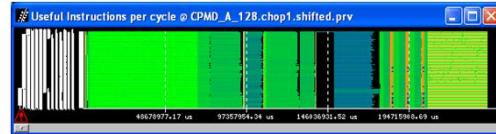
Color encoding



Useful duration

- Derived metrics

$$useful_IPC = \frac{\#instr}{\#cycles} * useful$$



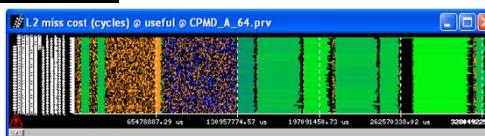
$$MPI_call_Cost = \frac{MPI_call_duration}{\#bytes}$$



- Models

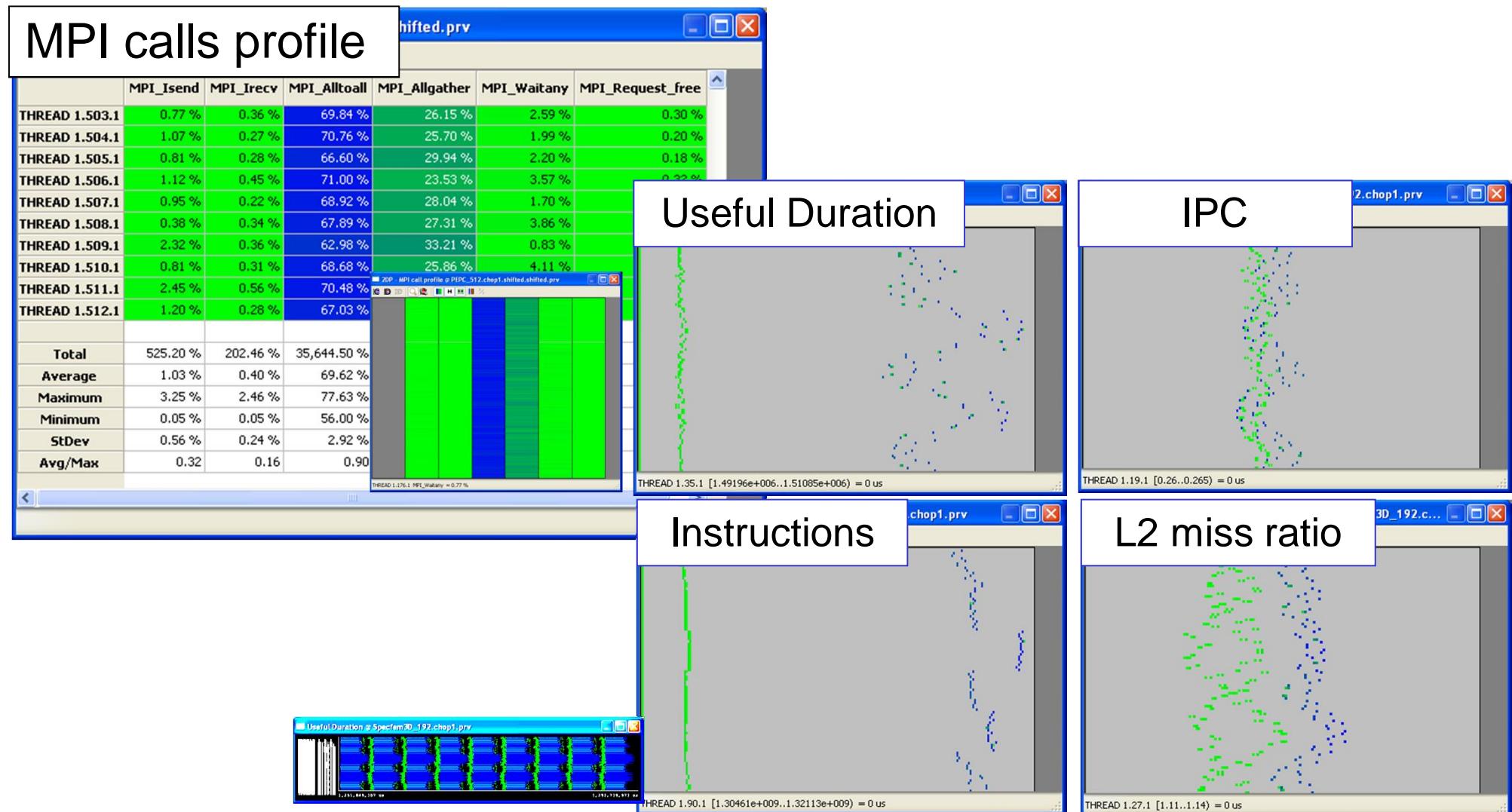
$$L2_{miss_{latency}} = \frac{cycles - instr/idealIPC}{L2misses}$$

$$preempted_{time} = elapsed - \frac{cycles}{clock_{freq}}$$



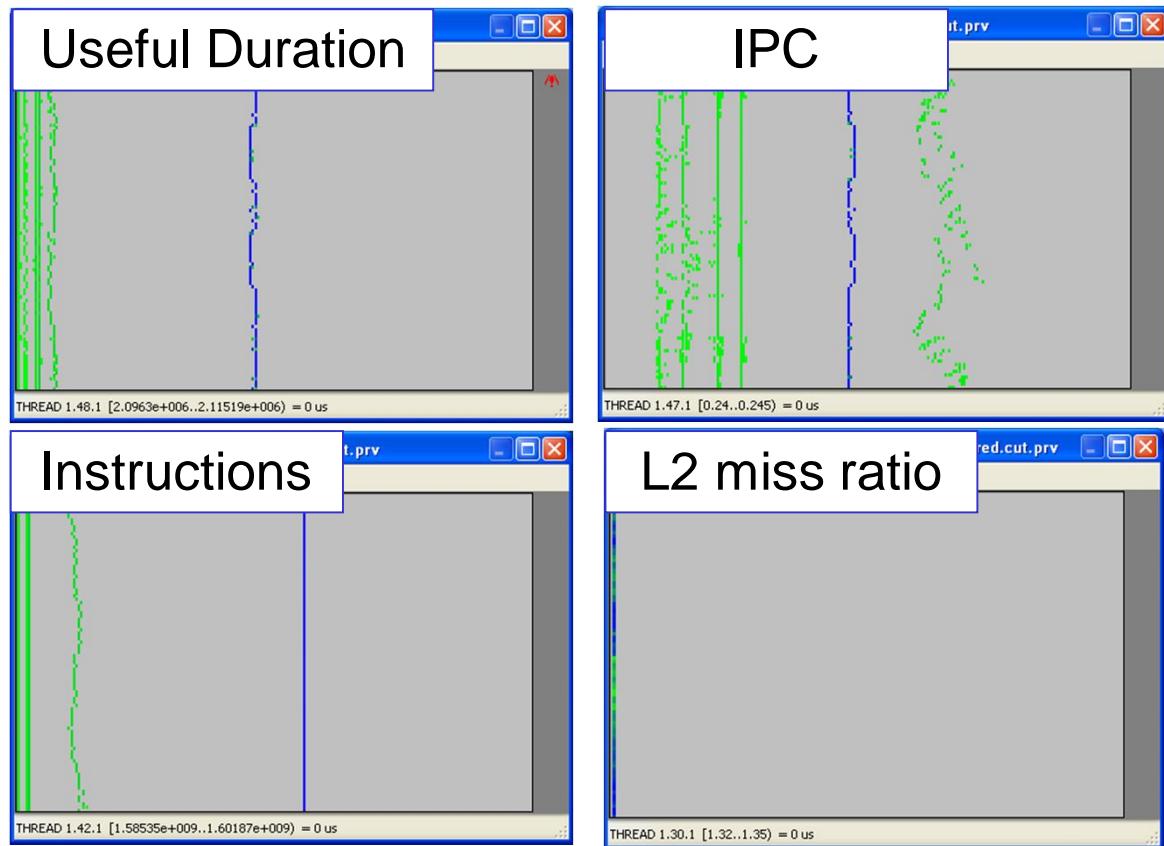
Tables: Profiles, histograms, correlations

Huge number of statistics computed from timelines



Tables: Profiles, histograms, correlations

By the way: six months later



Outline



Extrace
Paraver
Dimemas

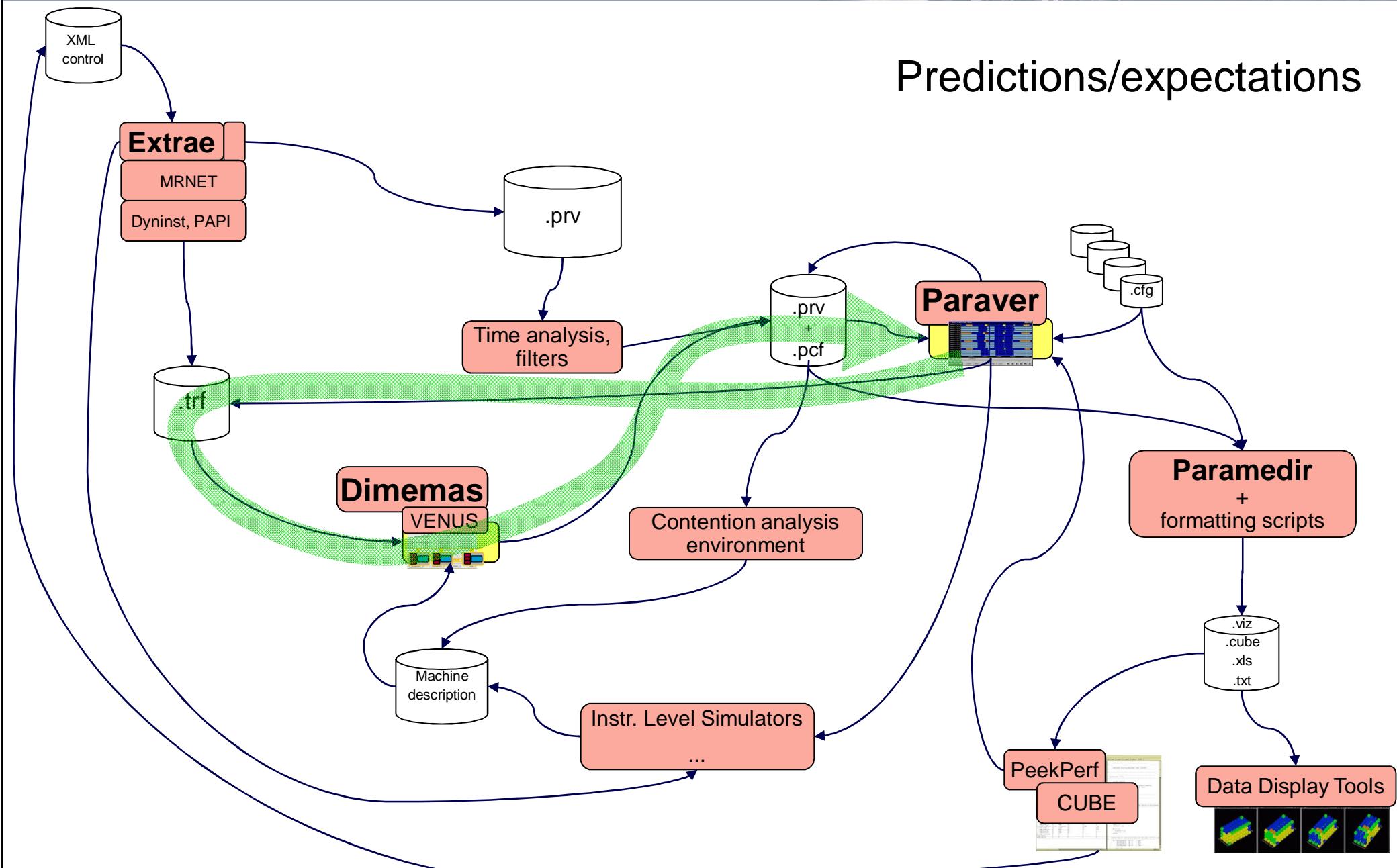
Scaling model
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Scalability

BSC-Tools Environment



Predictions/expectations



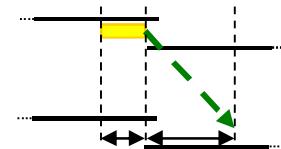
Dimemas: Coarse grain, Trace driven simulation

Simulation: Highly non linear model

Linear components

Point to point communication

$$T = \frac{\text{MessageSize}}{\text{BW}} + L$$



Sequential processor performance

Global CPU speed

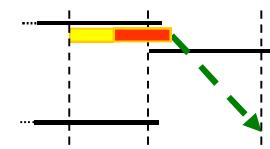
Per block/subroutine

Non linear components

Synchronization semantics

Blocking receives

Rendezvous

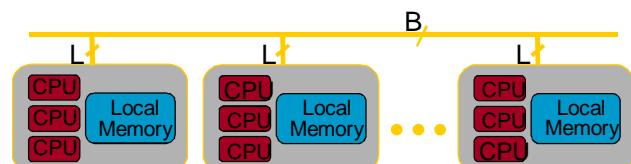


Resource contention

CPU

Communication subsystem

links (half/full duplex), busses



Collective communication model

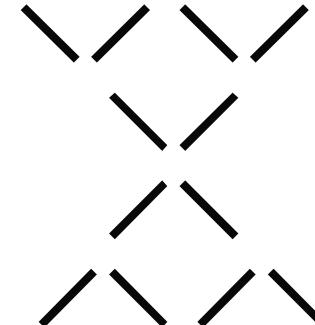


Generic model

Barrier / Fan-in / Fan-out

Cost of communication phase

Generic



Per call

Model factor

Lin / log / const

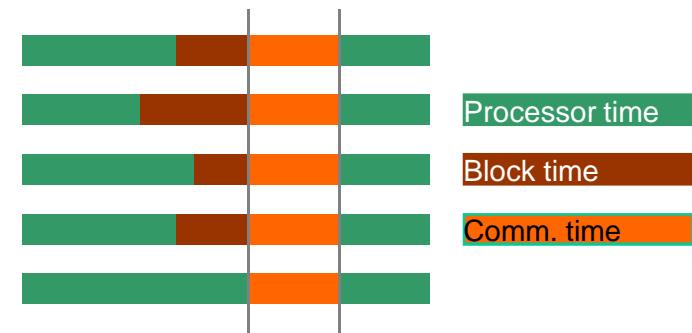
Size of message

Min over all processes

Avg over all processes

Max over all processes

Collective



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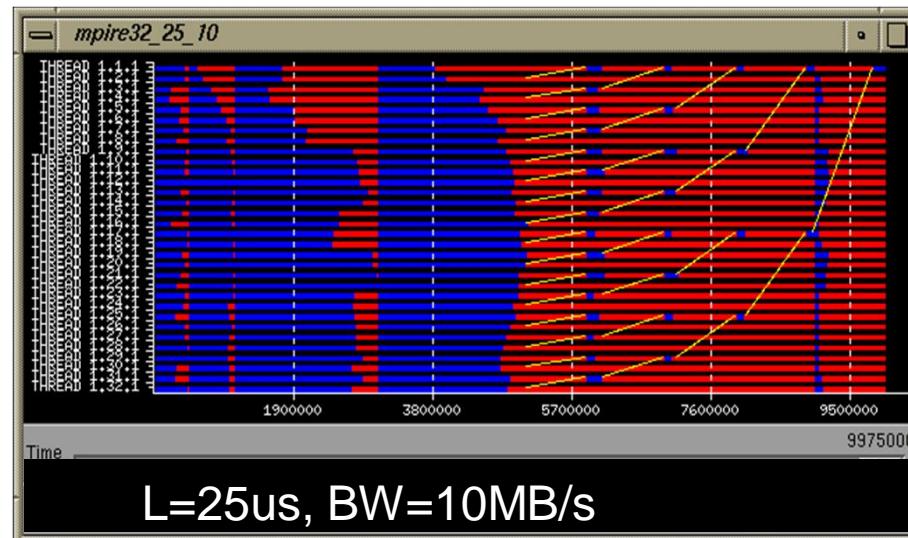
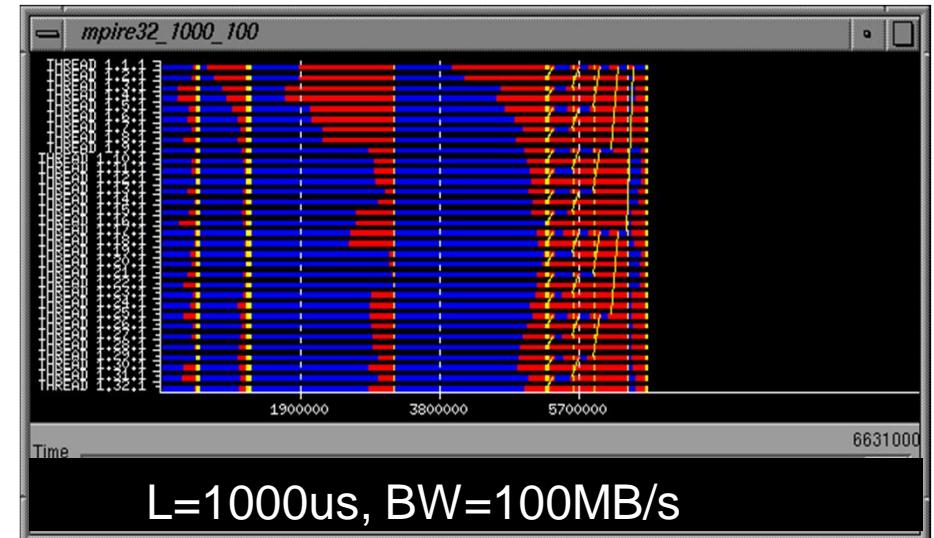
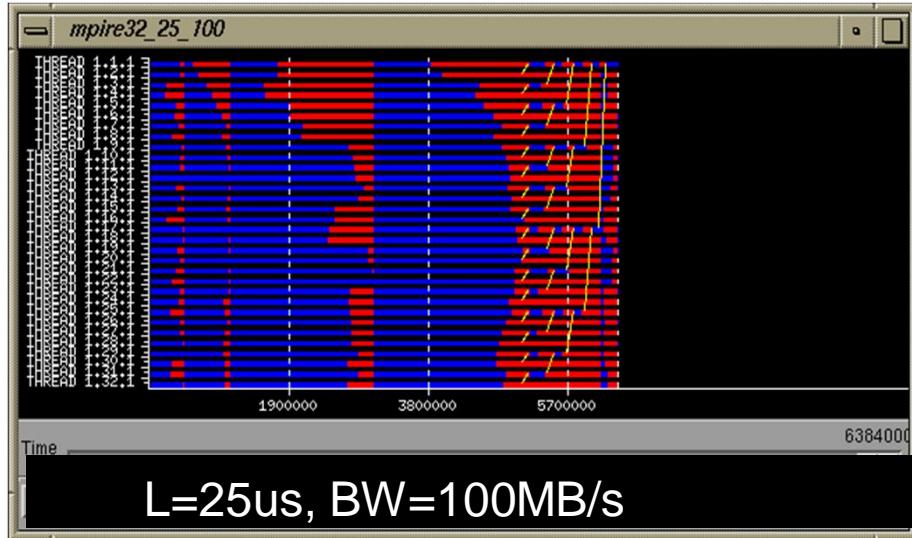
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Understanding applications



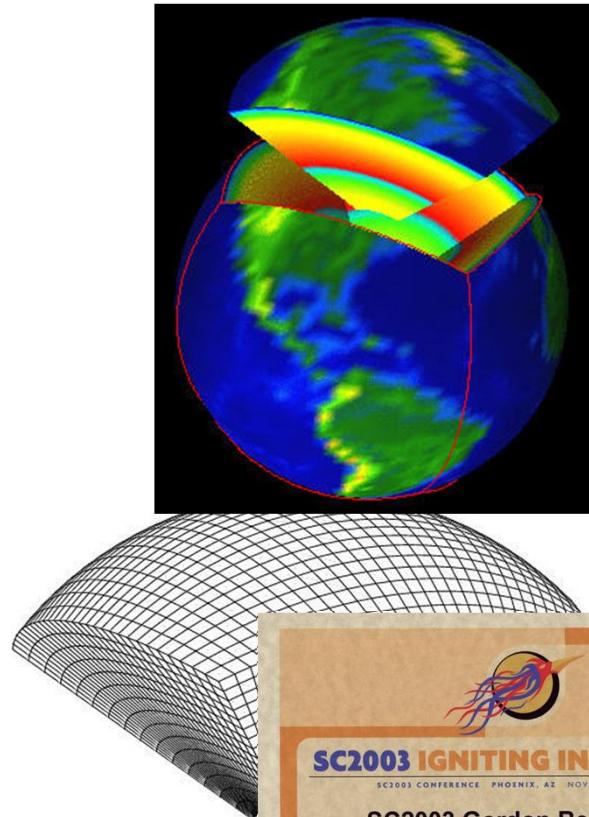
MPIRE 32 tasks, no network contention



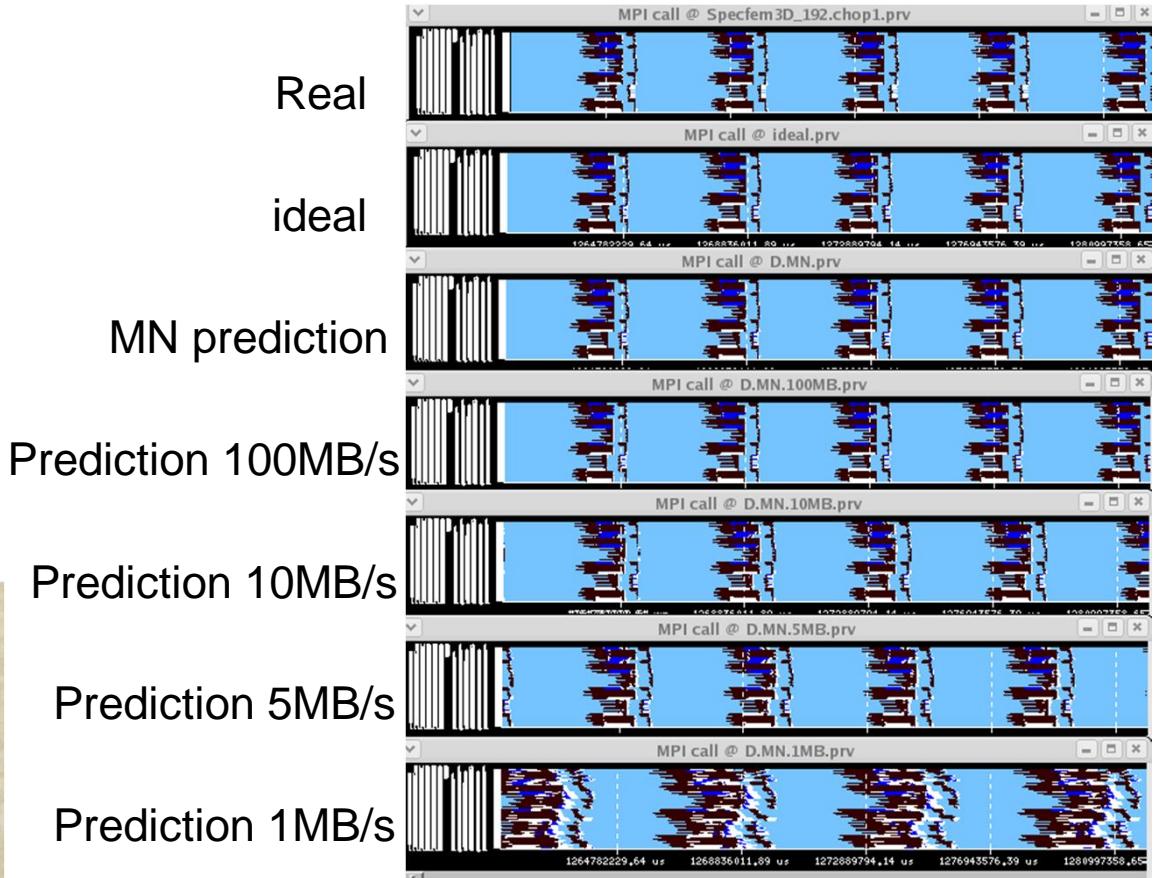
All windows same scale

Insight → advice appropriate direction

SPECFEM3D – asynchronous communication?



Courtesy Dimitri Komatitsch

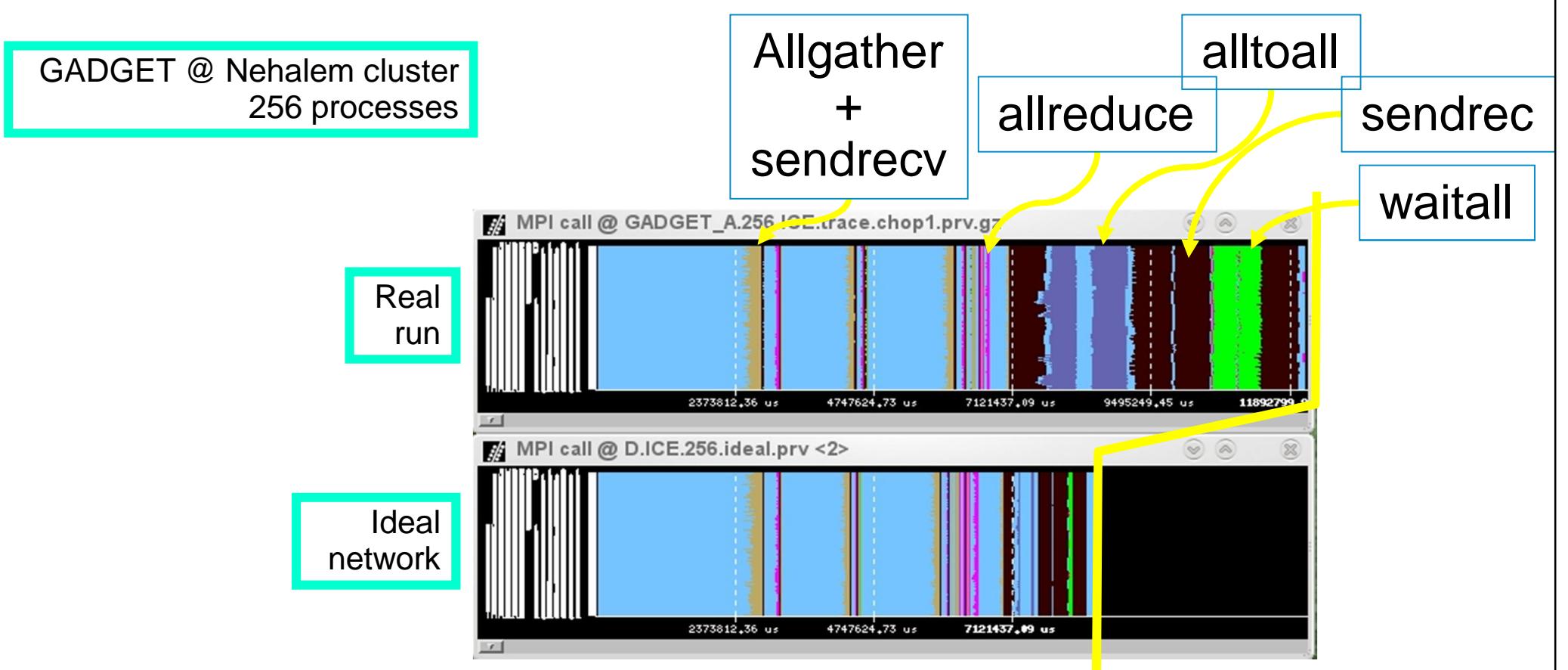


Intrinsic application behavior



Load balanced and dependence problems?

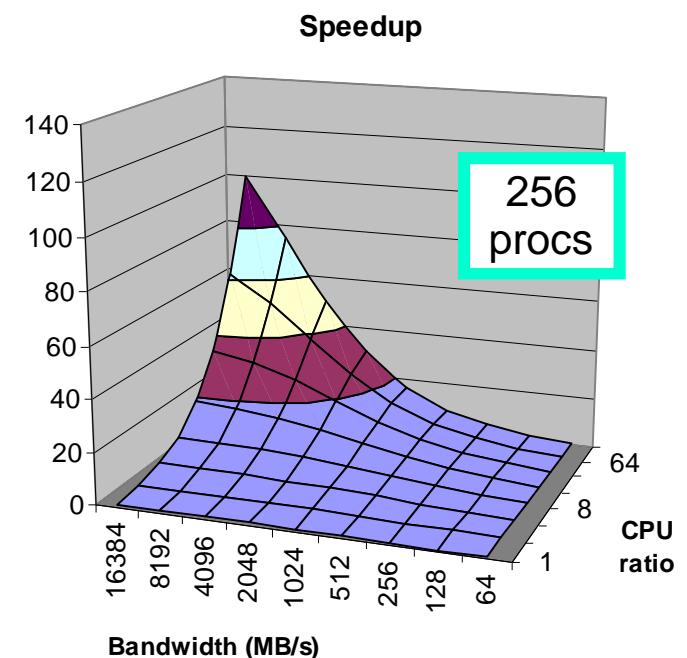
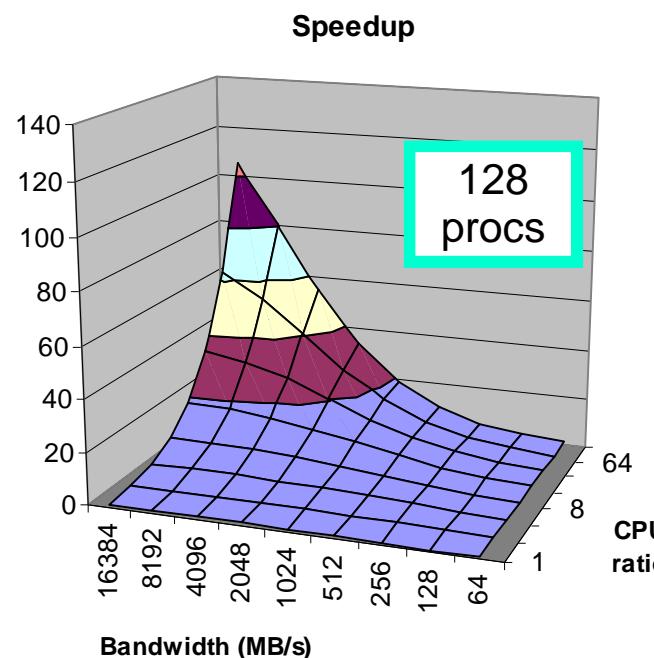
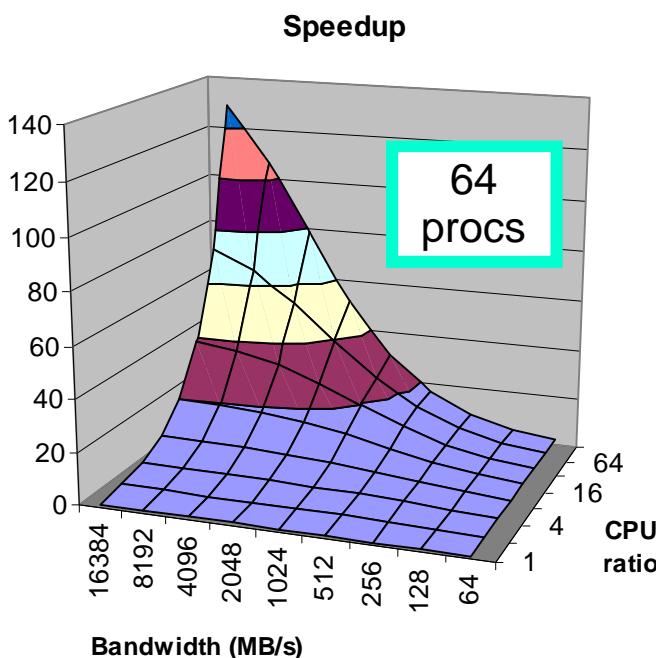
$$BW = \infty, \quad L = 0$$



Impact of architectural parameters

Ideal speeding up ALL the computation bursts by the CPUratio factor

The more processes the less speedup (higher impact of bandwidth limitations) !!!!!



GADGET

What if: Potential of hybrid parallelization

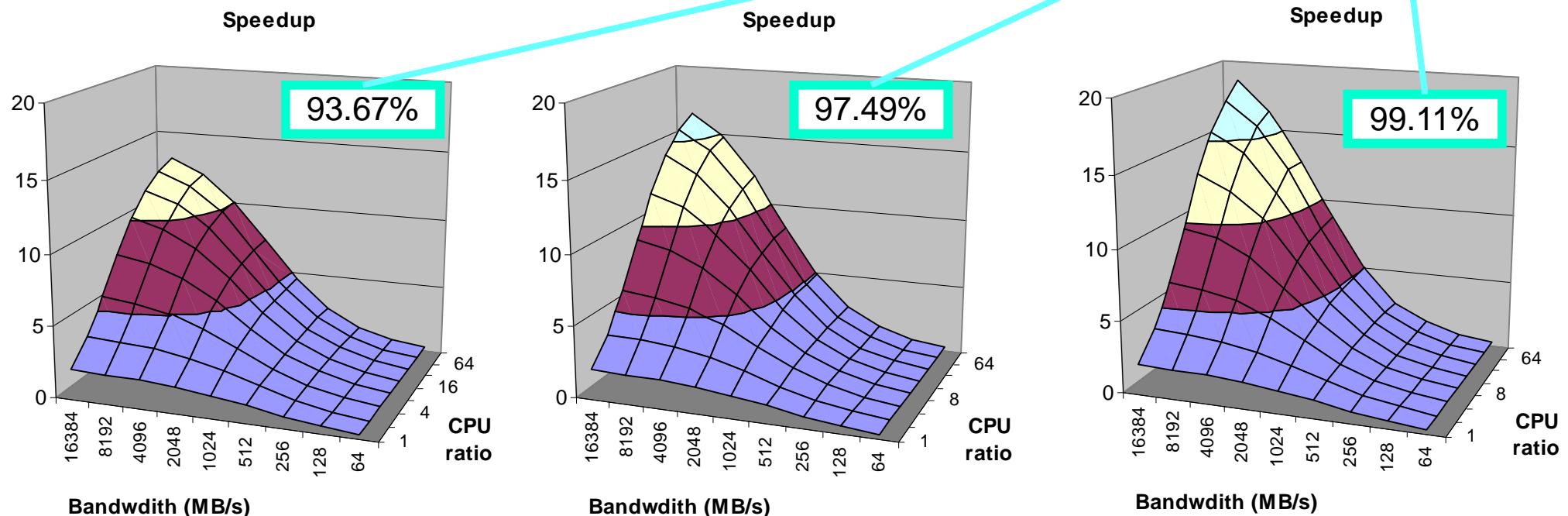
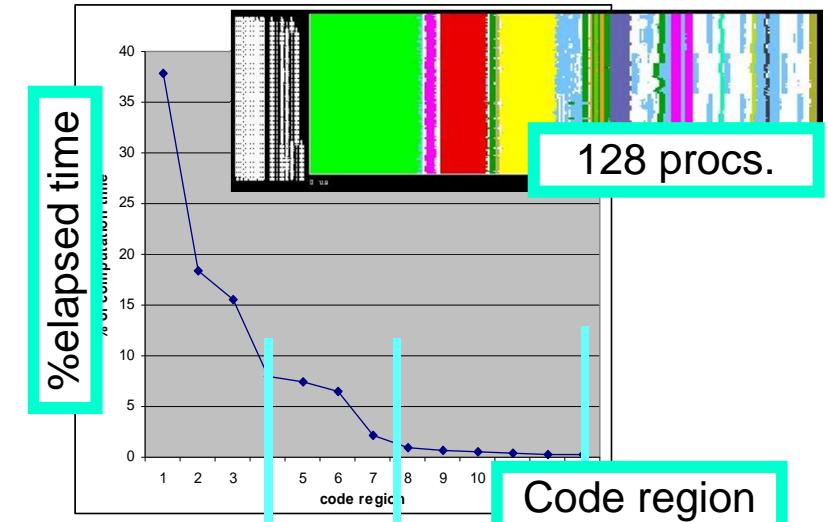


Hybrid/accelerator parallelization

Speedup SELECTED regions by the CPUratio factor

We do need to overcome the **hybrid Amdahl's law**

→ **asynchrony + Load balancing mechanisms !!!**



Presenting application performance

Factors modeling parallel efficiency

Load balance (LB)

Micro load balance (μ LB) or serialization

Transfer

$$\eta = LB * \mu LB * Transfer$$

Factors describing serial behavior

Performance: **IPC**

Scaling model

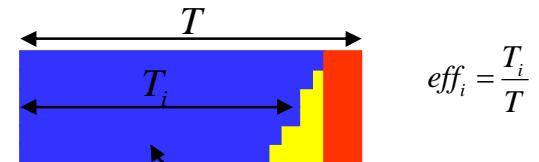
$$Sup = \frac{P}{P_0} * \frac{\eta}{\eta_0} * \frac{IPC}{IPC_0} * \frac{\#instr_0}{\#instr}$$

Scaling model



$$Sup = \frac{P}{P_0} * \frac{LB}{LB_0} * \frac{\text{CommEff}}{\text{CommEff}_0} * \frac{IPC}{IPC_0} * \frac{\#instr_0}{\#instr}$$

Directly from real execution metrics

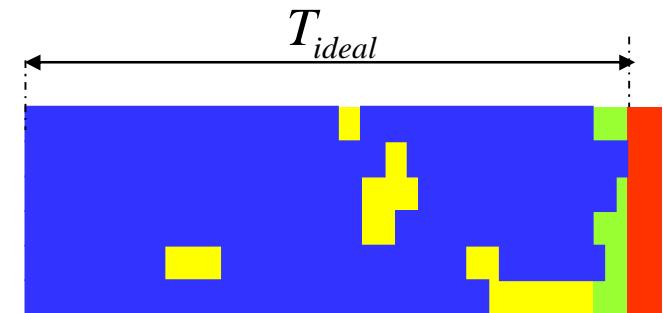


$$\text{CommEff} = \max(\text{eff}_i)$$

$$LB = \frac{\sum_{i=1}^P \text{eff}_i}{P * \max(\text{eff}_i)}$$



$$\text{microLB} = \frac{\max(T_i)}{T_{ideal}} \quad Transfer = \frac{T_{ideal}}{T}$$



Migrating/local load imbalance
Serialization

$$Sup = \frac{P}{P_0} * \frac{\text{macroLB}}{\text{macroLB}_0} * \frac{\text{microLB}}{\text{microLB}_0} * \frac{\text{Transfer}}{\text{Transfer}_0} * \frac{IPC}{IPC_0} * \frac{\#instr_0}{\#instr}$$

Requires Dimemas simulation

Modeling Parallel performance



Almost
Acceptable
scalability

GADGET @ PRACE data set 1

Platform	Processors	Input	Iteration time (s)	Speedup	Relative efficiency
MN	64	A	78,71	1,00	1,00
MN	128	A	44,22	1,78	0,89
MN	256	A	22,78	3,46	0,86
BGP	64	A	250,41	1,00	1,00
BGP	128	A	131,78	1,90	0,95
BGP	256	A	79,10	3,17	0,79
ICE	64	A	40,41	1,00	1,00
ICE	128	A	21,65	1,87	0,93
ICE	256	A	12,15	3,32	0,83

Modeling Parallel performance

Platform	Processors	Input	Iteration time (s)	Speedup	Relative efficiency	Parallel Efficiency	LB	microLB	Transfer
MN	64	A	78,71	1,00	1,00	0,66	0,90	0,94	0,78
MN	128	A	44,22	1,78	0,89	0,58	0,97	0,92	0,65
MN	256	A	22,78	3,46	0,86	0,56	0,95	0,77	0,76
BGP	64	A	250,41	1,00	1,00	0,87	0,90	0,99	0,97
BGP	128	A	131,78	1,90	0,95	0,86	0,96	0,98	0,91
BGP	256	A	79,10	3,17	0,79	0,75	0,95	0,89	0,90
ICE	64	A	40,41	1,00	1,00	0,88	0,91	0,97	0,83
ICE	128	A	21,65	1,87	0,93	0,68	0,98	0,95	0,73
ICE	256	A	12,15	3,32	0,83	0,61	0,94	0,95	0,68

Almost Acceptable scalability

Performs not so well

Fair load balance

Good in BGP

Transfer Issue

GADGET @ PRACE data set 1

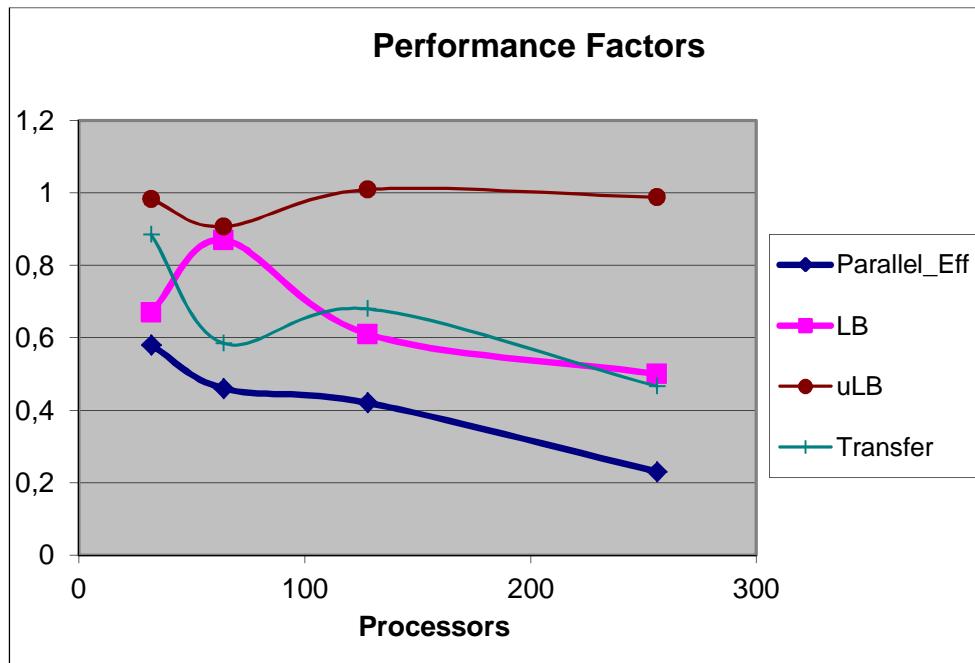
Although we have observed production runs at BSC with horrible load balance

Parallel Performance Models

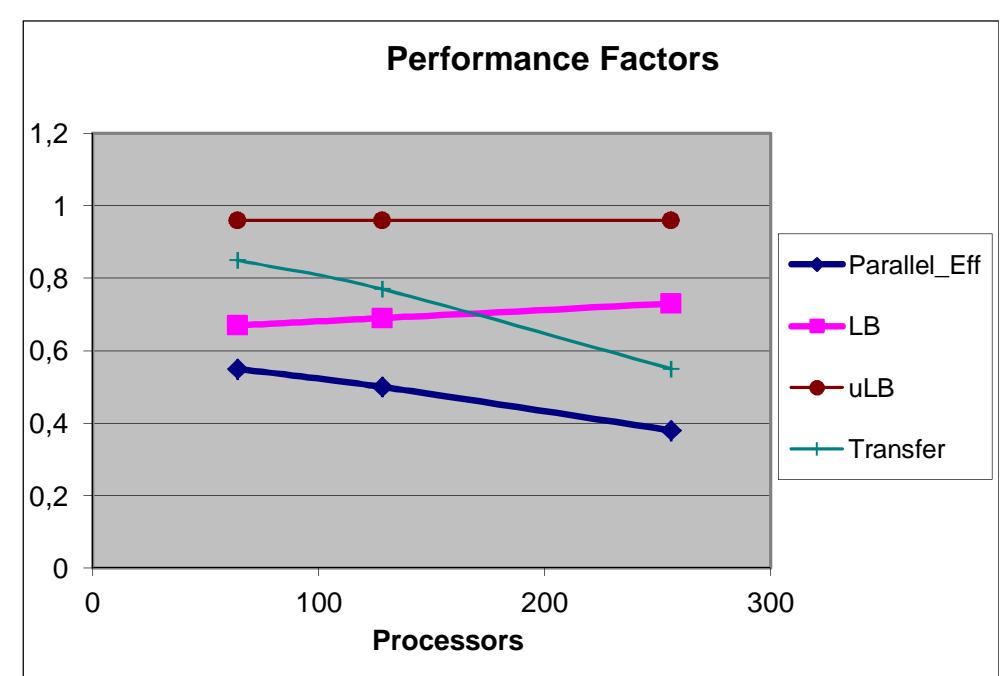


GROMACS

Original Version



v4.5



Old → new version: -43% instr, -52% time

Outline



Extrace

Paraver

Dimemas

Scaling model

Structure detection

HWC analyses

Projection and CPI Stack models

Folding

Scalability

Performance @ serial computation bursts

Burst = continuous computation region
between exit of an MPI call and entry to the next

Scatter plot representation of bursts

Collapse time dimension

N dimensional space of HWC derived relevant metrics

- Instructions: idea of computational complexity, computational load imbalance,...
- IPC: Idea of absolute performance and performance imbalance

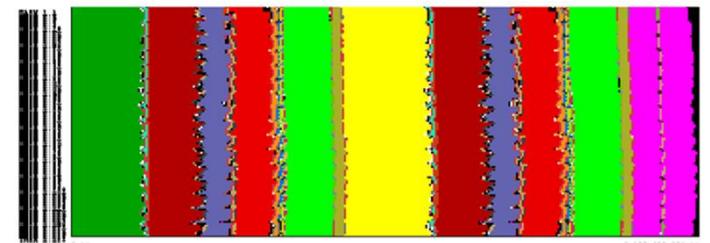
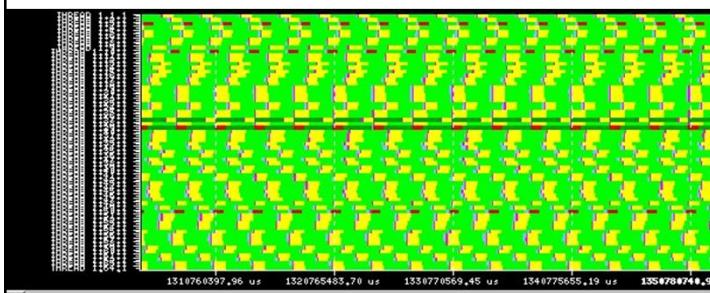
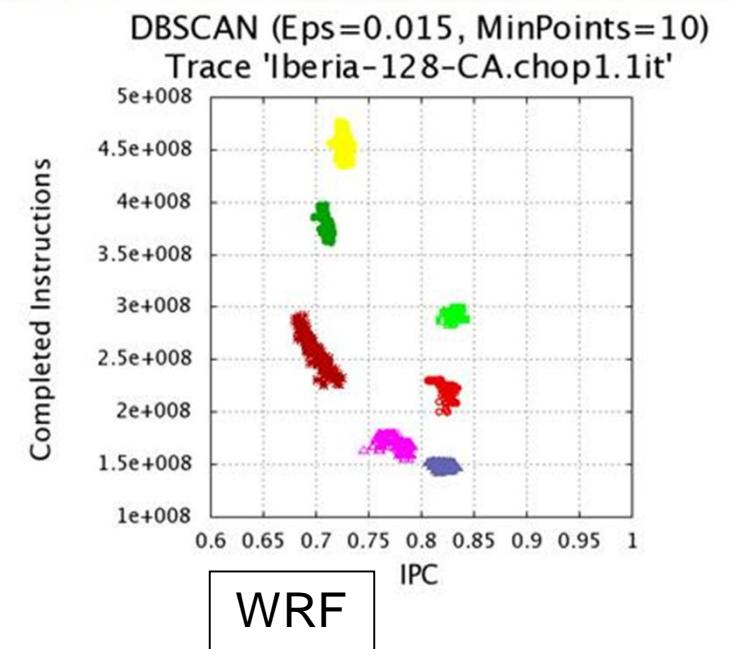
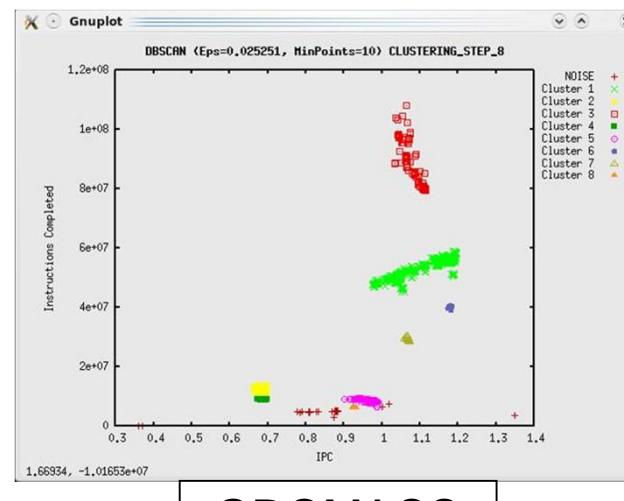
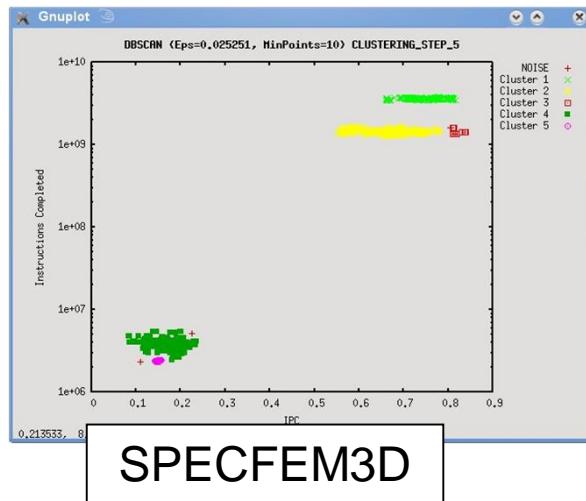
Structure

Clouds, clusters: Burst of “similar” characteristics

How those similarities spread in the timeline?

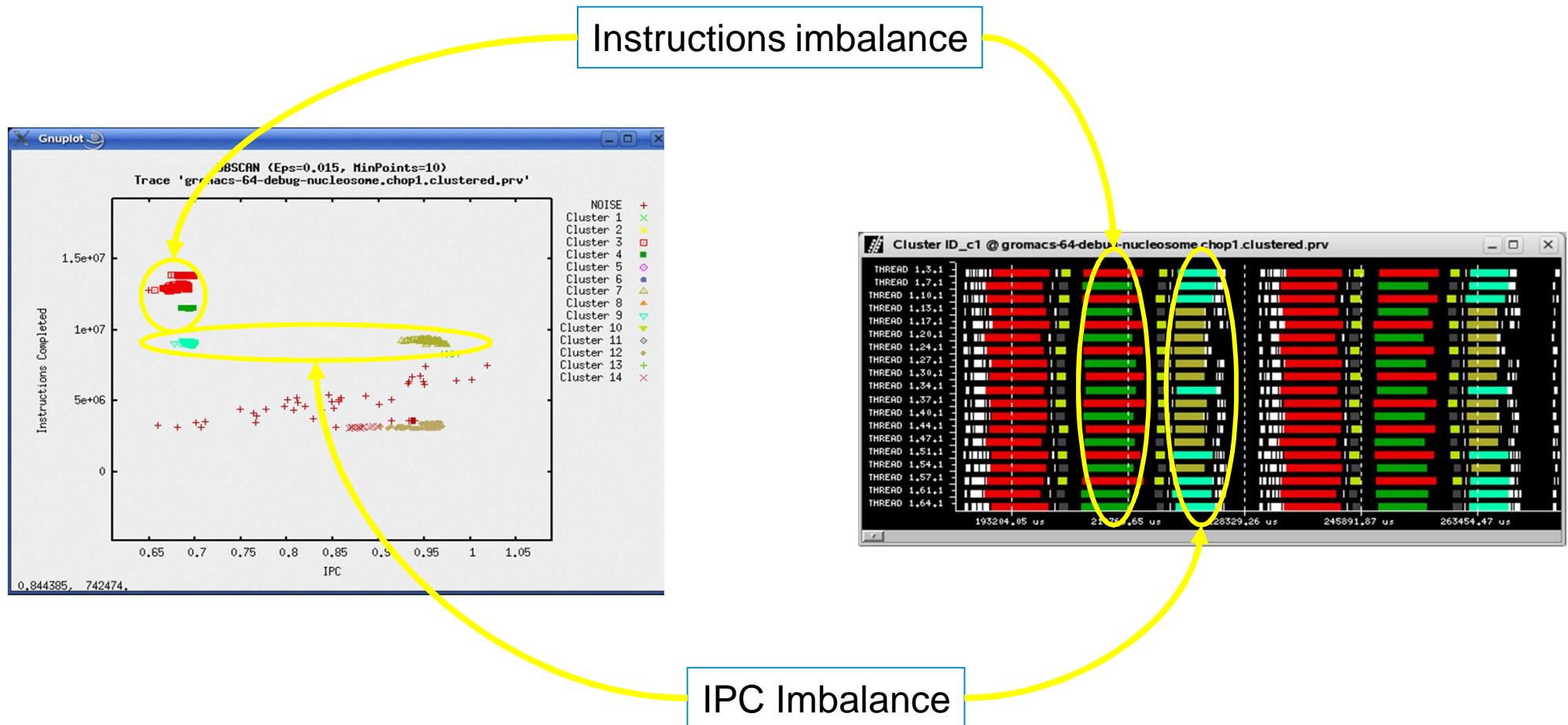
How does it relate to source code?

Performance @ serial computation bursts



The importance of joint timeline+scatterplots

GROMACS FFTs balance



Automatic clustering quality assessment

Leverage Multiple Sequence alignment tools from Life Sciences

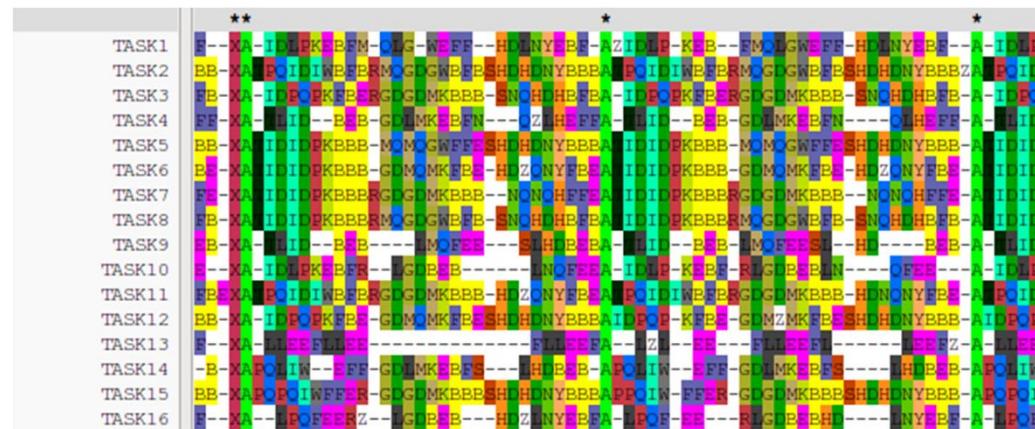
Process == Sequence of clusters \leftrightarrow sequence of amino acids == DNA

CLUSTAL W, T-Coffee, Kalign2

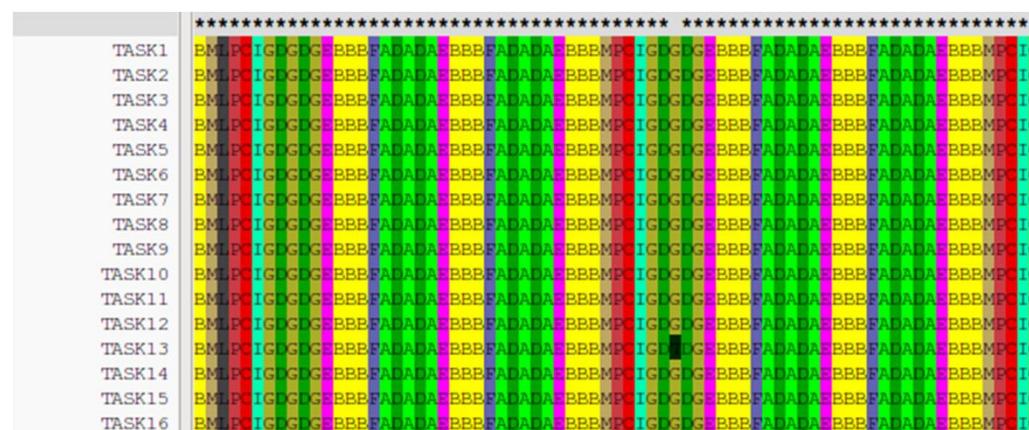
Cluster Sequence Score (0..1)

Per cluster / Global

Weighted average



BT.A
0022



BT.A
0043

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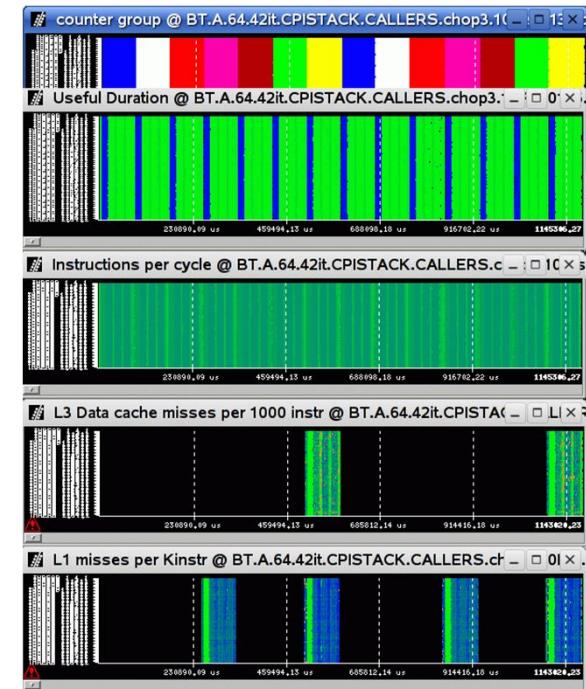
HWC projection



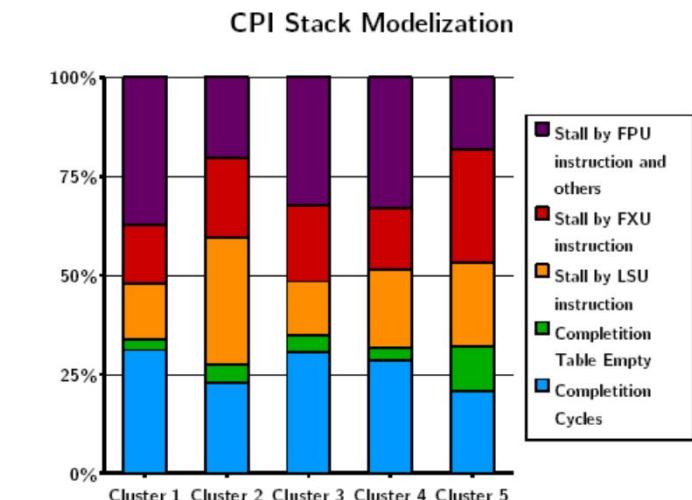
Full characterization

All HWCs and ratios between them
CPI stack model

From a single run



CLUSTER	1	2	3	4	5
%TIME	54.88	17.96	16.90	6.44	1.42
AVG. BURST DUR. (ms)	1.02	0.78	13.14	2.50	1.11
IPC	1.02	0.65	0.89	0.91	0.53
MIPS	2231.8	1423.3	1966.5	2001.8	1163.0
MFLOPS	339.2	46.3	191.6	269.2	23.6
L1M/KINSTR	0.92	1.53	1.19	1.17	2.88
L2M/KINSTR	0.06	1.26	0.06	0.35	0.21
MEM.BW (MB/s)	16.79	218.47	13.87	85.77	29.76



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Folding: Instrumentation + sampling

Extremely detailed time evolution of hardware counts, rates and callstack

Minimal overhead

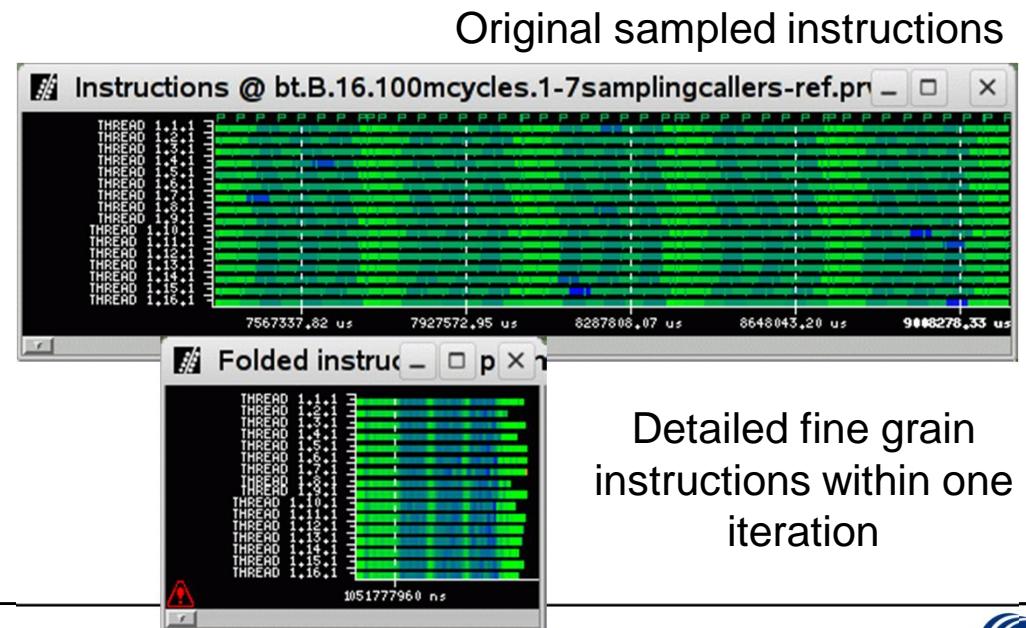
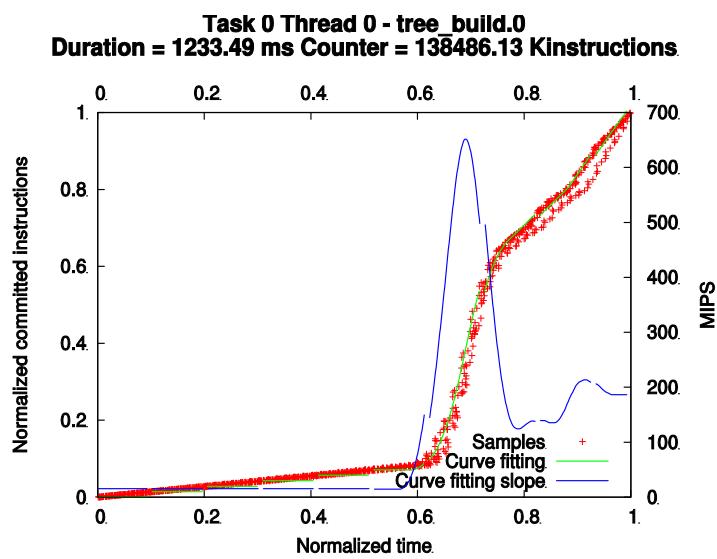
Based on

trace: instrumentation events (iteration, MPI, ...) and periodic samples.

Application structure: manual iteration instrumentation, routines, clusters

Folding

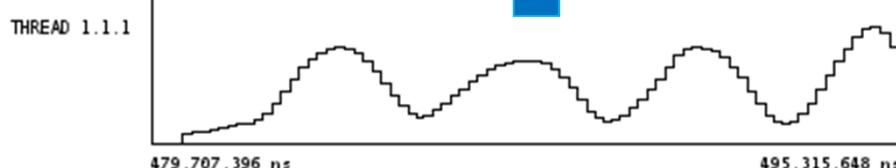
Postprocessing to project all samples into one instance



Folding → profiles of rates



Folded source code line



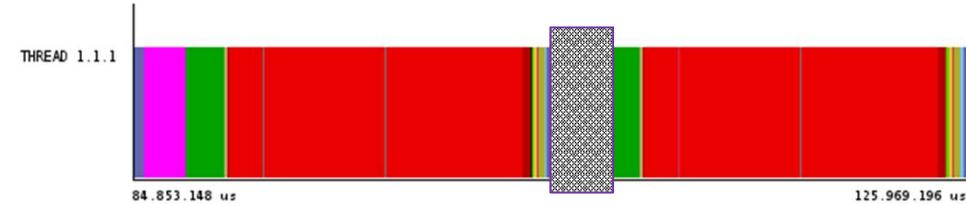
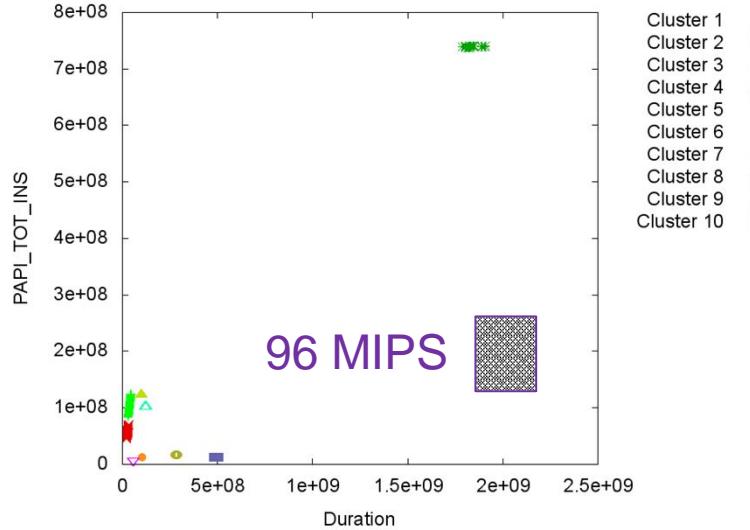
Folded instructions

```
Fitxer Edita Eines Sintaxi Buffers Finestra Ajuda  
18 c-----  
19 c----- loop over all cells owned by this node  
20 c-----  
21 do c = 1, ncells  
22  
23 c-----  
24 c----- compute the reciprocal of density, and the kinetic energy,  
25 c----- and the speed of sound.  
26 c-----  
27 do k = -1, cell_size(3,c)  
28 do j = -1, cell_size(2,c)  
29 do i = -1, cell_size(1,c)  
30 rho_inv = 1.0d0/u(i,j,k,c)  
31 rho_i(i,j,k,c) = rho_inv  
32 us(i,j,k,c) = u(2,i,j,k,c) * rho_inv  
33 vs(i,j,k,c) = u(3,i,j,k,c) * rho_inv  
34 ws(i,j,k,c) = u(4,i,j,k,c) * rho_inv  
35 square(i,j,k,c) = 0.5d0* (  
36 > u(2,i,j,k,c)*u(2,i,j,k,c) +  
37 > u(3,i,j,k,c)*u(3,i,j,k,c) +  
38 > u(4,i,j,k,c)*u(4,i,j,k,c) ) * rho_inv  
39 qs(i,j,k,c) = square(i,j,k,c) * rho_inv  
40 enddo  
41 enddo  
42
```

42,1 4%



DBSCAN (Eps=0.005, MinPoints=10)
Trace 'pepc.sorted.chop1.clustered.prv'

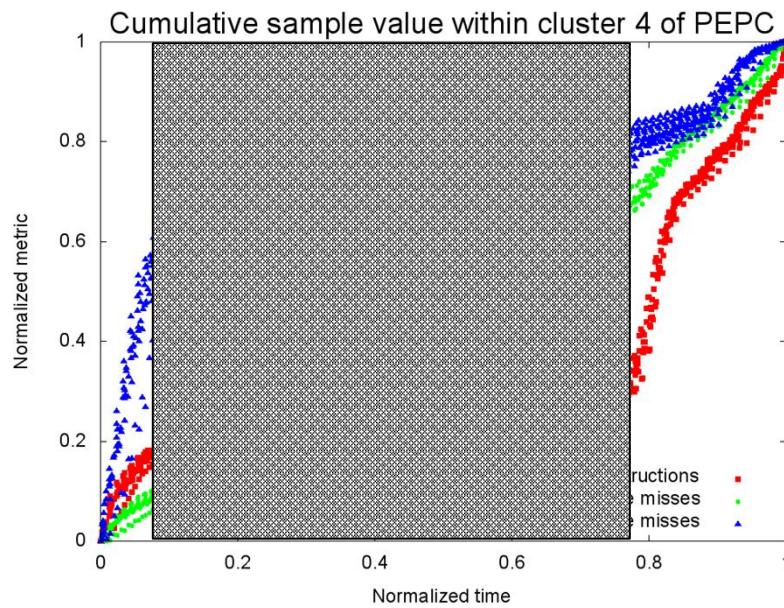


Performance metrics

16 MIPS

2.3 M L2 misses/s

0.1 M TLB misses/s



```
htable%node = 0
htable%key = 0
htable%link = -1
htable%leaves = 0
htable%childcode = 0
```



```
do i = 1, n
  htable(i)%node = 0
  htable(i)%key = 0
  htable(i)%link = -1
  htable(i)%leaves = 0
  htable(i)%childcode = 0
End do
```

Changes

-70% time

-18% instructions

-63% L2 misses

-78% TLB misses

253 MIPS (+163%)

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Scalability

Data reduction



Data handling/summarization capability

Software counters, filtering and cutting

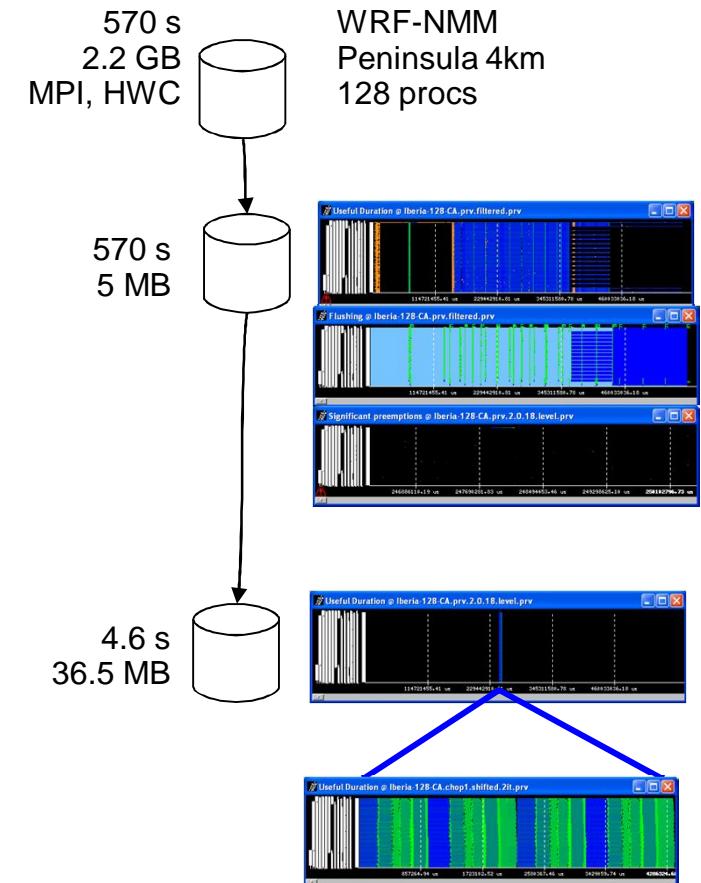
Automatizable through signal processing techniques:

Mathematical morphology to clean up perturbed regions

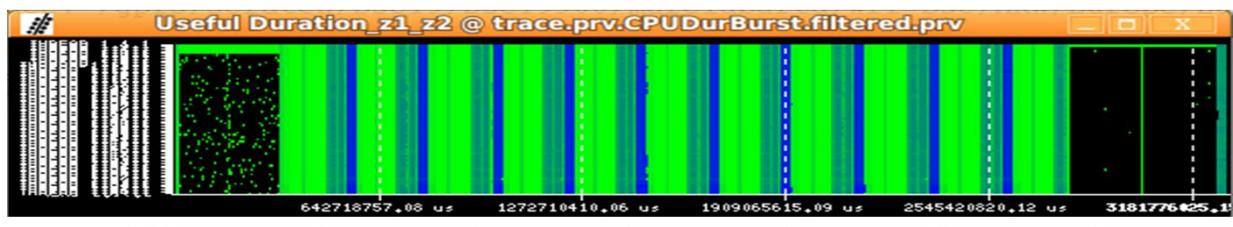
Wavelet transform to identify coarse regions

Spectral analysis for detailed periodic pattern

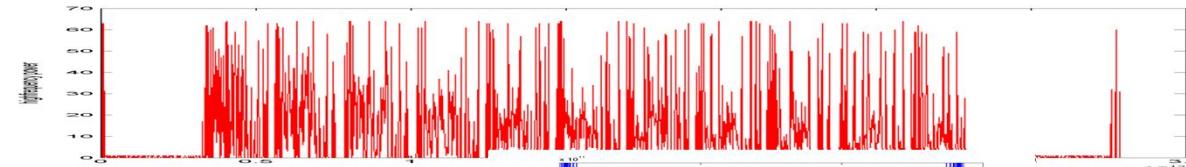
Algorithms applied to traces and online
Extrace (Stand alone or using on MRNET)
Paraver



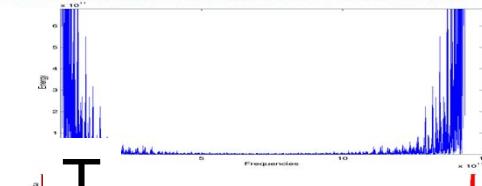
Spectral analysis



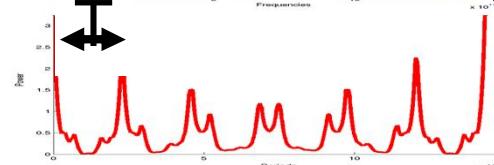
Wavelet
High
frequency



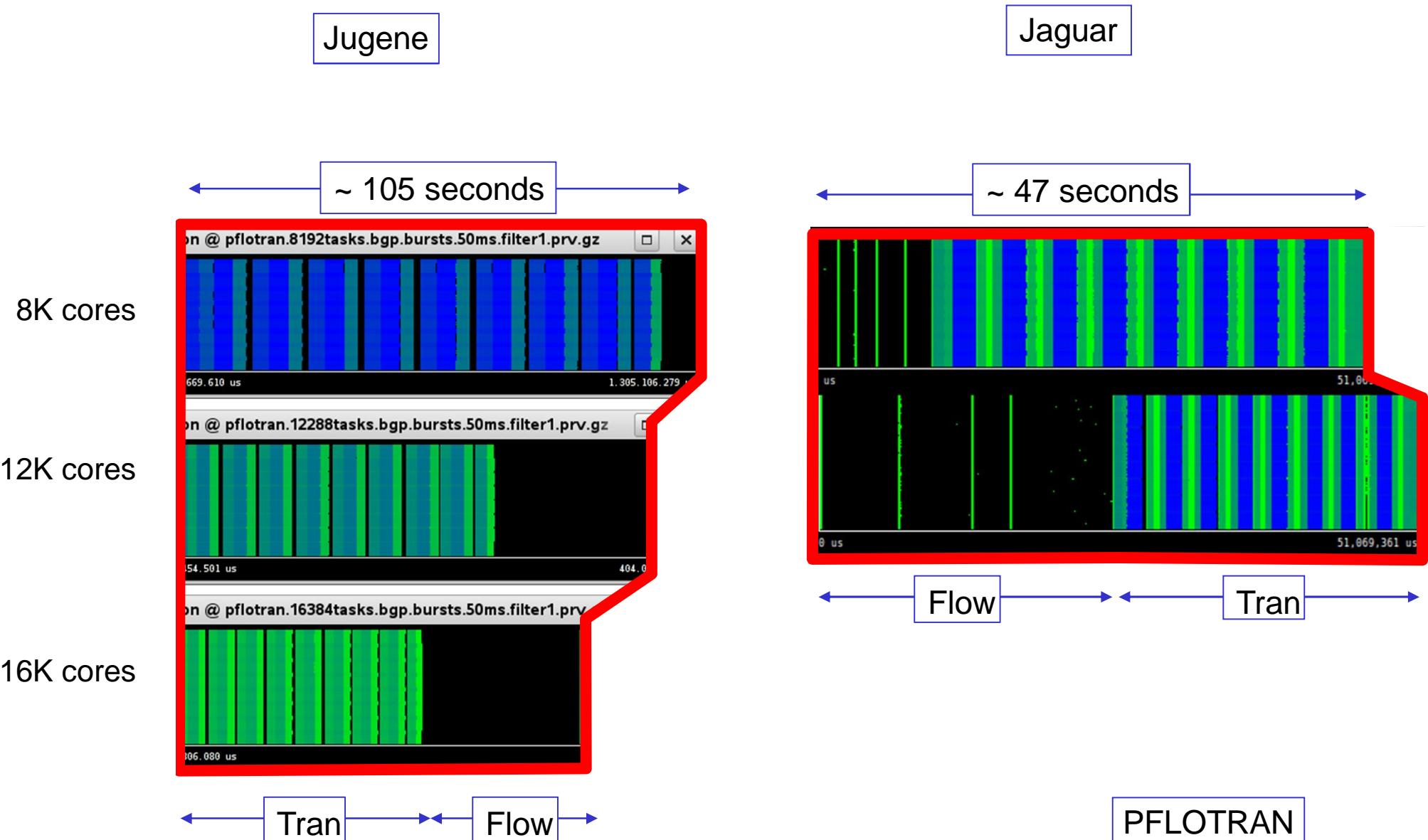
Spectral density



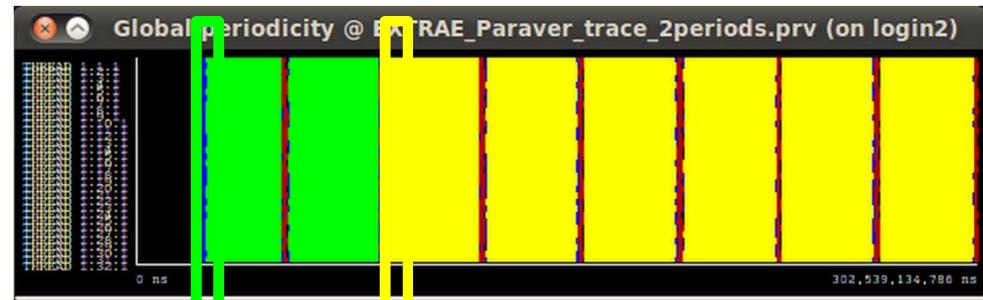
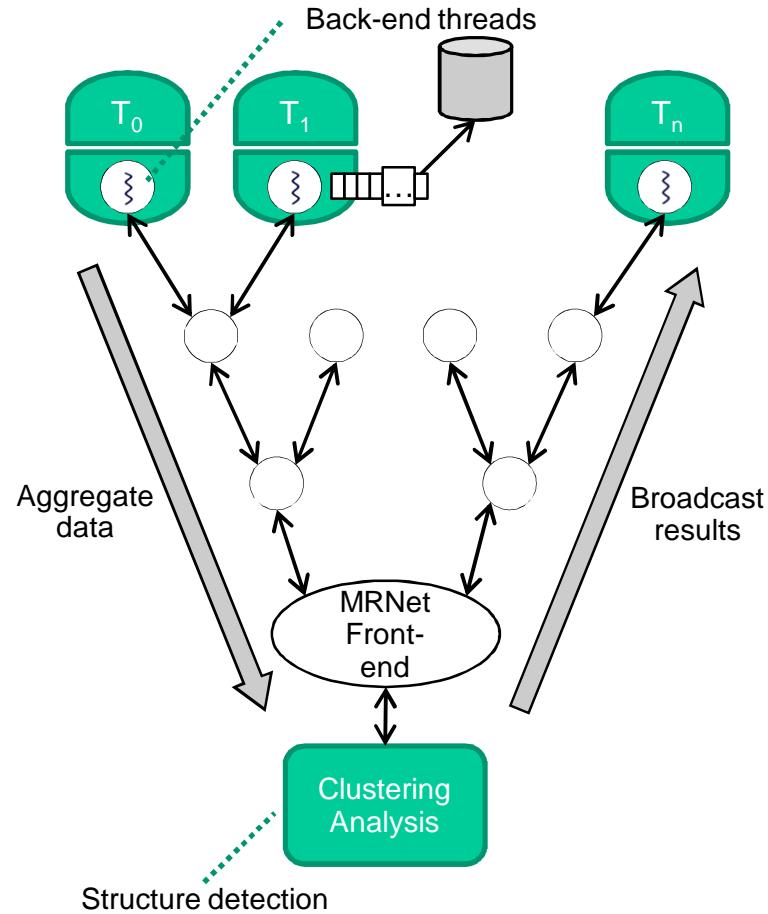
Autocorrelation



Scalability: online data reduction



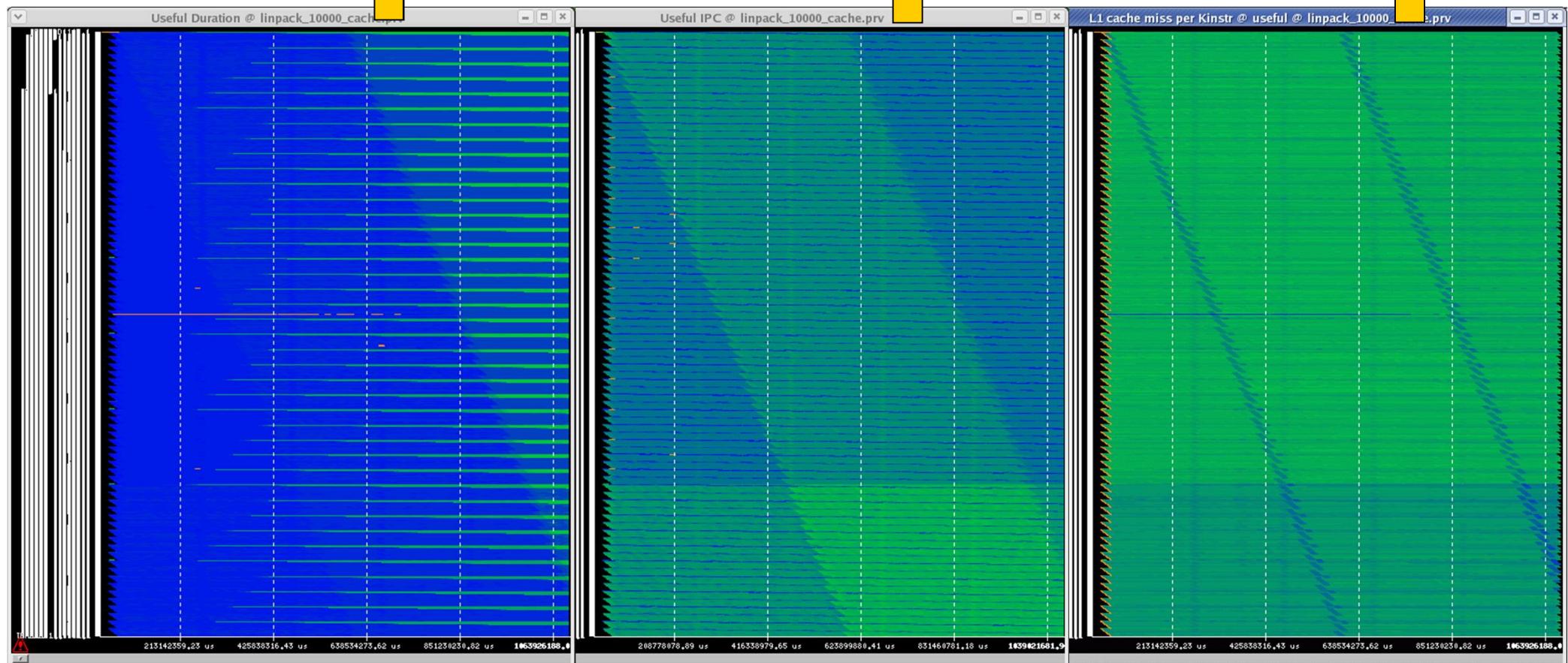
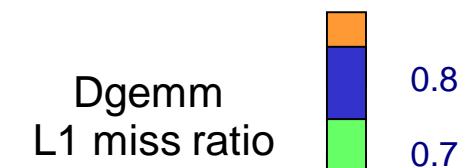
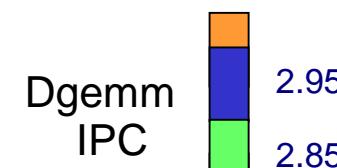
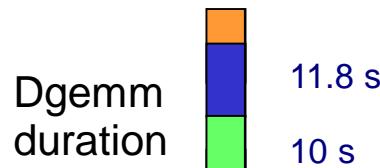
Scalability: online automatic interval selection



“G. Llort et all, “Scalable tracing with dynamic levels of detail” ICPADS 2011

Scalable display: Non linear rendering

Linpack @ Marenostrum: 10k cores x 1700 s



Conclusion



- Extreme flexibility:
 - Maximize iterations of the hypothesis – validation loop
 - Learning curve
 - “Don’t ask whether something can be done, ask how can it be done”
- Detailed and precise analysis
 - Squeeze the information obtainable form a single run
 - Insight and correct advise with estimates of potential gain
- Data analysis techniques applied to performance data

www.bsc.es/paraver