Damian Pacynko

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EDUCATION

University of Toronto Sep 2020 – May 2025
Bachelor of Applied Science in Electrical Engineering - 3.47 CGPA
Business Minor
Relevant Courses: Computer and Programming Fundamentals; Strategies and Practice I and II; Digital Systems; Computer Organization; Digital Electronics; Computer Architecture

SKILLS

Programming
C, C++, Verilog, ARM Assembly, Python, MATLAB

Analysis/Simulation
ModelSim, Quartus, Altium, LTspice, Typhoon

Soft Skills
Teamwork, Leadership, Problem-Solving, Communication

WORK AND TECHNICAL EXPERIENCE

Urban Concept Electrical Division Lead Oct 2021 – present
University of Toronto Supermileage Team (UTSM) Toronto, ON
- Managed 3 electrical sub-teams in the planning and design of the electrical systems integrated in U of T's first hydrogen-powered vehicle.
- Designed a custom STM32-based on-board computer for collecting data from various sensors using UART, I2C, and SPI communication protocols.

Sales Assistant Apr 2020 – Aug 2022
Sikorski Sausages Company LTD London, ON
- Coordinated shipments out of province by communicating with the shipping company and client, leading to smooth product deliveries.
- Responsible for the collection, packing, and delivery of $100,000+ worth of product orders every week.

Project Manager Sep 2020 – May 2021
Eng. Strategies and Practices I and II Design Project, U of T Toronto, ON
- Organized and devised a system that ensured members completed deliverables on time.
- Led the team in the smooth completion of the design of an accessible deck for a client.

PROJECTS

IQ RF Quadrature Mixer
Hardware Design and Communication, U of T
- Designed an IQ quadrature mixer module in the transmitter chain of a software defined radio (SDR).
- Prototyped the design on a breadboard and tested the circuit using oscilloscopes and function generators.
- Laid out and manufactured a PCB using Altium and soldered components onto the board.
- Integrated the module into a fully working SDR.

16-bit Custom Processor
Computer Organization, U of T
- Designed and implemented a custom 16-bit, 8 register, interrupt-enabled soft processor in Verilog.
- Employed a custom instruction set architecture, which was debugged using microbenchmarks in ModelSim.
- Tested the processor functionality using custom made assembly programs.

EXTRACURRICULAR ACTIVITIES

Co-ed Div 1 Intramural Soccer Sep 2021 – present
Team Captain Toronto, ON

Tri-Campus D-League Soccer Jan 2022 – present
Member, Leadership Group Toronto, ON

UTSM Oct 2021 – present
Electrical Division Lead Toronto, ON