

1 2 3 4 5	
VO.0.1. Demoved 10k register between CIE and CIC EMCYSTOD. Would have proveded ESTOD given from using high with minimal mainboard register divides givenit	
V0.0.1 — Removed 10k resistor between SJ5 and SIG_EMCYSTOP. Would have prevented ESTOP signal from going high with mini mainboard resistor divider circuit. Corrected mini mainboard symbol voltage on throttle input to 0—6.6v (V2 bottom right corner,) also corrected in notes.	
Confected mini manibulard symbol voltage on throttle input to v=0.0v (v2 bottom right conner), also confected in notes.	
Sheet: /Revisions/ File: Revisions.kicad_sch	
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