

# Universidade do Minho

Escola de Engenharia Departamento de Informática

Damien da Silva Vaz

Implementing a Syntax Directed Editor for LISS.



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Implementing a Syntax Directed Editor for LISS.

Master dissertation
Master Degree in Computer Science

Dissertation supervised by

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### **ACKNOWLEDGEMENTS**

Firstly, I would like to thank my supervisor Pedro Rangel Henriques and co-supervisor Daniela da Cruz. They are the most who supported me throw this ambitious project and took me to the final stage of my university career.

Thank you also to my family and friends (Ranim, Bruno, Chloé, Tiago, Tamara, Saozita, Nuno, David, Natália) for supporting me.

And last but not least, I would like to dedicate this thesis to my, particularly, most beautiful mother. Despite you couldn't be here to watch me conclude my studies. Wherever you are, I hope that you are proud of me. None of this could have been made without their unconditional help.

### ABSTRACT

The aim of this master work is to implement LISS language in ANTLR compiler generator system using an attribute grammar which create an abstract syntax tree (AST) and generate MIPS assembly code for MARS (MIPS Assembler and Runtime Simulator). Using that AST, it is possible to create a Syntax Directed Editor (SDE) in order to provide the typical help of a structured editor which controls the writing according to language syntax as defined by the underlying context free grammar.

### **RESUMO**

O tema desta dissertação é implementar a linguagem LISS em ANTLR com um gramática de atributos e no qual, irá criar uma árvore sintática abstrata e gerar MIPS assembly código para MARS (MIPS Assembler and Runtime Simulator). Usando esta árvore sintática abstrata, criaremos uma SDE (Editor Dirigido a Sintaxe) no qual fornecerá toda a ajuda típica de um editor estruturado que controlará a escrita de acordo com a gramática.

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## A LISS CONTEXT FREE GRAMMAR

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### INTRODUCTION

In informatics, solving problems with computers is related to the necessity of helping the end-users, facilitating their life. And all these necessities pass through developers who creates programs for this purpose.

However, developing programs is a difficult task; analyzing problems, and debugging software takes effort and time.

And this is why we must find a solution for these problems.

Developing a software package requires tools to help the developers to maximize their programming productivity. These tools are: on one hand, compilers to generate lower-level code (machine code) from the high-level source code (the input program written in an high-level programming language); on the other hand, editors to create that source code. And to make easier and safer the programmers work, high-level programming languages were created for facilitating their work.

This is not enough to overcome all the difficulties for creating a program in a safety way and having a high level productivity!

This is why we need to have fresh ideas and to implement more features to help on solving these problems.

#### 1.1 OBJECTIVES

In this work, this project aims to develop an editor with the concept of a SDE (Syntax Directed Editor).

It is intended that the editor works with language designed by the members of the Language Processing group at UM which is called LISS.

LISS language will be specified by an attribute grammar that will be passed, as input, to ANTLR. The compiler generated by ANTLR will generate MIPS assembly code (lower-level source code).

The front-end and the back-end of that compiler will be explained and detailed along the next pages.

### 1.2. Research Hypothesis

#### 1.2 RESEARCH HYPOTHESIS

### 1.3 THESIS OUTCOMES

### 1.4 DOCUMENT STRUCTURE

In this section, the project planned for this master thesis will be explained.

First, create an ANTLR version of the CFG grammar for LISS language.

Second, extend the LISS CFG to an AG in order to specify throw it the generation of MIPS assembly code. To verify the correctness of the assembly code generated, a simple MIPS simulator, named MARS, will be selected to provide all the tools for checking it.

Third, the desired Structure-Editor, SDE, will be developed based on ANTLR. It will be implemented in with Java SWING because ANTLR has always been implemented via Java and it is said, also, to use Java target as a reference implementation mirrored by other targets. SWING is a GUI widget toolkit for Java which provides all the API for creating an interface with Java. At this phase, we will create an IDE similar to other platforms but with the capacity of being a syntax-directed editor.

Fourthly, to complete the SDE functionality, an incremental compiler shall be included. Incremental compilation (Reps et al., 1983; Holsti, 1986; Vogt et al., 1990) means that only the part that was changed must be processed again. And like that, both tasks (edition and compilation) are done synchronously at the same time and having an editor which compiles cleverly.

Finally, exhaustive and relevant tests will be made with the tool created and, the outcomes will be analyzed and discussed.

### LANGUAGES AND GRAMMAR: CONCEPT & TOOLS

A grammar (Chomsky, 1962; Gaudel, 1983; Waite and Goos, 1984; Aho et al., 1986; Kastens, 1991b; Muchnick, 1997; Hopcroft et al., 2006; Grune et al., 2012) is a set of derivation rules (or production) that explains how words are used to build the sentences of a language.

A grammar (Deransart et al., 1988; Alblas, 1991; Kastens, 1991a; Swierstra and Vogt, 1991; Deransart and Jourdan, 1990; Räihä, 1980; Filè, 1983; Oliveira et al., 2010) is considered to be a language generator and also a language recognizer (checking if a sentence is correctly derived from the grammar).

The rules describe how a string is formed using the language alphabet, defining the sentences that are valid according to the language syntax.

One of the most important researchers in this area was Noam Chomsky. He defined the notion of grammar in computer science's field.

He described that a formal grammar is composed by a finite set of production rules (left hand side  $\mapsto$  right hand side)

where each side is composed by a sequence of symbols.

These symbols are split into two sets : non terminals, terminals; the start symbol is a special non-terminal.

There is, always, at least one rule for the start symbol (see Figure 1) followed by other rules to derive each non-terminal. The non terminals are symbols which can be replaced and terminals are symbols which cannot be.

One valid sentences (Example in Figure 1), could be: bbebee.

In the compilers area two major classes of grammars are used : CFG (Context-free grammar) and AG ( Attribute Grammar).

The difference between these two grammars are that a CFG is directed to define the syntax (only) and, AG contains semantic and syntax rules.

An AG is , basically, a GFC grammar extended with semantic definitions. It is a formal way to define attributes for the symbols that occur in each production of the underlying grammar. We can associate values to these attributes later, after processed with a parser; the evaluation will occur applying those semantic definition to any node of the abstract syntax tree. These attributes are divided into two groups: synthesized attributes and inherited attributes.



Figure 1.: CFG example <sup>1</sup>

The synthesized attributes are the result of the attribute evaluation rules for the root symbol of each subtree, and may also use the values of the inherited attributes. The inherited attributes are passed down from parent nodes to children or between siblings.

Like that it is possible to transport information anywhere in the abstract syntax tree which is one of the strength for using an AG (as seen on Listing 2.1).

```
facturas : fatura (facturas)*
           ;
 fatura : 'FATURA' cabec 'VENDAS' corpo {System.out.println("Total Factura
     : "+$corpo.totOut);}
         ;
7 cabec : idFat idForn 'CLIENTE' idClie {System.out.println("Factura n: "+
     $idFat.text);}
        ;
8
 idFat : numFat ;
10
11
numFat: ID;
 idForn : nome morada 'NIF:' nif 'NIB:' nib
15
16
 idClie: nome morada 'NIF:' nif
17
18
20 nome : STR ;
```

#### 2.1. Formal Grammar

```
22 morada : STR;
 nif : STR;
25
 nib : STR;
  corpo returns [int totOut]
      : linha '.' {$totOut += $linha.linhatot;}
29
         (linha '.' {$totOut += $linha.linhatot;})*
30
31
  linha returns [int linhatot]
33
         : refProd '|' valUnit '|' quant {$linhatot = $valUnit.val * $quant.
34
            quan; System.out.println("Ref: "+$refProd.text+" Total linha: "+(
            $linhatot)+" Euros");}
35
        ;
    refProd : ID;
37
  valUnit returns [int val]
           : NUM \{ val = NUM.int; \}
39
40
  quant returns [int quan]
      : NUM {$quan = $NUM.int;}
43
```

Listing 2.1: Example of an AG

In this way, an AG will be used to specify the translation from syntax tree directly into code for some specific machine or into another intermediate language. For our thesis, the AG will be processed by ANTLR tool in order to build automatically the parser, the attribute evaluator, and the code generation.

### 2.1 FORMAL GRAMMAR

According to Noam Chomsky, a classic formalization of generative grammars is composed by:

- A finite set N of nonterminals symbols.
- A finite set  $\Sigma$  of terminals symbols.
- A finite set P of production rules.

#### 2.1. Formal Grammar

### • A start symbol $S \in P$

A grammar is formally constructed by that tuple  $(N, \sum, P, S)$ .

Grammar is a set of productions rules which describes the syntax of the language (not semantic). Each grammar has only one start symbol production that defines where the grammar begins. And each production is composed by two things: LHS (Left Hand Side) and RHS (Right and Side). Left Hand Side represents the non terminal and the right hand side represents the behaviour of the rule (composed by non terminal and terminal).

```
liss: 'program' identifier body;
```

Listing 2.2: A rule production

In 2.2, we can see that it is composed by two sides. The left hand side and the right hand side, delimited by ':'. On the LHS, 'liss' is a non-terminal and on the RHS, it is composed by the terminal 'program' followed by two non-terminals. This is the syntax of one production rule of the grammar.

Now let's speak about the entire syntax of the LISS.

#### LISS LANGUAGE

LISS (da Cruz and Henriques, 2007a) -that stands for Language of Integers, Sequences and Sets- is an imperative programming language, defined by the Language Processing members (Pedro Henriques and Leonor Barroca) at UM for teaching purposes (compiler course).

The idea behind the design of LISS language was to create a simplified version of the more usual imperative languages although combining functionalities from various languages.

It is designed to have atomic or structured integer values, as well as, control statements and block structure statements.

Now, let's explain in the next sections the basic statements of the language and its data types, using a context free gammar.

### 3.1 LISS DATA TYPES

There are 5 types available. From atomic to structured types, they are known as: integer, boolean, array, set and sequence.

Used for declaring a variable in a program, the data type gives us vital information for understanding what kind of value we are dealing with.

Let's obverse a LISS code example:

```
a -> integer;
b -> boolean;
c -> array size 5,4;
d -> set;
e -> sequence;
```

Listing 3.1: Declaring a variable in LISS

As we can see in Listing 3.1, some variables ('a','b','c','d' and 'e') are being declared each one associated to a type ('integer', 'boolean', 'array', 'set' and 'sequence'). Syntactically, in

LISS, this is done by writing the variable name followed by an arrow and the type of the variable (see Listing 3.2).

```
variable_declaration : vars '->' type ';'
2
    vars : var (',' var )*
3
4
    var : identifier value_var
    value_var :
7
            | '=' inic_var
9
    type: 'integer'
10
       'boolean'
11
        'set'
        'sequence'
13
        | 'array' 'size' dimension
14
15
    dimension : number (',' number )*
16
17
    inic\_var : constant
18
            | array_definition
19
            | set_definition
            | sequence_definition
21
22
    constant : sign number
23
           | 'true'
24
             'false'
25
26
    sign :
27
        | '+'
28
29
```

Listing 3.2: CFG for declaring a variable in LISS

Variables that are not initialized, have a default value (according to Table 1).

Table 1.: LISS data types

Type	Default Value
boolean	false
integer	0
array	[0,,0]
set	{}
sequence	nil

Additionally, we may change the default values of the variables by initializing them with a different value (see an example in Listing 3.3). This can be made by writing an equal symbol after the variable name and, then, inserting the right value according to the type (see example in Listing 3.2).

```
a = 4, b -> integer;
t = true -> boolean;
vector1 = [1,2,3], vector2 -> array size 5;
a = { x | x<10} -> set ;
seq1 = <<10,20,30,40,50>>, seq3 = <<1,2>>, seq2 -> sequence;
```

Listing 3.3: Initialize a variable

Now, let's define which types are, correctly, associated with the arithmetic operators and functions in LISS (see Table 2).

Table 2.: Operations and signatures in LISS

Table 2.: Operations and signatures in LISS				
Operators && Functions	Signatures			
+ (add)	integer x integer -> integer			
- (subtract)	integer x integer -> integer			
(or)	boolean x boolean -> boolean			
++ (union)	set x set -> set			
/ (division)	integer x integer -> integer			
* (multiply)	integer x integer -> integer			
&& (and)	boolean x boolean -> boolean			
** (intersection)	set x set -> set			
== (equal)	integer x integer -> integer; boolean x boolean -> boolean			
!= (not equal)	integer x integer -> integer; boolean x boolean -> boolean			
<(less than)	integer x integer -> boolean			
>(greater than)	integer x integer -> boolean			
<= (less than or equal to)	integer x integer -> boolean			
>= (great than or equal to)	integer x integer -> boolean			
in (contains)	integer x set -> boolean			
tail	sequence -> sequence			
head	sequence -> integer			
cons	integer x sequence -> sequence			
delete	integer x sequence -> sequence			
copy	sequence x sequence -> void			
cat	sequence x sequence -> void			
isEmpty	sequence -> boolean			
length	sequence -> integer			
isMember	integer x sequence -> boolean			

So, in Table 2, we list the operators and functions, available in LISS, and their signature. In order to understand the table better, we will explain how to read the table and its signature with one example.

Consider the symbol '+' (Table 2), indicates that both operands must be of type integer. The result of that operation, indicated by the symbol '->', will be an integer. Semantically, operations must be valid according to Table 2; otherwise the operations would be incorrect and throw an error.

**Arrays.** LISS supports a way of indexing a collection of integer values such that each value is uniquely addressed. LISS also supports an important property of multidimensionality.

Called as 'array', it is considered to be a static structured type due to the fact that its dimensions and maximum size of elements in each dimension is fixed at the declaration time.

The operations defined over arrays are:

- 1. indexing
- 2. assignment

Arrays can be initialized, in the declaration section, partially or completely in each dimension. For example, consider an array of dimension 3x2 declared in the following way:

```
array1 = [[1,2],[5]] -> array size 3,2;
```

Thi is equivalent to the initialization below:

```
array1 = [[1,2],[5,0],[0,0]] \rightarrow array size 3,2;
```

Notice that the elements that are not explicitly assigned, are initialized with the value o (see Table 1).

The grammar for array declaration and initialization is shown below.

```
array_definition : '[' array_initialization ']'
;
array_initialization : elem (',' elem)*
;
elem : number
```

```
8  | array_definition
9 ;
```

**Sets.** The type *set*, in LISS, is a collection of integers with no repeated numbers.

It is defined by an expression, in a comprehension, instead of by enumeration of its element. A *set* variable can have an empty value and, syntactically, this is done by writing '{}'.

To define a set by comprehension, the free variable and the expression shall be return between curly brackets. The 'identifier' (free variable) is separated from the expression by an explicit symbol '|'.

The expression is built up from relational and boolean operators to define an integer interval.

The operations defined for sets are:

- 1. union
- 2. intersection
- 3. in (membership)

Let's see an example of its syntax below:

```
set1 = \{x \mid x < 6 \& x > -7\} -> set;
```

This declaration defines a set including all the integers from -7 to 6 (open interval) and others numbers are not included in the set.

The syntax for set declaration and initialization is:

```
set_definition : '{' set_initialization '}'
;

set_initialization :
| identifier '|' expression
;
```

**Sequences.** Considered as a dynamic array of one dimension, the type sequence is a list of ordered integers. But, in opposition to the concept of an array, its size is not fixed; this means that it grows dinamically at run time like a linked list. A sequence can have the empty value (syntactically done by writing '<<>>'). If not empty, the sequence value is defined by enumerating its components (integers) in the right order. Let's see deeper with one example:

```
c=<<1,2,3>> -> sequence;
```

Listing 3.4: Example of valid operations using sequence on LISS

In the example of Listing 3.4 the sequence is defined by three numbers (3,2,1). The operations defined for the sequence are:

- 1. tail (all the elements but the first)
- 2. *head* (the first element of the sequence)
- 3. cons (adds an element in the head of the sequence)
- 4. *delete* (remove a given element from the sequence)
- 5. *copy* (copies all the elements to another sequence)
- 6. *cat* (concatenates the second sequence at the end of the first sequence)
- 7. *isEmpty* (true if the sequence is empty)
- 8. *length* (number of elements of the sequence)
- 9. *isMember* (true if the number is an element of the sequence)

Those operations will be explained further and deeper.

The grammar below defines how to declare a sequence:

### 3.1.1 LISS lexical conventions

Once you've declared a variable of a certain type, you cannot redeclare it again with the same name.

The variable name must be unique (see Listing 3.5).

```
program single_variable_name{
    declarations
    int=1 -> integer;
    int=true -> boolean; //cannot declare this variable with this name
        (already exists)
    statements
}
```

Listing 3.5: Conflicts with variable names

Keywords cannot be used as variable names.

For example, you cannot declare a variable with the name *array* due to the fact that *array* is a keyword in LISS (in this case, a type).

See the example in Listing 3.6.

```
array -> array size 3,4; //variable 'array' cannot be declared as a name
integer -> integer;
```

Listing 3.6: Conflicts with keyword names

Variable names contain only letters and numbers, or the underscore sign. However the first character of the variable name must be a letter (lower or upper case). See the example below:

```
My_variable_1
MyVariable1
```

Numbers are composed of digits (one or more). Nothing more is allowed. See example below:

```
1 1562
2 1
```

A string is a sequence of n-characters enclosed by double quotes. See example below:

```
"This is a string"
```

#### 3.2 LISS BLOCKS AND STATEMENTS

A LISS program is always composed of two parts: declarations and statements (a program block). LISS language is structured with a simple hierarchy. And this is done by structuring LISS code as a block.

Any program begins with a name then appear the declaration of variables and subprograms. After that appear the flow of the program by writing statements.

Let's see one example (see Listing 3.7).

```
program sum{
    declarations
    int=2 -> integer;
    statements
    writeln(int+3);
}
```

Listing 3.7: The structure of a LISS program (example)

So a program in LISS begins by, syntactically, writing 'program' and then the name of the program (in this case, the name is 'sum'). A pair of curly braces delimits the contents of the program; that is done by opening it after the name of the program and closing it at the end of the program. After the left brace, appear the declaration and statement blocks.

As in a traditional imperative language (let's compare 'C language'), if we don't take the habit of declaring the variable always in a certain part of the code, it becomes confusing. This makes the programmer's life harder to understand the code when the code is quite long.

So, in LISS, we always declare variables first (syntactically written by 'declarations') and then the statements (syntactically written by 'statements'). This is due to the fact that LISS wants to help the user to create solid and correct code. And in this case, the user will always know that all the variable declarations will be always at the top of the statements and not randomly everywhere (see grammar in Listing 3.8).

```
liss: 'program' identifier body
;

body: '{'
    'declarations' declarations
    'statements' statements
    '}'

;
```

Listing 3.8: CFG for program in LISS

### 3.2.1 LISS declarations

The declaration part is divided into two other parts: variable declarations and subprogram declarations, both optional.

The first part is explained in section 3.1; the subprogram part will be discussed later in section 3.3.

This part is specified by the following grammar (see Listing 3.9).

```
declarations : variable_declaration* subprogram_definition*
;
```

Listing 3.9: CFG for declarations in LISS

### 3.2.2 LISS statements

As said previously, under the statements part, we control and implement the flow of a LISS program. In LISS, we may write none or, one or more statements consecutively.

Every statement ends with a semicolon, unless two type of statements (conditional and cyclic statements) as shown in Listing 3.10.

```
statements : statement*
2
   statement : assignment ';'
            write_statement ';'
            | read_statement ';'
5
            | function_call ';'
            | conditional_statement
7
            | iterative_statement
8
            | succ_or_pred ';'
            copy_statement ';'
10
            cat_statement ';'
12
```

Listing 3.10: CFG for statements in LISS

Let's see one example of a LISS program which shows how the language shall be used (see Listing 3.11).

```
program factorial {
    declarations
    res=1, i -> integer;
    statements
    read(i);
```

```
for(j in 1..i){
    res=res*j;

writeln(res);

}
```

Listing 3.11: Example of using statements in LISS

**Assignment.** This statement assigns, as it is called, values to a variable and it is defined for every type available on LISS. This operation is done by writing the symbol "=" in which a variable is assigned to the left side of the symbol and a value to the right side of the symbol.

Notice that an assignment requires that the variable on the left and the expression on the right must agree in type.

Let's see in Listing 3.12 an example.

```
program assignment1 {
    declarations
    intA -> integer;
    bool -> boolean;
    statements
    intA = -3 + 5 * 9;
    bool = 2 < 8;
}</pre>
```

Listing 3.12: Example of assignment in LISS

In Listing 3.12, we can see assignment statements of integers and boolean types. Those assignments are correct, as noticed in the previous paragraphs, because they have the same type on the left and right side of the symbol equals (operations of integers assigned to a variable of integer type and operation of booleans assigned to a variable of boolean type).

The grammar that rules the assignment is shown at Listing 3.13.

```
assignment: designator '=' expression;
```

Listing 3.13: CFG for assignment in LISS

**I/O.** The input and output statements are also available in LISS.

The *read* operations, called syntactically as 'input' in LISS, assign a value to a variable obtained from the standard input and require to be an atomic value (in this case, only an integer value).

```
program input1 {
```

```
declarations
myInteger -> integer;
statements
input(myInteger);
}
```

Listing 3.14: Example of input operation in LISS

Notice that, in Listing 3.14, the variable *myInteger* must be declared and must be integer otherwise the operations fails. The grammar that rules the input statement, is shown in Listing 3.15.

```
read_statement : 'input' '(' identifier ')'
;
```

Listing 3.15: CFG for input operation in LISS

The *write* operations, called syntactically as 'write' or 'writeln' in LISS, print an integer value in the standard output. Notice that 'write' operation only prints the value and doesn't move to a new line; instead, 'writeln' moves to a new line at the end.

Listing 3.16 shows some more examples.

```
writeln(4*3);
writeln(2);
writeln();
```

Listing 3.16: Example of output operations in LISS

Note that the write statement may have as assignment, an atomic value as well as an empty value or some complex arithmetic expression (see grammar in 3.17).

Listing 3.17: CFG for output operation in LISS

**Function call.** The function call is a statement that is available for using the functions created in the program under the section 'declarations' (as described in Section 3.2.1). This will allow reusing functions that were created by calling them instead of creating duplicated code.

See Listing 3.18 for a complete example.

```
program SubPrg {
    declarations
      a = 4, b = 5, c = 5 -> integer;
      d = [10,20,30,40], ev -> array size 4;
      subprogram calculate() -> integer
10
         declarations
11
                fac = 6 \rightarrow integer;
12
                res = -16 \rightarrow integer;
13
           subprogram factorial (n -> integer; m -> array size 4) -> integer
16
                  declarations
17
                         res = 1 -> integer;
18
                  statements
                         while (n > 0)
                              res = res * n;
                              n = n -1;
23
                         }
25
                         for (a in o..3) stepUp 1
                           d[a] = a*res;
                         return res;
30
                }
31
         statements
                res = factorial(fac,d);
33
                return res/2;
34
       }
35
36
37
    statements
38
```

Listing 3.18: Example of call function in LISS

In Listing 3.18, we can see that the function *calculate()*, called in the main program, and that is created under the declarations section.

The grammar who rules the function call is shown in Listing 3.19.

```
function_call : identifier '(' sub_prg_args ')'

sub_prg_args :

args

args :

args : expression (',' expression )*

;
```

Listing 3.19: CFG for call function in LISS

### 3.2.3 LISS control statements

LISS language includes some statements for controlling the execution flow at runtime with two different kind of behaviour.

The first one is called conditional statement and it has only one variant in LISS language (see Listing 3.20).

The second one is called cyclic statement or iterative statement, and it has two variants (see Listing 3.20).

```
conditional_statement : if_then_else_stat
;
iterative_statement : for_stat
| while_stat
;
;
```

Listing 3.20: CFG for control statement in LISS

These control statements, mimics the syntax and the behaviour of other modern imperative language.

CONDITIONAL The if-statement, which is common across many modern programming languages, performs different actions according to decision depending on the truth value of a control conditional expression: an alternative 'else' block is also allowed (optional).

If the conditional expression evaluates 'true', the content of 'then' block will be executed. Otherwise, if the condition is 'false', the 'then' block is ignored; and if an 'else' block is provided it will be executed alternatively.

Let's see an example in Listing 3.21.

```
if (y==x)
then{
    x=x+1;
} else{
    x=x+2;
}
```

Listing 3.21: LISS syntax of a if statement

The code shown in Listing 3.21, means that the if-statement evaluates the conditional expression y==x'. If the expression, which must be boolean, is true, then every action in the 'then' block will be executed and the block 'else' will be ignored. Otherwise, if the condition is false, every action in the 'else' block is executed ignoring the 'then' block.

If the else-statement is not provided, the if-statement will finish and do not perform any actions.

The syntax of the if-statement in LISS is shown in Listing 3.22.

Listing 3.22: CFG for iterative statement in LISS

ITERATIVE We should take a look at the behaviour of each iterative control statement to understand it deeper.

The for-statement offers two variants to control the repetition. Normally, in a conventional way, the for-loop has a control variable which takes a value in a given range and step up or step down by a default or an explicit value.

In LISS, the control variable is set in a given integer interval defined by the lower and upper bounds. By default, the step is one, which means that the control variable is incremented by one at the end of each iteration but it is possible to increment or decrement it by a different value, setting it explicitly. Additionally, we may write a condition for filtering the values in the interval. This can be done as shown in the following example:

```
for(a in 1..10) stepUp 2 satisfying elems[a]==1{
    ...
}
```

Listing 3.23: LISS syntax of a for-loop statement

In Listing 3.23, the control variable 'a' is set to a range 1 to 10 and would be increased (due to the 'stepUp' constructor) by 2. Also there is a filter condition (after the 'satisfying' keyword) that restricts the values of 'a' to those that makes the condition 'elems[a]==1' true. Notice that the filter expression must be boolean.

After each cycle, the control variable will be incremented with value 2 and the filter condition tested again.

This is the first way of expressing the control in a for-loop statement. Let's see the second way in the sequel.

There is also the possibility to assign to the control variable the values in an array, like illustrated in the following example:

Listing 3.24: LISS syntax of a for-each statement on array

In Listing 3.24, the control variable 'b' is assigned with all of the elements of the array and begins with his lower index (zero) until his upper index (size of the array minus one). Notice that, in this case, we cannot apply an increment or decrement neither a filter condition.

The next grammar fragment describes the cycle 'for' in LISS:

```
minimum: number
           | identifier
12
13
    maximum: number
14
           identifier
15
16
    step :
17
        | up_down number
18
19
    up_down : 'stepUp'
20
           | 'stepDown'
21
22
    satisfy:
23
           'satisfying' expression
24
25
```

Listing 3.25: CFG for for-statement in LISS

Finally, the while-statement consists in a block of code that is executed repeatly until the control condition evaluates 'false'.

Each time that the 'while' block is performed, the conditional expression associated will be evaluated again to decide whether to repeat the execution of the statements in the block or to continue the normal program flow.

Let's see an example in Listing 3.26.

```
while (n > 0)
{
    res = res * n;
    pred n;
}
```

Listing 3.26: LISS syntax of a while-statement in LISS

In Listing 3.26, the while-statement is controlled by the conditional expression 'n>0' that is evaluated at the beginning. If the condition is true, then all the actions that are inside the braces will be performed. Later, after executing all the actions, the condition will be evaluated again. If the condition remains 'true', then those actions would be executed again otherwise if the condition is false, the while-statement will be exited.

The syntax that rule the while-statement is shown below:

```
while_stat : 'while' '(' expression ')'

'{' statements '}'

;
```

Listing 3.27: CFG for while-statement in LISS

### 3.2.4 Others statements

LISS language offers other statements to make it more expressive easing the codification of any imperative algorithm.

**Succ/Pred.** Those statements are available for incrementing or decrementing a variable. This is a common situation in modern programming languages, making life easier for the developers.

The keyword 'succ' means increment (successor) and the syntax 'pred' means decrease (predecessor). Only integer variables can be used with those constructors.

Listing 3.28 illustrates both statements.

```
succ int1;
pred int1;
```

Listing 3.28: Example of using succ/pred in LISS

As we can see in Listing 3.28, variable 'int1' is, first, incremented by 1 and then it is decremented also by 1.

Grammar of 'succ' and 'pred' in LISS is shown in Listing 3.29.

```
succ_or_pred : succ_pred identifier
;
succ_pred : 'succ'
| 'pred'
;
;
```

Listing 3.29: CFG for succ and pred in LISS

**Copy statement.** This statement is applied only to variables of type sequence. Basically, it copies one sequence to another sequence. Let's see an example in Listing 3.30.

```
copy(seq1, seq2);
```

Listing 3.30: Example of copy statement in LISS

Notice that 'copy' is a statement and not a function: it modifies the arguments but does not return any value.

In Listing 3.30, the statement 'copy' copies the content of the variable seq1 to seq2.

The grammar for 'copy' statement is in Listing 3.31.

### 3.3. LISS subprograms

```
copy_statement : 'copy' '(' identifier ',' identifier ')'
;
```

Listing 3.31: CFG for copy statement in LISS

### Cat statement.

'Cat' statement is simular to 'copy', it only operates with variables of type sequence. The behaviour of this statement is to concatenate a sequence to another sequence. Let's see an example in Listing 3.32).

```
cat(seq1, seq2);
```

Listing 3.32: Example of cat statement in LISS

In Listing 3.32, 'cat' concatenates the content of *seq2* to *seq1*. Again, 'cat' is not a function; it modifies the arguments instead of returning a value.

The grammar for cat-statement is shown in Listing 3.33.

```
cat_statement : 'cat' '(' identifier ',' identifier ')'
;
```

Listing 3.33: CFG for cat statement in LISS

### 3.3 LISS SUBPROGRAMS

In LISS, it is possible to organize the code by splitting the general block of statements into sub-programs. This allows the programmer to reuse or to give more clarity to his code by creating functions or procedures. Also, it is possible to create sub-programs inside sub-programs by using a nesting strategy.

The syntax that defines a sub-program in LISS is shown in Listing 3.34.

```
subprogram_definition: 'subprogram' identifier '(' formal_args ')'
    return_type f_body

;
f_body: '{'
        'declarations' declarations
        'statements' statements
        returnSubPrg
        '}'

formal_args:
        | f_args
```

### 3.3. LISS subprograms

```
f_args : formal_arg (',' formal_arg )*
12
13
    formal_arg : identifier '->' type
14
15
    return_type :
16
               /->' typeReturnSubProgram
17
18
    returnSubPrg:
19
                | 'return' expression ';'
20
21
```

Listing 3.34: CFG for block structure in LISS

Note that every variable declared inside of a sub-program is local, and it can be accessed only by other nested sub-programs. However, variables declared in the program (not in a sub-program) are considered global and can be accessed by any sub-program. The usual scope rules are applied to LISS.

As can be inferred from the syntax above (Listing 3.34), the body of a sub-program is identical to the body of a program — the same declarations can be made and similar statements can be used.

### 3.4. Evolution of LISS syntax

#### 3.4 EVOLUTION OF LISS SYNTAX

Due to the maturity of the language already done along the years, we have added some few but extra changes for a better experience of the programming language.

One of the first changes was concerned with declarations in order to avoid mixing functions and variable declarations. We, indirectly, teach the programmer by doing it in the right way. So we declare, first, the variables and then the functions.

```
declaration : variable_declaration * subprogram_definition * ;
```

Another change was to add punctuation after each statement (see Figure 3.35).

```
statement : assignment ';'
| write_statement ';'
| read_statement ';'
| conditional_statement
| iterative_statement
| function_call ';'
| succ_or_pred ';'
| copy_statement ';'
| cat_statement ';'
|;'
```

Listing 3.35: Function statement

Another change was adding also a 'cat\_statement' rule which works with only sequences. It concatenates a sequence with another sequence.

Regarding arrays, it was previously possible to use any expression to access elements of the array. So it was possible to index with a boolean expression what does not make any sense. Now only integers are allowed (see in Listing 3.36).

```
elem_array : single_expression (',' s2=single_expression )*
;
```

Listing 3.36: Rule element of array

In the previous version of LISS, it was allowed to create a boolean expression associating relational operators, but we decided to change that and not permit associativity; only able to create one boolean expression (see Listing 3.37). It does not make sense to have an expression like that : '3 == 4 == 5 != 6'.

```
expression : single_expression (rel_op single_expression )?
;
```

Listing 3.37: Rule for Boolean expression

We added the possibility of using parenthesis on expressions (see Listing 3.38).

```
factor: '(' expression ')'
;
```

Listing 3.38: Rule factor

We changed the rules of two pre-defined functions: 'cons' and 'del'. These functions were working both in the same way. Waiting for an expression and a variable as arguments. Now, we decide to change that allowing to expression as arguments giving more expressive power to those functions (see Listing 3.39).

```
cons // integer x sequence -> sequence
: 'cons' '(' expression ',' expression ')'
;

delete // del : integer x sequence -> sequence
: 'del' '(' expression ',' expression ')'
;
;
```

Listing 3.39: Rule cons and delete

Besides adding some improvements to the grammar, we additionally deleted a rule which we thought not necessary to control the for-statement (see Listing 3.40).

```
type_interval: 'in' range
| 'inArray' identifier
| 'inFunction' identifier
| 'inFunction' identifier
| 'inFunction' identifier
```

Listing 3.40: Rule type interval

Last but not least, we also added comments to the programming language, giving more power to the programmer.

```
fragment

COMMENT

: '/*'.*?'*/' /* multiple lines comment*/

| '//'~('\r' | '\n')* /* single line comment*/

;;
```

Listing 3.41: Lexical rule for Comment

### TARGET MACHINE: MIPS

MIPS, from Microprocessor without Interlocked Pipeline Stages, is a Reduced Instruction Set Computer (RISC) developed by MIPS Technologies. Born in 1981, a team led by John L. Hennessy at Stanford University began to work on the first MIPS processor.

The main objective for creating MIPS, was to increase performance with deep instructions pipelines, a main problem back to the 80's. Some instructions, as division, would take a longer time to complete due to the fact that CPU needed to wait that the division ended before passing to the next instruction into the pipeline.

As MIPS solved those problems, it was primarly used for embedded systems and video games consoles (which requires a lot of arithmetic computation).

Now, the architecture of MIPS, along the years, has gained maturity and provides different versions of it (MIPS<sub>32</sub>, MIPS<sub>64</sub>....) <sup>1</sup>.

Figure 2 illustrate the architecture of MIPS.

<sup>1</sup> according to wikipedia https://en.wikipedia.org/wiki/MIPS\_instruction\_set

## 4.1. MIPS coprocessors

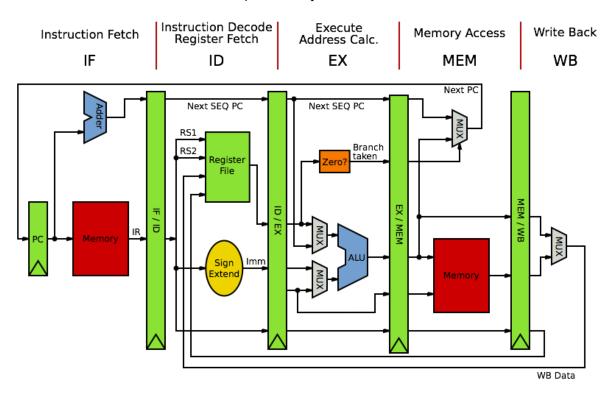


Figure 2.: MIPS architecture <sup>2</sup>

In this chapter, we will talk about all the architecture of MIPS with the 32-bit version.

## 4.1 MIPS COPROCESSORS

MIPS was born for solving complex arithmetic problems by reducing the consuming time in those operations.

And this is done by the implementation of coprocessors with MIPS.

The MIPS architecture defines four coprocessors designed, respectively, CPo, CP1, CP2 and CP3:

- 1. Coprocessor o, denoted by *CPo*, is incorporated on the CPU chips, it supports the virtual memory system and exception handling (also known as the *System Control Coprocessor*).
- 2. Coprocessor 1, denoted by *CP1*, is reserved the floating point coprocessor.
- 3. Coprocessor 2, denoted by CP2, is reserved for specific implementations.
- 4. Coprocessor 3, denoted by *CP*3, is reserved for the implementations of the architecture.

## 4.2. MIPS cpu data formats

Notice that coprocessor - *CPo*, translates virtual addresses into physical addresses, manages exceptions, and handles switch between kernel, supervisor and user modes.

## 4.2 MIPS CPU DATA FORMATS

The CPU of MIPS defines different formats:

- *Bit* (1 bit, b)
- *Byte* (8 bits, B)
- Halfword (16 bits, H)
- Word (32 bits, W)

## 4.3 MIPS COMPILER REGISTER USAGE

MIPS architecture has 32 registers dedicated and there are some conventions to use those registers correctly. Table 3 summarizes those registers, and their usage.

Table 3.: MIPS registers

Name	Number	nber Use	
\$zero	\$zero \$o has constant o		
\$at	\$1	register reserved for assembler (temporary)	No
\$vo - \$v1 \$2 - \$2 register reser		register reserved for values for function returns and expression evaluation	No
\$ao - \$a3	\$ao - \$a3  \$4 - \$7  registers reserved for function arguments		No
\$to - \$t7	\$8 - \$15	temporary registers	No
\$so - \$s7   \$16 - \$23   saved tempo		saved temporary registers	Yes
\$t8 - \$t9	\$24 - \$25	temporary registers	No
\$ko - \$k1	\$26 - \$27	register reserved for OS kernel	N/A
\$gp \$28		global pointer	Yes
\$sp	\$29	stack pointer	Yes
\$fp \$30		frame pointer	Yes
\$ra	\$31	return address	N/A

*Note: N/A (Not applicable)* 

The Table 3 is composed of 4 columns:

1. *Name* column refers to the nomenclature of the register available in MIPS. Those will be used to interact with the instructions available in MIPS.

## 4.3. MIPS compiler register usage

- 2. *Number* column defines the number of each register. This number can also be used to refer to the register in an instruction.
- 3. Use column refers to the meaning/definition of each register.
- 4. *Callee must preserve?* column provides information about the volatility of the register (used when a function is called).

Also, beside of those 32 registers, 3 registers are dedicated to the CPU. And they are known by:

- PC Program Counter register
- HI Multiply and Divide register higher result
- LO Multiply and Divide register lower result

*PC* is the register which holds the address of the instruction that is being executed at the current time; *HI* and *LO* registers has different context whenever the instruction is being executed. In this case, let's see what context they have:

- when there is a multiply ( *mul* instruction) operation, the *HI* and *LO* registers store the result of integer multiply.
- when there is a multiply-add (*madd* instruction) or multiply-subtract (*msub* instruction) operation, the *HI* and *LO* register store the result of integer multiply-add or multiply-subtract.
- when there is a division ( *div* instruction) operation, the *HI* register store the remainder of the division and the *LO* register store the quotient of the division operation.
- when there is a multiply-accumulate (instruction) operation, the *HI* and *LO* registers store the accumulated result of the operation.

See an overview of the MIPS registers in Figure 3.

## 4.3. MIPS compiler register usage

General Purpose Registers	Special Purpose	Registers
31 0	31	0
r0 (hardwired to zero)	Н	
r1	LC	)
r2		
r3		
r4		
r5		
r6		
r7		
r8		
r9		
r10		
r11		
r12		
r13		
r14		
r15		
r16		
r17		
r18		
r19		
r20		
r21		
r22		
r23		
r24		
r25		
r26		
r27		
r28		
r29		
r30	31	0
r31	PC	;

Figure 3.: MIPS register

### 4.4 MIPS INSTRUCTION FORMATS

Instructions, in MIPS, are divided into three types:

- R-Type
- I-Type
- J-Type

Each instruction is denoted by an unique mnemonic that represents the correspondent low-level machine instruction or operation.

Next sections provide the necessary details.

```
4.4.1 MIPS R-Type
```

R-Type instruction refers a register type instruction (it is the most complex type in MIPS). The idea behind that instruction is to operate with registers only.

This type has the following format, mostly, in MIPS (see Listing 4.1).

```
OP rd, rs, rt
```

Listing 4.1: R-Type instruction format

In Listing 4.1, the instruction is composed of one mnemonic, denoted by *OP*, and three operands, denoted by *rd* (destination register), *rs* (source register), *rt* (another source register).

The R-Type instruction format as the following mathematical semantics:

```
rd = rs OP rt
```

To understand better this instruction, let's see an example of one R-Type instruction in MIPS (see Listing 4.2).

```
add $t1, $t1, $t2
```

Listing 4.2: Example of a R-Type instruction

The instruction shown in Listing 4.2 means that register \$t1 shall be added (due to *add* mnemonic) to register \$t2 and their sum (the result) stored in register \$t1.

The following equivalence explains that meaning.

$$OP \ rd, \ rs, \ rt \iff rd = rs \ OP \ rt$$

$$\downarrow \downarrow$$

$$add \$t1, \$t1, \$t2 \iff \$t1 = \$t1 \ add \$t2$$

$$\downarrow \downarrow$$

$$\$t1 = \$t1 + \$t2$$

Table 4 defines the bit-structure of a R-Type instruction in a 32-bit machine.

Table 4.: R-Type binary machine code

opcode	rs	rt	rd	shift (shamt)	funct
6 bits	5 bits	5 bits	5 bits	5 bits	6 bits

Let's explain each of the columns in Table 4.

- **opcode** defines the instruction type. For every R-Type instruction, *opcode* is set to the value o. The *opcode* field is 6 bits long (bit 31 to bit 26).
- **rs** this is the first source register; it is the register where it will load the content of the register to the operation.The *rs* field is 5 bits long (bit 25 to bit 21).
- **rt** this is the second source register (same behaviour as *rs* register). The *rt* field is 5 bits long (bit 20 to bit 16).
- **rd** this is the destination register; it is the register where the results of the operation will be stored. The *rd* field is 5 bits long (bit 15 to bit 11).
- **shift amount** the amount of bits to shift for shift instructions. The *shift* field is 5 bits long (bit 10 to bit 6).
- **function** specify the operation in addition to the *opcode* field. The *function* field is 6 bits long (bit 5 to bit 0).

Let's see an example of a R-Type instruction and its transformation to machine code in Table 5.

add \$t0, \$t0, \$t1  

$$\downarrow$$
  
add \$8, \$8, \$9  
 $\downarrow$   
 $(8)_{10} = (01000)_2$   
 $(9)_{10} = (01001)_2$   
add instruction (funct field) =  $(100000)_2$ 

opcode (6bits)	rs (5bits)	rt (5bits)	rd (5bits)	shift (shamt) (5bits)	funct (6bits)
000000	01000	01001	01000	00000	100000

Table 5.: Transformation of R-Type instruction to machine code

In Table 5, the instruction 'add \$to, \$to, \$t1' will be normalized with the name of the register according to the number associated for the register in MIPS (see Table 3). Then we do some operations for the two numbers (8 and 9), we translate them as a binary number with 5 bits long. Also we give the information for the *add* instruction, which is set for the MIPS architecture (not predictable).

After that, we complete the table for R-Type instruction according to Table 4 with the informations available and the restriction/rules associated to R-Type instruction in MIPS.

Notice that the *opcode* field for R-Type instruction are set to the value o (according to the explanation from Table 4).

Finally, we have our binary machine code for the instruction 'add \$to, \$to, \$t1' in Table 5.

## 4.4.2 MIPS I-Type

I-Type instruction is an instruction which operate with an immediate value and a register value.

Several different Immediate ( *I-Type*) instructions formats are available.

Let's see those differents formats for this type in Table 6.

31 – 26	25 21	20 – 16	15 11	10 6	5 — o	
opcode	rs	rt immediate				
opcode	rd	offset				
opcode		offset				
opcode	rs	rt	rd offset		et	
opcode	base	rt	offset func		function	

Table 6.: Distinct I-Type instruction formats

In Table 6, there are 5 differents instruction formats which explains the structure of the bits for every instruction.

The most frequent MIPS I-Type instruction is the first one, denoted as Imm16 (Immediate instruction with 16 bits immediate value), is used for logical operands, arithmetic signed operands, load/store address byte offsets and PC-relative branch signed instruction displacements (see Table 7).

$$31 - 26$$
  $25 - 21$   $20 - 16$   $15 - 0$  opcode rs rt immediate

Table 7.: Immediate (I-Type) Imm16 instruction format

Let's see examples of Imm16 instruction:

```
addi $to, $to, 10 // Arithmetic operation
ori $to, $t1, 5 // Logical operation
beq $to, $t1, 1 // Conditional branch operation
lw $to, array1($to) //Data transfer operation
```

The second instruction, denoted as Immediate Off21 instruction (Immediate instruction with 21bits offset), is used for comparing a register against zero and branch (offset field is larger than the usual 16-bit field (immediate field of the first instruction from the table above)). See Table 8.

Table 8.: Immediate (I-Type) Off21 instruction format

The third instruction, denoted as Immediate Off26 instruction (Immediate instruction with 26 bits offset), is used for PC-relative branches with very large displacements (unconditional branches (BC mnemonic instruction) & branch-and-link (BALC mnemonic instruction) with a 26-bit offset,). See Table 9.

31 — 26	25 -	o
opcode		offset

Table 9.: Immediate (I-Type) Off26 instruction format

The fourth instruction, denoted as Immediate Off11 instruction (Immediate instruction with 11 bits offset), is used for the newest encodings of coprocessor 2 load and store instructions (LWC2, SWC2, LDC2, SWC2). See Table 10.

31 — 26	25 — 21	20 ——— 16	15 ——— 11	10 0
opcode	rs	rt	rd	offset

Table 10.: Immediate (I-Type) Off11 instruction format

Finally, the last one (fifth instruction), denoted as Immediate Off9 instruction (Immediate instruction with 9 bits offset), is used for SPECIAL3 instructions such as EVA memory acceses ( *LBE* mnemonic instruction). Also this is primarly used for instruction encodings that have been moved, such as *LL* menmonic and *SC* mnemonic instruction. See Table 11.

31 — 26	25 21	20 —— 16	15 7	6	5 ——— o
opcode	base	rt	offset	0	function

Table 11.: Immediate (I-type) Off9 instruction format

Notice that, for our project related to the thesis and the difficulty level required, only the first instruction (Immediate (I-Type) Imm16 instruction format) will be explained. The others format are complicated and not really related to the project.

4.4.3 MIPS J-Type

## 4.5. MIPS assembly language

## 4.5 MIPS ASSEMBLY LANGUAGE

Each of the high-level language statements in MIPS are executed sequentially (excepted loop and conditional statements).

### 4.5. MIPS assembly language

MIPS has 6 type of instructions:

- instructions for data transfer
- instructions for arithmetic
- instructions for logical
- instructions for conditional branch
- instructions for unconditional jump

It uses a mnemonic to represent each low-level machine instruction or operation. Operations require three operands in order to form a complete instruction in MIPS assembly code, the address for the result and the address of the two operands.

A simple example of a complete instruction can be seen in Listing 4.3.

```
add $s1,$s2,$s3
```

Listing 4.3: Sum of two registers in MIPS assembly code

The instruction shown in Listing 4.3, means that register \$s2 shall be added to register \$s3 and their sum (the result) stored in \$s1 register (note that each operand is represented by \$ sign).

As mentioned in the previous section, the AG processed by the compiler generator ANTLR will compose MIPS assembly. The MIPS assembly instructions generated so far will be converted into executable machine code by an assembler included in MARS environment (a MIPS simulator and debugger) according to the process depicted in Figure 4.

After presenting in the previous chapter the essential definitions for the intended work, in this chapter the project planned for this master thesis will be explained.

First, create an ANTLR version of the CFG grammar for LISS language.

Second, extend the LISS CFG to an AG in order to specify throw it the generation of MIPS assembly code. To verify the correctness of the assembly code generated, a simple MIPS simulator, named MARS, will be selected to provide all the tools for checking it.

Third, the desired Structure-Editor, SDE, will be developed based on ANTLR. It will be implemented in with Java SWING because ANTLR has always been implemented via Java and it is said, also, to use Java target as a reference implementation mirrored by other targets. SWING is a GUI widget toolkit for Java which provides all the API for creating an interface with Java. At this phase, we will create an IDE similar to other platforms but with the capacity of being a syntax-directed editor.

Fourthly, to complete the SDE functionality, an incremental compiler shall be included. Incremental compilation (Reps et al., 1983; Holsti, 1986; Vogt et al., 1990) means that only

### 4.5. MIPS assembly language

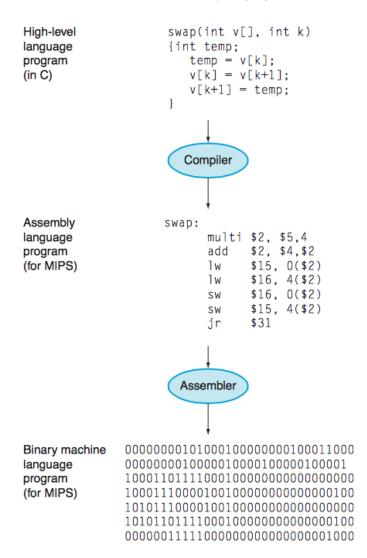


Figure 4.: Stages converting high-level language into binary machine language <sup>3</sup>

the part that was changed must be processed again. And like that, both tasks (edition and compilation) are done synchronously at the same time and having an editor which compiles cleverly.

Finally, exhaustive and relevant tests will be made with the tool created and, the outcomes will be analyzed and discussed.

### COMPILER DEVELOPMENT

Earlier in the history of computers, software was primarily written in assembly language. Due to the low productivity of programming assembly code, researchers invented a way that add some more productivity and flexibility for programmers; they created the compiler allowing to wire programs in high level programming languages.

A compiler is a software program which converts a high-level programming language (source code) into a lower level programing language for the target machine (known as machine code or assembly language).

The compiler task is divided into several steps (see Figure 5):

- 1. Lexical analysis
- 2. Syntactic analysis or parsing
- 3. Semantic analysis
- 4. Optimization
- 5. Code generation

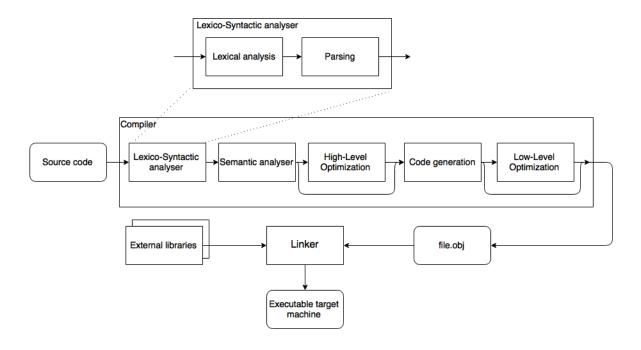


Figure 5.: Traditional compiler

The task of constructing a compiler for a particular source language is complex. Firstly, the lexical analysis must recognize words; these words are a string of symbols each of which is a letter, a digit or a special character.

The Lexical analysis divides program text into "words" or "tokens" and once words are identified, the next step is to understand sentence structure (role of the parser). We can think the parsing as an analogy of our world by constructing phrases which requires a subject, verb and object. So, basically, the parser do a diagramming of sentences (see Figure 6). Once the sentence structure is understood, we must extract the "meaning" with the semantic analyzer. The duty of the semantic analyzer is to perform some semantic analysis to catch some inconsistencies. Finally, after that, it may or may not have some optimization regarding the source code. Then the code generator translates the intermediate representation of the high-level programming into assembly code (lower level programming).

### 5.1. Compiler generation with ANTLR



Figure 6.: Parsing

### 5.1 COMPILER GENERATION WITH ANTLR

Terence Parr, the man who is behind ANTLR (ANother Tool for Language Recognition (Parr, 2007, 2005)) made a parser (or more precisely, a compiler) generator that reads a context free grammar, a translation grammar, or an attribute grammar and produces automatically a processor (based on a LL(k) recursive-descent parser) for the language defined by the input grammar.

An ANTLR specification is composed by two parts : the one with all the grammar rules and the other one with lexer grammar.

Listing 5.1 is the one with the grammar rules; in that case it is an example of an AG.

Listing 5.1: AG representation on ANTLR

On the other hand, the lexer grammar defines the lexical rules which are regular expressions as can be seen in Listing 5.2. They define the set of possible character sequences that

## 5.1. Compiler generation with ANTLR

are used to form individual tokens. A lexer recognizes strings and for each string found, it produces the respective tokens.

```
Lexer ______*/

ID : ('a'..'z'|'A'..'Z'|'_-') ('a'..'z'|'A'..'Z'|'o'..'9'|'_-'|'-')*

NUM : 'o'..'9'+
```

Listing 5.2: Lexer representation

The parser generator by ANTLR will be able to create an abstract syntax tree (AST) which is a tree representation of the abstract syntactic structure of source code written in a programming language (see Figure 7).

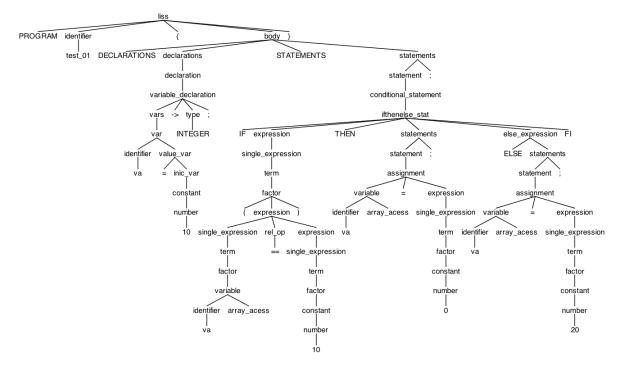


Figure 7.: AST representation

ANTLR will be used to generate MIPS assembly code according to the semantic rule specified in the AG for LISS language.

## SDE: DEVELOPMENT

Before we try to explain the concept of a Syntax-Directed Editor (SDE) (Reps and Teitelbaum, 1989b; Ko et al., 2005; MI-students et al., 2010; Teitelbaum and Reps, 1981; Reps et al., 1986; Reps and Teitelbaum, 1989a; Arefi et al., 1989), let's start defining what is an Integrated Development Environment (IDE).

An IDE is described as a software application that provides facilities to computer programmers for software development. It consists , normally, of a source code editor, a compiler, a debugger, and others tools. IDEs are designed for maximizing the productivity of programmers with visual interface and contains, normally, an interpreter, a compiler or both (see Figure 8).

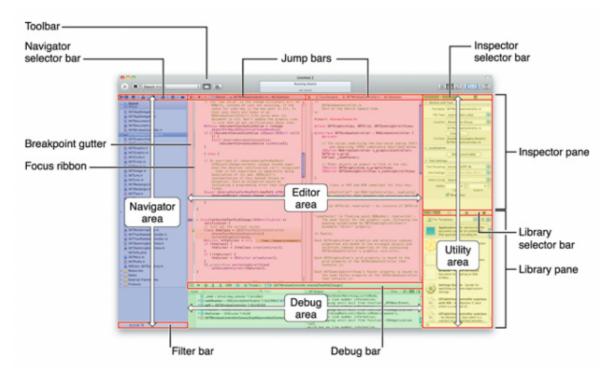


Figure 8.: Example of an IDE visual interface (XCode) <sup>1</sup>

### 6.1. What is a template?

Programs are created top down in the editor sections by inserting statements and expressions at the right cursor position of the current syntactic template and we can, by the cursor, change simply from one line of text to another one.

A SDE has the same approach of an IDE which is (as said above) an interactive programming environment with integrated facilities to create, edit, execute and debugging programs. The difference between them is that SDE encourages the program writing at a high level of abstraction, and promotes the programming based on a step by step refinement process.

It liberates the user from knowing the language syntactic details while editing programs. SDE is basically guided by the syntactic structure of a programming language in both editing and execution. It is a hybrid system between a tree editor and a text editor.

The notion of cursor is really important in the context of SDE because, when the editing mode is on, the cursor is always located in a placeholder of a correct template (see next section) and the programmer may only change to another correct template at that placeholder or to its constituents.

It reinforces the idea that the program is a hierarchical composition of syntactic objects, rather than a sequence of characters.

### 6.1 WHAT IS A TEMPLATE?

The grammar of a programming language is a collection of production (or derivation rules) that state how a non-terminal symbol (LHS) is decomposed in a sequence of other symbols (RHS). A template is just the RHS of a grammar rule. Templates cannot be altered, they have placeholders for inserting a phrase or another template and they are generated by editor commands, according to the grammar production.

```
IF (condition)
THEN statement
ELSE statement
```

Listing 6.1: Example of a IF Conditional template

In Listing 6.1 we can see the editor template for the if-statement, where *condition* and *statement* are placeholders.

The notion of template is very important because templates are always syntactically correct for two reasons:

- 1. First, the command is validated to guarantee that it inserts a template permitted.
- 2. Second, the template is not typed, so it contains no lexical errors.

### 6.2. Conception of the SDE

So a correct program (i.e., a valid sentence of the programming language) is created by choosing templates and replacing placeholders by others templates or by concrets values (numeric or string constants or identifiers).

To clarify the definition of SDE, we will explain it with the help of an example.

```
PROGRAM <identifier>
VAR
     i:integer
BEGIN
                                         statement -> NIL
     i:=1;
                                                   assign
                                                   if
     WHILE ( i <> 100) DO
                                                   while
           BEGIN
                                                   io
                 [<statement>];
                i:=(i+1)
           END
END.
```

Figure 9.: SDE example

Figure 9 shows the main window of a standard Syntax-Directed Editor. In this figure, two boxes are displayed. The left one is the editor window where we code the program, and the right one exhibits templates choices.

Every <...> tag represents a placeholder, and [...] represents the actual cursor position.

As the cursor changes its position, moving from one placeholder to another placeholder, the right box will be updated according to the grammar rules in the context of the new cursor position. In this example, the cursor in Figure 9 is placed at the placeholder corresponding to a *statement*; at the same time, the right box will be updated with all the possibile templates according to the *statement* derivation rules (RHS).

To sum up, this is how a SDE works.

### 6.2 CONCEPTION OF THE SDE

# CONCLUSION

7.1 FUTURE WORK

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## LISS CONTEXT FREE GRAMMAR

LISS (da Cruz and Henriques, 2007a) is an imperative programming language, defined by the Language Processing members (Pedro Henriques and Leonor Barroca) at UM for teaching purposes. It allows handling integers, sets of integers, dynamic sequences, complex numbers, polynomials, etc., etc (da Cruz and Henriques, 2007b,a, 2006a,b, 2005).

The idea behind the design of LISS language was to create a simplified version of the more usual imperative languages although combining functionalities from various languages.

```
grammar LissGIC;
  /* ***** Program ***** */
  liss: 'program' identifier body
  body : '{'
         'declarations' declarations
10
         'statements'
                         statements
         '}'
12
13
14
    ***** Declarations ***** */
15
16
  declarations : variable_declaration* subprogram_definition*
18
19
    ***** Variables ***** */
20
21
  variable_declaration : vars '->' type ';'
23
24
```

```
25 vars : var (',' var )*
27
28 var : identifier value_var
30
 value_var :
    | '=' inic_var
           ;
33
34
35 type : 'integer'
      | 'boolean'
       set'
37
       | 'sequence'
38
       | 'array' 'size' dimension
39
40
41
42 typeReturnSubProgram : 'integer'
                       'boolean'
43
44
45
46 dimension : number (',' number )*
     ;
47
48
 inic_var : constant
      | array_definition
50
           | set_definition
51
          | sequence_definition
52
53
54
 constant : sign number
      | 'true'
56
          | 'false'
57
58
60 sign :
      | '+'
61
       | '-'
62
63
64
65 /* ***** Array definition ***** */
67 array_definition : '[' array_initialization ']'
```

```
68
69
  array_initialization : elem (',' elem)*
                        ;
71
72
  elem: number
        | array_definition
74
75
76
  /* ***** Sequence definition ***** */
77
  sequence_definition : '<<' sequence_initialization '>>'
80
81
  sequence_initialization :
                              values
83
84
  values : number (',' number )*
         ;
87
  /* ***** Set definition ***** */
89
  set_definition : '{' set_initialization '}'
92
93
  set_initialization :
                       | identifier '|' expression
95
96
97
  /* ***** SubProgram definition ***** */
99
subprogram_definition: 'subprogram' identifier '(' formal_args ')'
      return_type f_body
101
102
  f_body : '{'
103
            'declarations' declarations
104
            'statements' statements
105
            returnSubPrg
106
            '}'
107
108
```

```
/* ***** Formal args ***** */
111
  formal_args :
112
                | f_args
113
114
  f_args : formal_arg (',' formal_arg )*
117
118
  formal_arg : identifier '->' type
119
120
121
  /* ***** Return type ***** */
123
  return_type :
124
                  '->' typeReturnSubProgram
125
126
127
  /* ***** Return ***** */
129
  returnSubPrg:
130
                   'return' expression ';'
131
132
133
  /* ***** Statements ***** */
134
135
  statements : statement*
136
137
138
  statement : assignment ';'
              | write_statement ';'
140
              | read_statement ';'
141
               conditional_statement
142
               iterative_statement
143
              | function_call ';'
144
              | succ_or_pred ';'
145
              copy_statement ';'
146
              cat_statement ';'
147
148
149
  /* ***** Assignment ***** */
150
assignment: designator '=' expression
```

```
153
154
  /* ***** Designator ***** */
155
156
  designator : identifier array_access
157
158
159
  array_access :
160
                 | '[' elem_array ']'
161
162
163
  elem_array : single_expression (',' single_expression )*
165
166
  /* ***** Function call ***** */
167
168
  function_call : identifier '(' sub_prg_args ')'
170
171
sub_prg_args:
                args
173
174
  args : expression (',' expression )*
177
178
  /* ***** Expression ***** */
179
180
  expression : single_expression ( rel_op single_expression )?
181
182
183
  /* ***** Single expression ***** */
184
185
  single_expression : term ( add_op term )*
186
                      ;
187
188
  /* ***** Term ***** */
  term : factor ( mul_op factor )*
190
191
192
193 /* ***** Factor ***** */
195 factor : inic_var
```

```
designator
196
              '(' expression ')'
197
              '!' factor
198
              function_call
199
              specialFunctions
200
201
   specialFunctions: tail
203
                          head
204
                          cons
205
                          member
206
                          is_empty
207
                          length
                          delete
209
210
211
   /* ***** add_op, mul_op, rel_op ***** */
212
213
   add_op : '+'
215
              '||'
216
              '++ '
217
218
219
  mul_op : '*'
221
              '&&'
222
223
224
  rel_op : '=='
              '!='
227
              '<'
228
              '>'
229
              '<='
230
              ′>=′
231
              'in'
232
233
234
      ***** Write statement ***** */
235
236
   write_statement : write_expr '(' print_what ')'
237
238
```

```
239
  write_expr : 'write'
240
               /writeln/
241
242
243
  print_what :
               expression
245
246
247
  /* ***** Read statement ***** */
248
249
  read_statement : 'input' '(' identifier ')'
251
252
  /* ***** Conditional & Iterative ***** */
253
254
  conditional_statement : if_then_else_stat
256
257
  iterative_statement : for_stat
                         while_stat
259
260
261
  /* ***** if_then_else_stat ***** */
262
  if_then_else_stat : 'if' '(' expression ')'
                         'then' '{' statements '}'
265
                         else_expression
266
267
268
  else_expression :
                      'else' '{' statements '}'
270
271
272
  /* ***** for_stat ***** */
273
274
  for_stat : 'for' '(' interval ')' step satisfy
               '{' statements '}'
276
277
278
  interval : identifier type_interval
280
281
```

```
282 type_interval : 'in' range
                | 'inArray' identifier
                 ;
284
285
286 range : minimum '..' maximum
       ;
287
288
289 minimum : number
      identifier
290
         ;
291
292
<sub>293</sub> maximum : number
     identifier
294
         ;
295
296
297 step :
     | up₋down number
301 up_down : 'stepUp'
    | 'stepDown'
302
303
304
305 satisfy:
    'satisfying' expression
307
308
309 /* ***** While_Stat ***** */
while_stat : 'while' '(' expression ')'
       '{' statements '}'
311
312
313
/* ***** Succ_Or_Predd ***** */
315
316 succ_or_pred : succ_pred identifier
318
succ_pred : 'succ'
     | 'pred'
320
             ;
321
323 /* ***** SequenceOper ***** */
324
```

```
325 tail // tail : sequence -> sequence
       : 'tail' '(' expression ')'
327
328
329 head // head : sequence -> integer
       : 'head' '(' expression ')'
331
332
  cons // integer x sequence -> sequence
333
       : 'cons' '(' expression ',' expression ')'
334
335
336
  delete // del : integer x sequence -> sequence
337
         : 'del' '(' expression ',' expression ')'
338
339
340
  copy_statement // copy_statement : seq x seq -> void
                 : 'copy' '(' identifier ',' identifier ')'
342
343
344
  cat_statement //cat_statement : seq x seq -> void
345
                : 'cat' '(' identifier ',' identifier ')'
346
                ;
347
348
  is_empty // is_empty : sequence -> boolean
349
           : 'isEmpty' '(' expression ')'
350
351
352
  length // length : sequence -> integer
         : 'length' '(' expression ')'
354
355
356
  /* ***** set_oper ***** */
357
358
  member // isMember : integer x sequence -> boolean
         : 'isMember' '(' expression ',' identifier ')'
361
362
363
364
365 /*
     */
```

```
366
  string: STR
367
368
       ;
369
<sub>370</sub> number : NBR
        ;
371
372
373 identifier: ID
374
375 /*
     */
376
377
  /* ***** Lexer ***** */
378
379
380 NBR : ('o'..'9')+
381
_{383} ID : ('a'..'z'|'A'..'Z')('a'..'z'|'A'..'Z'|'o'..'9'|'_')* //removi o uso
     do signal '-' conflitos com os valores do signal
384
385
_{386} WS : ( [ \t\r\n] | COMMENT) -> skip
388
  STR : '"' ( ESC_SEQ | ~('"') )* '"'
389
390
391
392
393 fragment
394 COMMENT
      : '/*'.*?'*/' /* multiple comments*/
395
      | '//'^{\sim}(' \ r' \ | ' \ n')* /* single comment*/
396
397
398
399 fragment
  ESC_SEQ
          '\\' ('b'|'t'|'n'|'f'|'r'|'\"'|'\''|'\\')
401
402
```

lissGIC.g4

Auxiliary results which are not main-stream; or

Details of results whose length would compromise readability of main text; or Specifications and Code Listings: should this be the case; or Tooling: Should this be the case.

