Damon Hsieh

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TOEIC:850(2012)

Objective

Reaching for chances (BIOS Engineer) in order to extend my strength and vision based on my x86 BIOS/Storage/Software/Debug specialty to help the company expand their business and also keep on strengthening my specialty.

Summary

- 4.5 years ODM BIOS debugging, initializing/design, and bring-up experience for server projects that help ODM hardware members narrow down and debug the design problems quickly.
- 1.8 year ODM Storage card test automation and debugging for server projects, as a firmware engineer to help EE/SI/SIT functions to clarify issues, and provide suggestions to customer.
- Familiar with Dell project process.
- Familiar with Dell legacy BIOS and Dell UEFI BIOS architecture and working flow.
- Familiar with x86/PC architecture, PCI, PCI Express, SMbus, LPC/FWH, ISA, and SPI, and corresponding bus controllers BIOS initiation flow and driver implementation.
- Familiar with version control system.

Experience & Accomplishment

- **Senior BIOS/FW Engineer, FOXCONN.**   **Oct, 2007**– Present(**6.3+ years**)
    - Dell ODM server BIOS feature implementation
        - Implementing AHCI/RAID/ATA feature for Dell BIOS.
        - Solve and provide solution for defects found from SIT team.
    - BIOS project version control system.
        - Maintain and control source code, coding style and access right in the developers.
        - Establish mail automation delivery system for version control system.
        - Establish internal document system (e.q. wiki media)
    - Successfully helping the bring up of two server BIOS projects and also successfully assist the overall bugs handling no matter software, manufacturing, or hardware level to make the server project on schedule.
    - Dell ODM server Storage card testing and automation implementation
        - Cowork with Dell American Design Center for FW issue verification
        - Establish internal document system (e.q. Experience documents in html format)

Good at Protocol Specifications

- PCI, ACPI, SMBus, LPC, SPI, FWH, PCI-e, x86 64/32 Architecture, BBS, and SMBIOS
- NVMHCI, AHCI.

S/W Skill

- Assembly, C/C++, Perl, Elisp.
- DELL BIOS, Intel UEFI.
- Familiar w/ x86 architecture (CPU/chipset) initialization flow.
- Familiar w/ ACPI Power Management architecture.
- Legacy and UEFI BIOS architecture.

Education

- Master of Computer Science and Information Engineering.
    - National Chung Cheng University, R.O.C.
- Bachelor of Computer Science and information Engineering.
    - National Chung Cheng University, R.O.C.
- Master of Business Administration (2013 – Present)
    - Management School of Professional and Continuing Studies, National Taiwan University

Interest

Reading, Road Running, Photographing.