

# Chendi Li

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PhD student | AI+System

Personal website:  
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I am currently a PhD student at the University of Utah, and supervised by Prof. **P. (Saday) Sadayappan**. I received my master's degree from the State Key Laboratory of Computer Architecture, Institute of Computing Technology, Chinese Academy of Sciences, and supervised by Prof. **Yunquan Zhang**. My research primarily focuses on software system design for accelerators.

## RESEARCH INTERESTS

- High-Performance Computing
- High-Performance Machine learning
- Optimizing Tensor Computations

## EDUCATION

PhD student in Computer Science, University of Utah	Sep 2022 — expected 2027
Master in Computer Science, University of Chinese Academy of Sciences	Sep 2019 — July 2022
Bachelor in Computer Science, Hunan Agricultural University	Sep 2014 — June 2018

## RESEARCH EXPERIENCES

Graduate Student Research Assistant University of Utah	Sep 2022 — Present
Graduate Student Research Assistant State Key Laboratory of Computer Architecture, Institute of Computing Technology, Chinese Academy of Sciences	Sep 2019 — July 2022
Undergraduate Research Assistant State Key Laboratory of Computer Architecture, Institute of Computing Technology, Chinese Academy of Sciences	Jan 2018 — June 2019

## PUBLICATIONS

- [ICS 2024] Chendi Li, Yufan Xu, Sina Mahdipour Saravani, Saday Sadayappan, Accelerated Auto-Tuning of GPU Kernels for Tensor Computations
- [TPDS 2024] Cunyang Wei, Haipeng Jia, Yunquan Zhang, Jianyu Yao, Chendi Li, Wenxuan Cao. IrGEMM: An Input-Aware Tuning Framework for Irregular GEMM on ARM and X86 CPUs
- [ICS 2023] T Chen, H Jia, Y Zhang, K Li, Z Li, X Zhao, J Yao, Chendi Li, et al. OpenFFT: An Adaptive Tuning Framework for 3D FFT on ARM Multicore CPUs
- [ISPA 2021] Chendi Li, Haipeng Jia, Hang Cao, et al. AutoTSMM: An Auto-tuning Framework for Building High-Performance Tall-and-Skinny Matrix-Matrix Multiplication on CPUs
- [ICPADS 2021] Jianyu Yao, Boqian Shi, Chunyang Xiang, Haipeng Jia, Chendi Li, et al. IAAT: An Input-Aware Adaptive Tuning framework for Small GEMM
- [HPCC 2021] Tun Chen, Haipeng Jia, Zhihao Li, Chendi Li, et al. A Transpose-free Three-dimensional FFT Algorithm on ARM CPUs
- [HPC China 2020] Chendi Li, Guangting Zhang, Haipeng Jia. Fast Computation of Elementary Functions on ARM Platforms

## RESEARCH PROJECTS

Apache TVM, Contributor	Feb 2023 — Feb 2024
• Designed and developed the dynamic gradient algorithm in TVM. Experimental evaluation on a number of matrix-matrix multiplication and 2D convolution kernels demonstrates an order-of-magnitude improvement in auto-tuning time to achieve the same level of code performance. The paper was published in ICS 2024.	

**OpenBLAS, Contributor****Nov 2020 — June 2022**

- Developed and optimized pre-packed matrix-matrix multiplication and triangular solve with multiple right-hand sides (TRSM) for ARMv8 and x86 platforms. OpenBLAS is one of the most widely used open-source BLAS libraries.

**AutoTSM, Author****Nov 2020 — June 2022**

- Designed and developed AutoTSM, which is used to build high-performance tall-and-skinny matrix multiplication on mainstream CPUs. AutoTSM is competitive with Intel OneMKL and outperforms all conventional GEMM implementations on ARMv8 platform. This work was published in IEEE ISPA 2021 and it was published in TPDS'24 as a part of IrGEMM framework.

**IAAT, Contributor****Nov 2020 — Feb 2022**

- Launched the project and investigated JIT tools for small GEMM. IAAT is a template-driven, just-in-time small GEMM framework targeting CPUs. This work was accepted by IEEE ICPADS 2021.

**AutoFFT and OpenFFT, Contributor****Jan 2018 — Feb 2021**

- Optimized small-scale FFT, and contributed to multi-threading and 2D-FFT. AutoFFT and OpenFFT is a template-based FFT codes auto-generation framework that contributes to many Chinese vendors' libraries. This work was published in SC'19, TPDS'20, HPCC'21, and ICS'23.

**OpenVML, Contributor****Jan 2020 — Oct 2020**

- Enhancing the mathematical functions by manipulating IEEE 754 floating-point representations, OpenVML, a high-performance vector math library, demonstrated significant performance improvements over both the C standard library and ARMPL. This work was accepted and presented at HPC China 2020.

**AWARDS & HONORS**

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<b>2022</b>	Ph.D. Student Fellowship
<b>2021</b>	First-class scholarships
<b>2020</b>	Second-class scholarship
<b>2019</b>	Third-class scholarship, Outstanding intern in PerfXLab
<b>2015</b>	Collegiate programming contest first prize; Outstanding volunteer

**TECHNICAL SKILLS**

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<b>Tools</b>	Linux, Git, NVIDIA Nsight, Vim, GDB, CMake, OpenMP, Pthreads
<b>Programming/Scripting</b>	Python, C, C++, X86 and ARM Assembly, X86 and NEON intrinsic, CUDA, Latex