## ECE 383 FINAL PROJECT

by Dananga Agalakotuwa

I BUILT AN EMBEDDED SYSTEM WHICH ENGAGES MINDS AND CRITICAL THINKING THROUGH A GAME OF SINGLE PLAYER BLACKJACK.

# PROBLEM STATEMENT/ MOTIVATION

#### **Project Requirement**

"The final project in this class will be the design and implementation of an engineering prototype to address some need"





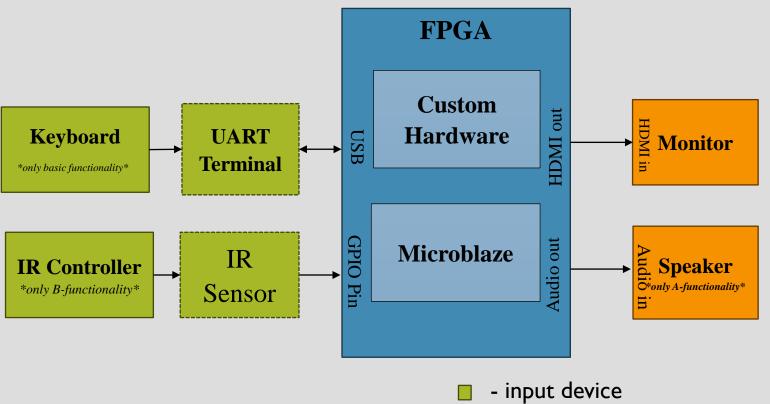


Controlled by an IR controller.

## NEED AND TECHNICAL OBJECTIVE

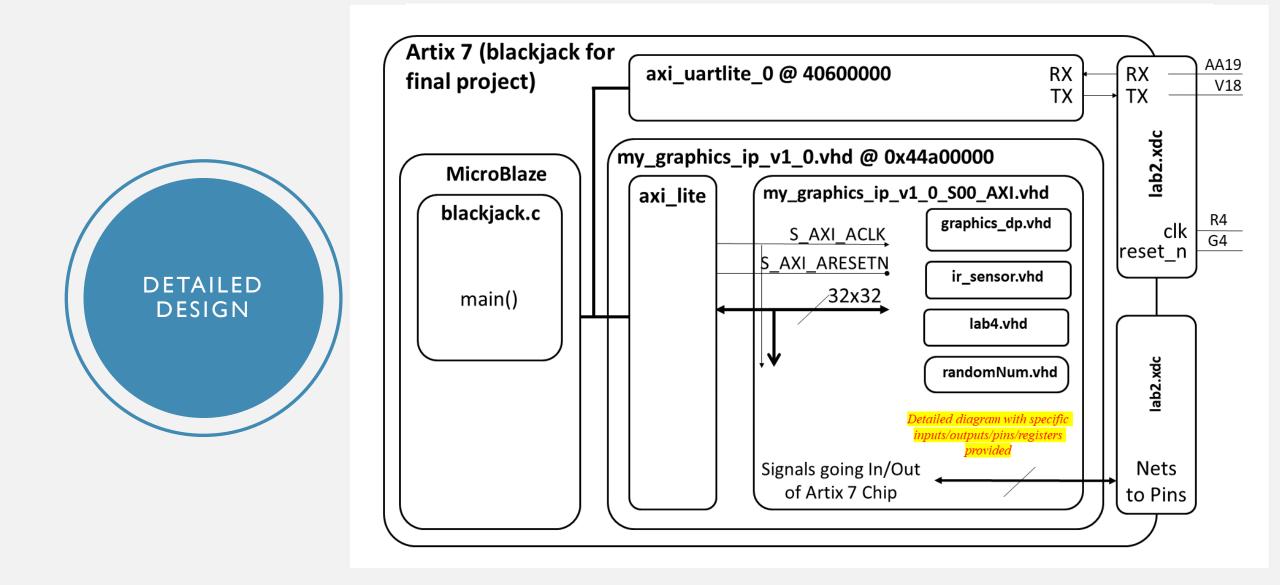
- Provide a platform for cadets to practice strategic thinking, safe-betting, and expand intelligence (counting cards).
- Encompass all material learnt in ECE 383
   and apply it to a single project showcasing
   ability to solve problems through the
   engineering process.
- Develop as a young engineer.

#### HIGH LEVEL ARCHITECTURE

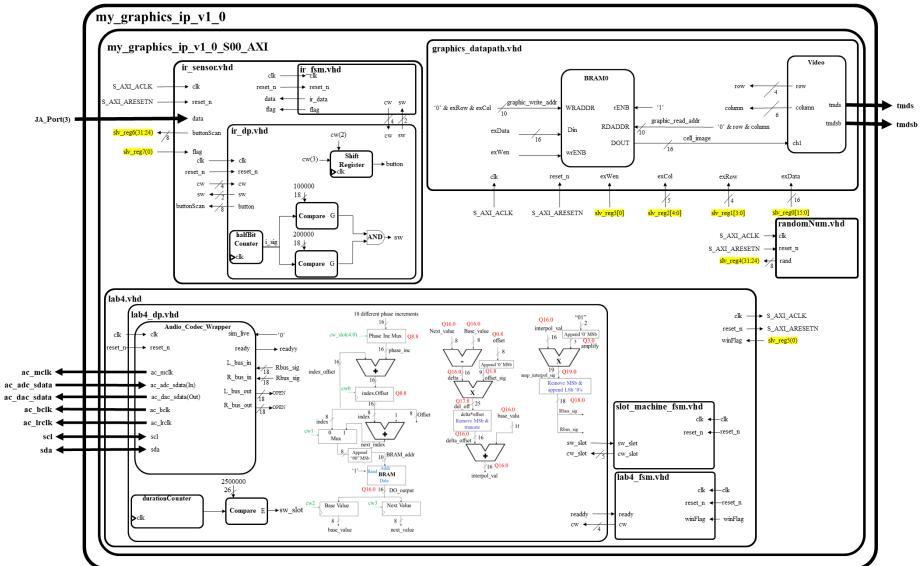


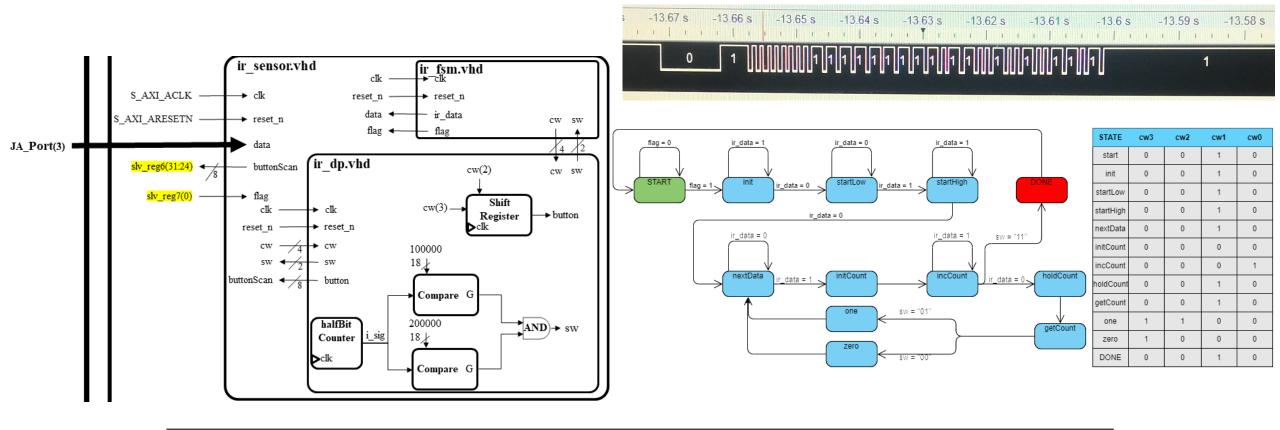
Outside Artix / (lab2.xdc)		
Signal	Туре	Package Pin
clk	clock	R4
reset	button	G4
ac_mclk	Audio Codec	U6
ac_adc_sda ta	Audio Codec	T4
ac_dac_sda ta	Audio Codec	W6
ac_bclk	Audio Codec	T5
ac_lrclk	Audio Codec	U5
sda	QSPI	V5
scl	QSPI	W5
tmds[3:0]	HDMI out	TI AB3 AA1 WI
tmdsb[3:0]	HDMI out	UI AB2 ABI YI
data	Pmod header JA	AB18

- Nexys Video FPGA board
- output device

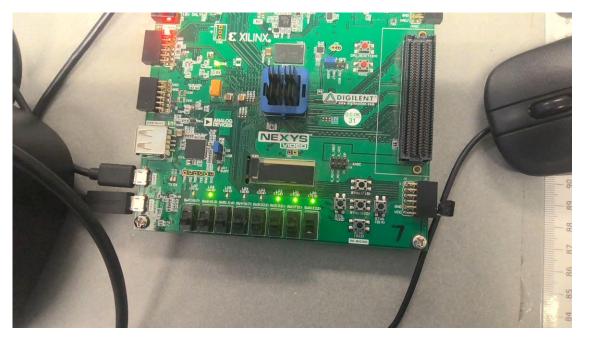




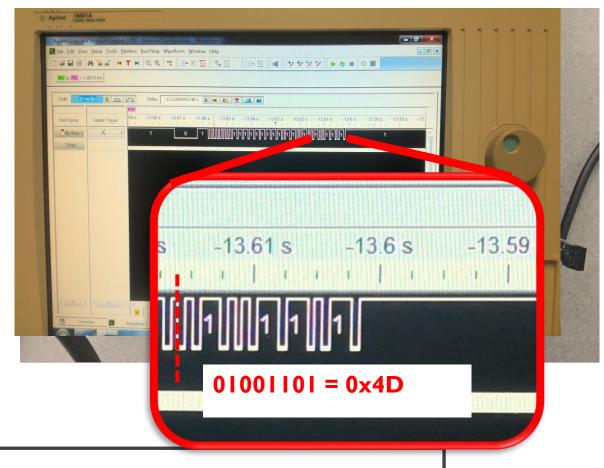




#### IR DESIGN





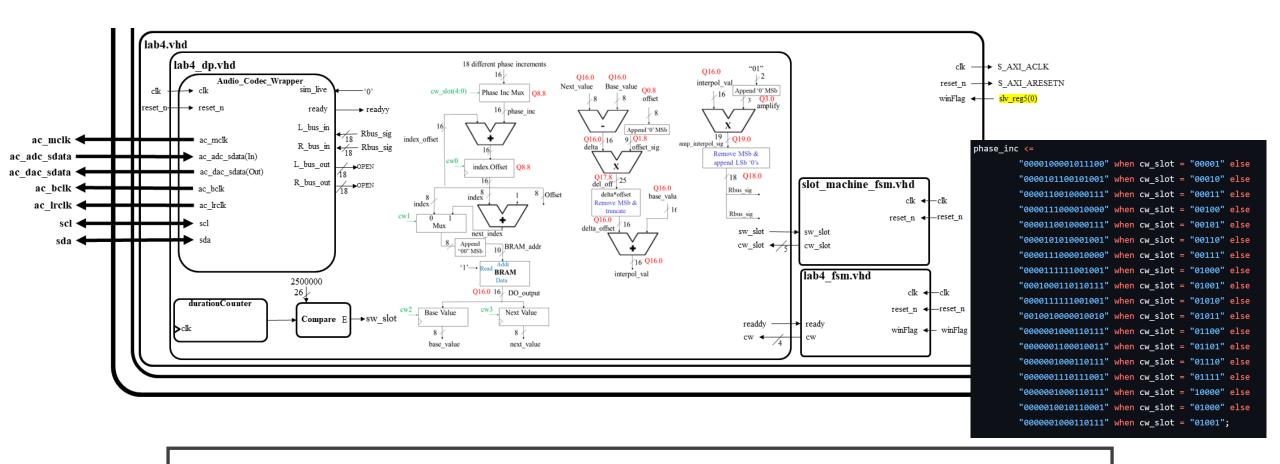


### IR TESTING

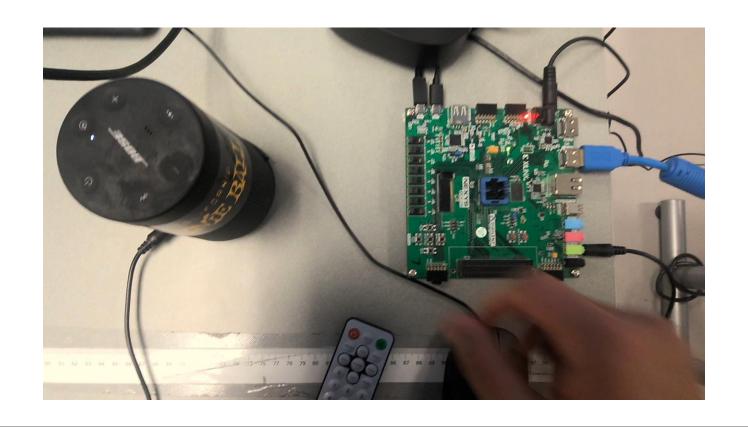
Made a separate Vivado project to test

Hooked up buttonScan to LEDs

Hooked up flag to a switch



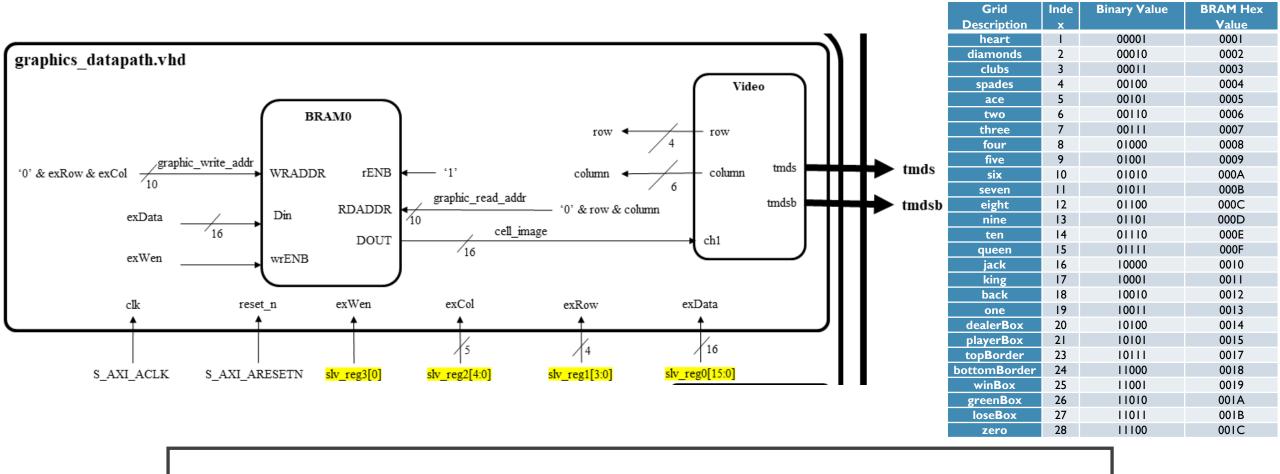
#### SOUND DESIGN



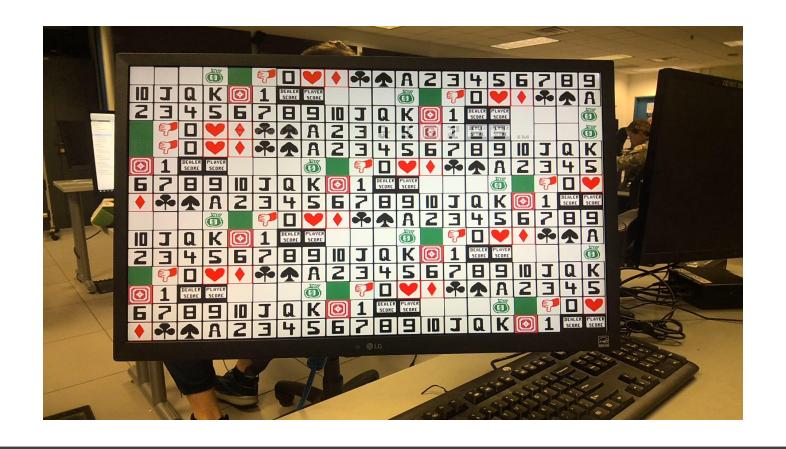
### SOUND TESTING

Made a separate Vivado project to test

Hooked up flag to a switch



#### **GRAPHICS DESIGN**

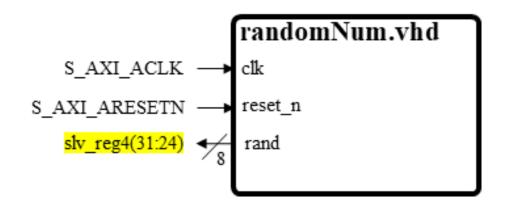


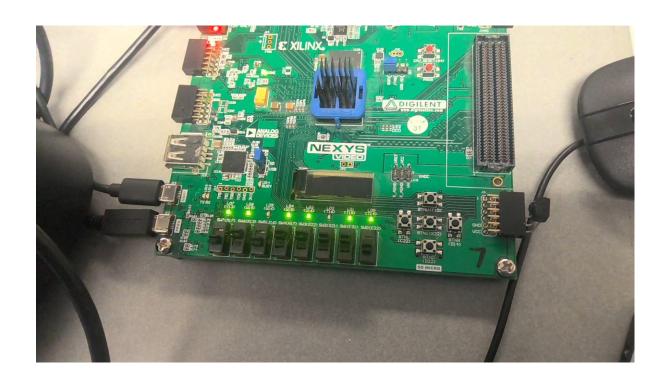
### **GRAPHICS TESTING**

Made a separate Vivado project to test

Edited the FSM given to us in grid memory hints

Saved a lot of time





## RANDOM NUMBER GENERATOR DESIGN AND TESTING

Made a separate Vivado project to test

Hooked up 8-bit random number to LEDs to see if they change

### VIDEO DEMO



