

## Dan Pond's Conference Schedule

	Location	Event
<b>Sunday, April 27, 2014</b>		
04:00 PM - 07:00 PM	MONTEREY TERRACE	Conference Check-In
04:00 PM - 09:00 PM	HOTEL LOBBY	Hotel Check-in
06:30 PM - 10:00 PM	MONTEREY BALLROOM	Welcome Reception
<b>Monday, April 28, 2014</b>		
06:30 AM - 08:00 AM	MONTEREY BALLROOM	Breakfast
07:00 AM - 08:00 AM	REGENCY FOYER	Late Conference Check-In
08:00 AM - 08:30 AM	REGENCY MAIN BALLROOM	Conference Welcome, <i>Zain Saidin, Executive Vice President and Chief Engineer</i>
08:00 AM - 12:00 AM	REGENCY MAIN BALLROOM	Timing Cards: Executive Presentations
08:30 AM - 08:45 AM	REGENCY MAIN BALLROOM	Why We Exist. What We Do. How We Do It., <i>Rick Wallace, President and Chief Executive Officer</i>
08:45 AM - 09:15 AM	REGENCY MAIN BALLROOM	The Semiconductor Fab Ecosystem. What Our Customers are Facing and How We Need to Adapt., <i>Bobby Bell, Executive Vice President, Wafer Inspection Group</i>
09:15 AM - 10:15 AM	REGENCY MAIN BALLROOM	Google Glass: A Path from Opaque to Clarity, <i>Simon Prakash, Sr. Director, Google[x]</i>
10:15 AM - 10:30 AM	REGENCY FOYER	Break
10:30 AM - 11:00 AM	REGENCY MAIN BALLROOM	How We Make the Best Products. Engineering Culture & Folklore., <i>Zain Saidin, Executive Vice President and Chief Engineer</i>
11:00 AM - 12:00 PM	REGENCY MAIN BALLROOM	Computational Photography, <i>Shree Nayar, T. C. Chang Professor of Computer Science at Columbia University</i>
12:00 PM - 01:00 PM	REGENCY TERRACE	Lunch
03:00 PM - 04:00 PM	REGENCY GRAND IV-VI	Poster Session
04:00 PM - 05:00 PM	CYPRESS	Speaker Support: White Paper Room Staff
05:00 PM - 06:00 PM	REGENCY FOYER	Happy Hour
05:00 PM - 06:00 PM	REGENCY GRAND IV-VI	Poster Session
06:30 PM - 08:00 PM	REGENCY MAIN BALLROOM	Dinner

# 2014 Engineering Conference

Accelerating Innovation



	Location	Event
<b>Tuesday, April 29, 2014</b>		
06:30 AM - 08:00 AM	MONTEREY BALLROOM	Breakfast
08:00 AM - 08:15 AM	REGENCY MAIN BALLROOM	Opening Remarks, <i>Zain Saidin, Executive Vice President and Chief Engineer</i>
08:00 AM - 12:00 AM	REGENCY MAIN BALLROOM	Timing Cards: Executive Presentation
08:15 AM - 08:45 AM	REGENCY MAIN BALLROOM	Effective Global Engineering, Manufacturing, and New Product Introduction, <i>Mike Kirk, Executive Vice President, Global Customer and Operations Group</i>
08:45 AM - 09:15 AM	REGENCY MAIN BALLROOM	The Transistor Over the Next 5-10 years. What it Means for Engineering at K-T, <i>Ahmad Khan, Group Vice President, Pattern Solutions Group</i>
09:15 AM - 10:15 AM	REGENCY MAIN BALLROOM	Periodic Pattern Perambulations: More Moore and More than Moore, <i>Steve Brueck, Distinguished Professor of Electrical and Computer Engineering and Physics, University of New Mexico</i>
10:15 AM - 10:30 AM	REGENCY FOYER	Break
10:30 AM - 11:00 AM	REGENCY MAIN BALLROOM	Technology at K-T. Some Great Things Going On Around the Company, <i>Ben Tsai, Executive Vice President of Corporate Alliances and Chief Technology Officer</i>
11:00 AM - 12:00 PM	REGENCY MAIN BALLROOM	The Future of Memory, <i>Gary Bronner, Vice President, Rambus Labs</i>
12:00 PM - 01:00 PM	REGENCY TERRACE	Lunch
01:00 PM - 02:30 PM	CYPRESS	Speaker Support: White Paper Room Staff
02:30 PM - 03:00 PM	REGENCY GRAND IV-VI	Poster Session
03:00 PM - 04:30 PM	CYPRESS	Speaker Support: White Paper Room Staff
04:30 PM - 05:00 PM	REGENCY MAIN BALLROOM	Closing Remarks & Speaker Recognition, <i>Zain Saidin, Executive Vice President and Chief Engineer</i>

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For more info contact:

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