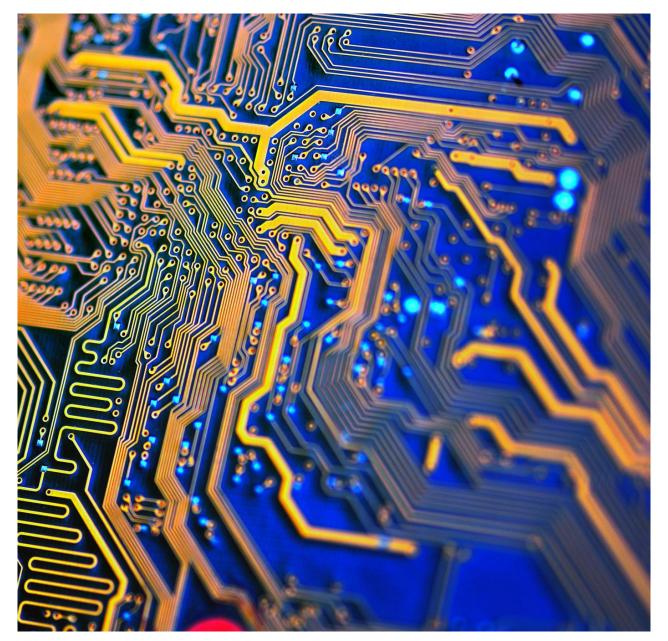
### Systemnahe Programmierung Hardware

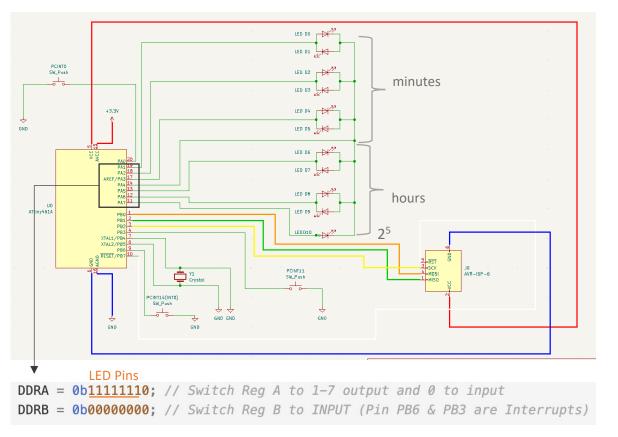
Binäruhr HTWK Leipzig



#### Global variables

```
// Global variables for setting Timer Hours, Minutes and Seconds
volatile uint8_t hours = 3;
volatile uint8_t minutes = 16;
volatile uint8_t seconds = 0;
volatile uint8_t ledSeq[11] = {0};
// Global variable for debouncing button and sleep modus
volatile int bouncer = 0;
volatile int sleep = 0;
      sleep++ in ISR(timer)
                                  bouncer++ in main()
```

### **PIN Configuration**



```
// constructing led pairs (LED to be shown, Pin-config for multiplexing)
 struct pair
    uint16 t bin;
    uint8_t ledPins;
                                                                    PCINT0
                                                                    HIGH
   initialize pair array
                                                    A 765 4 321
const struct pair ledPairs[] =
                                                                           A7 == HIGH
           bin
                       ledPins
                                                  0b 100 0 000 1
    {0b000000000001, 0b00000011},
                                     1/D0
                                                                           = D10 High
    {0b000000000010, 0b01111101},
                                    //D1
    {0b00000000100, 0b00000101},
                                    //D2
                                                        hours minutes
    {0b00000001000, 0b01111011},
                                    //D3
    {0b00000010000, 0b00001001},
                                    //D4
    {0b00000100000, 0b01110111},
                                    //D5
                                                       25 .... 20 26 ......20
    {0b00001000000, 0b00100001},
                                    1/D6
    {0b00010000000, 0b01011111},
                                    //D7
                                                                             25 Uhr
                                                  0b 10000 000000
    {0b00100000000, 0b01000001},
                                    //D8
                                                                             = 16 Uhr
    {0b01000000000, 0b00111111},
                                    //D9
    {0b10000000000000
                    0b10000001
                                    //D10
                                                         hours
                                                                  minutes
};
```

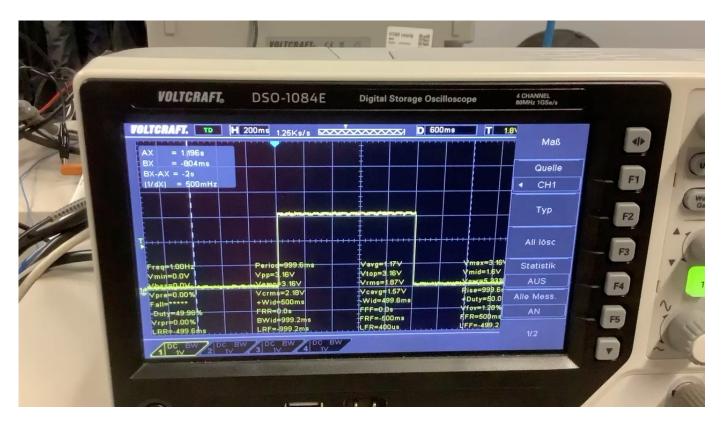
#### Timer

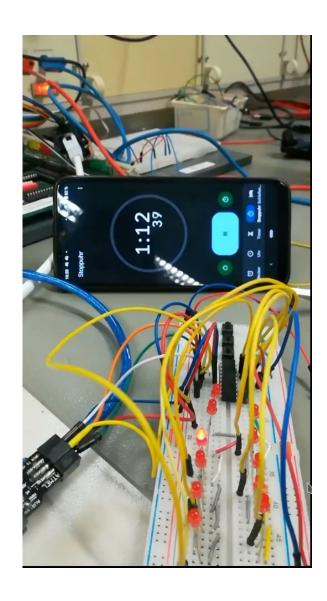
```
void setup()
                                                                                                                             TCCR1B - Timer/Counter1 Control Register B
    DDRA = 0b11111110; // Switch Reg A to 1-7 output and 0 to input
                                                                                                                                                         PSR1 DTPS11 DTPS10
                                                                                                                                                                           CS13
                                                                                                                                                                                 CS12
                                                                                                                                                                                        CS11
                                                                                                                                                                                              CS10
                                                                                                                                                                                                   TCCR1B
    DDRB = 0b00000000; // Switch Reg B to INPUT (Pin PB6 & PB3 are Interrupts)
    TCCR1B \mid = (1 << CS13); //set Timer1(Time) auf f/128 bei f= 2^15 overflow nach 1s
    TIMSK |= (1 << TOIE1); // set interrupts for Timer1
                                                                                                                                                               sets Timer/Counter1 of Control Register B
                                                                                                 Timer/Counter 1
   //Pin Change Interrupts - enable PCINTO, PCINT11 und PCINT14
                                                                                                                                                               Prescaler to CK/128
    //Pull up Widerstände setzen
                                                                                                  Overflow enable
                                                                                                                                                               Timer1 - 8Bit - 28 = 256
    PORTA |= (1 << PORTA0);
                                                                                                  ISR(overflow)
    PORTB |= (1 << PORTB6) | (1 << PORTB3);
                                                                                                                                                               Ex. Quarz = 2^{15} Hz
    GIMSK |= (1 << PCIE1) | (1 << PCIE0); //enable General Interrupt Mask Register for PCINT[7:0] or PCINT[15:12]
                                                                                                                                                   \frac{Prescaler * TimerWidth}{Finguras frequence} = \frac{128 * 2^8}{2^{15}} = 1
    PCMSK0 |= tastHours; //enable PCINTO on PORT A PIN AO
                                                                                                                                      Periode = -
    PCMSK1 |= tastMinSleep; //enable PCINT14 und PCINT11 on PORTB PIN B6
                                                                                                                                                      Eingangsfrequenz
    sei(); // enable global interrupts same as SREG |= 0x80;
    /*
                                                                                                                            ISR(TIMER1_0VF_vect)
        remains enabled: timers, pins pullups, interrupts, Clock, OSC
        disables: CLK_cpu, CLK_flash aka no new instructions are pulled prom flash nor are they processed
                                                                                                                               seconds++;
    set_sleep_mode(SLEEP_MODE_IDLE);
                                                                                                                               timeSetter();
                                                                                                                               sleep++;
   //inits LEDs according to initiated global minutes and hours
    updateSeq();
```

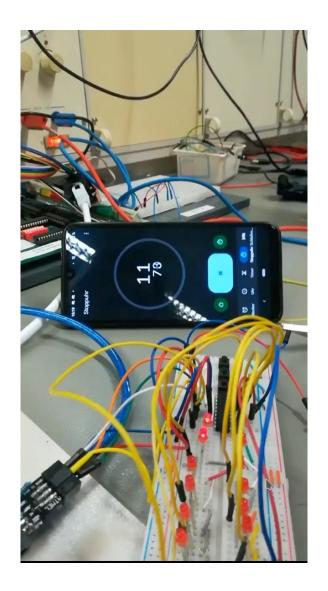
# Timer Accuracy

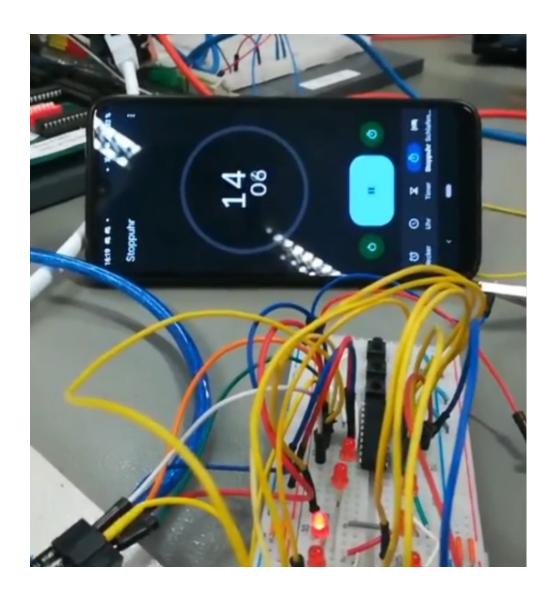
Via Oscilloscope

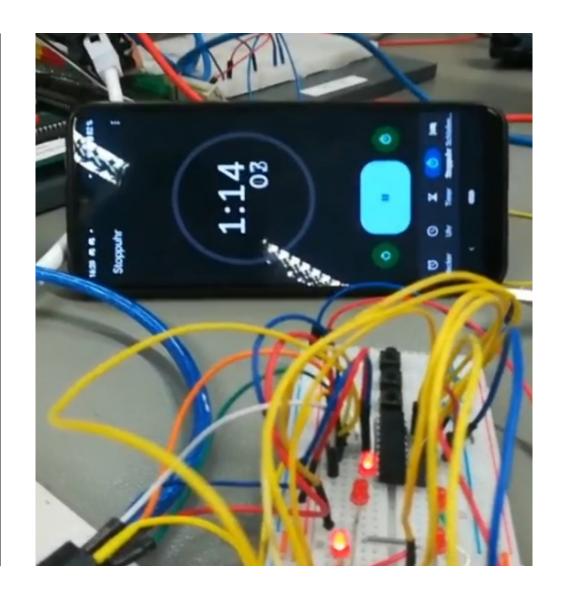












### Set Time

Change global variables

```
* when seconds are == 60, update LEDSeq && increment hours, minutes
* when minutes == 60 & hours == 24, reset
void timeSetter()
    if (seconds == 60){
       seconds = 0;
       minutes++;
       updateSeq();
    if(minutes == 60) {
       minutes = 0;
       hours++;
    if(hours >= 24) {
       hours = 0;
```

### Switches & SleepMode

```
void setup()
                                                                                                                                                                               PDIP/SOIC/TSSOP
    DDRA = 0b11111110; // Switch Reg A to 1-7 output and 0 to input
                                                                                                                                                         (MOSI/DI/SDA/OC1A/PCINT8) PB0 1
                                                                                                                                                                                        20 PA0 (ADC0/DI/SDA/PCINT0)
    DDRB = 0b00000000; // Switch Reg B to INPUT (Pin PB6 & PB3 are Interrupts)
                                                                                                                                                                                        19 PA1 (ADC1/DO/PCINT1)
                                                                                                                                                            (MISO/DO/OC1A/PCINT9) PB1 2
                                                                                                                                                       (SCK/USCK/SCL/OC1B/PCINT10) PB2 3
                                                                                                                                                                                        18 PA2 (ADC2/INT1/USCK/SCL/PCINT2)
                                                                                                                                                                                        17 PA3 (AREF/PCINT3)
                                                                                                                                                                 (OC1B/PCINT11) PB3 4
    TCCR1B \mid= (1 << CS13); //set Timer1(Time) auf f/128 bei f= 2^15 overflow nach 1s
                                                                                                                                                                           VCC 🖂 5
                                                                                                                                                                                        16 AGND
    TIMSK |= (1 << TOIE1); // set interrupts for Timer1
                                                                                                                                                                                        15 AVCC
                                                                                                                                                     (ADC7/OC1D/CLKI/XTAL1/PCINT12) PB4 7
                                                                                                                                                                                        14 PA4 (ADC3/ICP0/PCINT4)
                                                                                                                                                     (ADC8/OC1D/CLKO/XTAL2/PCINT13) PB5 = 8
                                                                                                                                                                                        13 PA5 (ADC4/AIN2/PCINT5)
    //Pin Change Interrupts - enable PCINT0, PCINT11 und PCINT14
                                                                                                                                                            (ADC9/INT0/T0/PCINT14) PB6 = 9
                                                                                                                                                                                        12 PA6 (ADC5/AIN0/PCINT6)
                                                                                                                                                           (ADC10/RESET/PCINT15) PB7 10
                                                                                                                                                                                        11 PA7 (ADC6/AIN1/PCINT7)
    //Pull up Widerstände setzen
    PORTA |= (1 << PORTA0);
                                                                                                                                                     Pin Change Interrupt Enable
    PORTB |= (1 << PORTB6) | (1 << PORTB3);
                                                                                                                                                     PCIE1: PCINT[0:7] & PCINT[15:12]
    GIMSK |= (1 << PCIE1) | (1<< PCIE0); //enable General Interrupt Mask Register for PCINT[7:0] or PCINT[15:12]
                                                                                                                                                     PCIEO: PCINT[11:8]
    PCMSK0 |= tastHours; //enable PCINTO on PORT A PIN AO
    PCMSK1 |= tastMinSleep; //enable PCINT14 und PCINT11 on PORTB PIN B6
                                                                                                                                                     Pin Change Mask Register
                                                                                                                                                     Enable PC Int on corresponding I/O Pin
    sei(); // enable global interrupts same as SREG |= 0x80;
                                                                                                                                                     #define tastHours 0b00000001;
                                                                                                                                                     #define tastMinSleep 0b01001000;
        remains enabled: timers, pins pullups, interrupts, Clock, OSC
        disables: CLK_cpu, CLK_flash aka no new instructions are pulled prom flash nor are they processed
    set sleep mode(SLEEP MODE IDLE);
    //inits LEDs according to initiated global minutes and hours
    updateSeq();
```

## Power Management

### Leistung

#### Ausgehend von einer 3V Batterie

Stromstärke I - LED AN (mA)	Stromstärke I – SLEEP (mA)
21,87	14,07
21,9	14,10
21,88	14,11
21,22	14,02
Ø 21,51	Ø 14,05

#### **NORMALBETRIEB**

$$P = 3V * 21,31 * 10^{-3} A = 0,06453 W$$

#### <u>IDLE</u>

$$P = 3V * 14,05 * 10^{-3} = 0,04212 W$$

#### Leistungsersparnis

$$100\% - \frac{0,04212}{0,06453} * 100 = 34,728\%$$

# Interrupts

Pin Change Interrupt for Switches

```
ISR(PCINT_vect)
   //Debouncing with bouncer variable (bouncer variable incremented in main)
   // determines if button Pin has PINCHANGE to LOW, when true execute code (increment hours, minutes...)
   //PinAO Interrupt - increment hours, reset bouncer and update(led)Seq
   if (!(PINA & (1<<PINA0))) {
       if(bouncer >= 10) {
          hours++;
          bouncer = 0;
           updateSeq();
   //PinB6 Interrupt - increment minutes, reset bouncer and update(led)Seq
   if (!(PINB & (1<<PINB6))) {</pre>
       if(bouncer >= 10) {
          minutes++;
          bouncer = 0;
           updateSeq();
   //PinB3 Interrupt - Wake, sleep reset - sleepmode activated when sleep >= 25 secs
   if (!(PINB & (1<<PINB3))) {</pre>
       if(bouncer >= 10) {
          sleep = 0;
           bouncer = 0;
```

# Update LED Sequence

Adding LED Pin Configurations for showing time to Array

```
* @brief gets the time and inputs it into an array
void updateSeq()
   // set first addr through last adrr of ledSeg to zero
   memset(ledSeq, 0, sizeof ledSeq);
   // disrespect actual time, in case actual time is 00:00
   uint16 t time = 0b0;
    /**
     * set bits 0 - 5 as actual minutes
     * set bits 6 - 10 as actual hours
   time = (hours << 6) ^ minutes;</pre>
   hours = 3 = (0b0000 0011 << 6) = 0b 0000 0000 1100 0000
   minutes = 16=
                       XOR
                                            0001 0000
                                0b 0000 0000 1101 0000
   time =
```

```
// fill array, if time is other than 00:00
if(time > 0b0)
   // special index for ledSeq to leave no cell blank
   int arrayInd = 0;
   // iterate 11 times over ledSeg
   for (int i = 0; i < 11; i++)
       * 0010 (1 will be shifted at each digit position iteration)
       *&1010 (00:10)
       * 0010 -> corresponding pin-config will be inserted into ledSeg
       * if result is 0b0, arrayIndex will not be incremented and no pin-config will be inserted
       //if 010(pattern) & 110(current) = 1, then add ledPins of struct ledPairs to ledSea
       if ((ledPairs[i].bin & time) != 0b0)
           ledSeg[arrayInd] = ledPairs[i].ledPins;
           arrayInd++;
                                        time = 0b0000 000011010000
                                             & {0b00000001000, 0b01111011},
                                                                                        //D3
                                                         bin
                                                                        ledPins
```

### Main

### Sleepmodus & Pseudo-PWM

```
int main()
    setup();
   while(true)
        if(sleep >= 25) {
            bouncer = 10;
            PORTA = 0 \times 01;
            sleep_mode();
        else {
            bouncer++;
            int i = 0;
            while(ledSeg[i] != 0b0)
                PORTA = ledSeq[i];
                i++;
            i = 0;
```

```
* 1. if: check for Sleep
* check if sleep is >= 25 seconds
* set bouncer to 10 to enable wake button
* set all ledPins to zero, so last array value is not on when going to sleep
* put controller to sleep
* 2. else: show Time Part (Pseudo PWM)
* increment bouncer every time main is executed
* go through ledSeq Array which is set in updateSeq() until index is not zero
* show ledSeg on PORTA, when array at index ... is zero, reset index, 2. while condition false
```

# Quellen

### ATtiny461A

https://ww1.microchip.com/downloads/en/DeviceDoc/doc8197.pdf