Thread Scheduler Efficiency Improvements for Multicore Systems

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Introduction

- Thread scheduler is an important system component that manages the processing programs receive in a given time
- Always running, so it must be efficient
- Most computers before 2001 were equipped with one processor containing one core
- At the end of the single-processor single-core era (early 2000s) thread scheduling was largely considered a solved problem by the Linux community

"...not very many things that have aged as well as the scheduler. Which is just another proof that scheduling is easy."

Linus, Torvals, 2001 [2]

Introduction

Hardware changed rapidly throughout the 2000s and those developments made thread scheduler implementation much more complex

One of these changes was the development of multicore systems

Introduction

- The computer processor is responsible for executing compiled code.
- A single-core processor has one processing unit, while a multi-core processor has many processing units.
- A processing unit contains registers which can be seen as a snapshot of the current state of a running program on that processing unit
- A processor with multiple cores allows it perform tasks concurrently on each core



Registers on a Single-core CPU



Ouad-core CPU

Outline

Concepts

Thread Scheduling on Linux

Bug fixes and two new schedulers

Conclusions

Outline

Concepts
Threads
Synchronicity and Locks

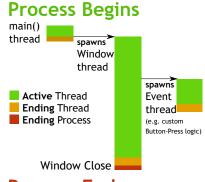
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Using Threads

- Threads allow a program to run multiple independent tasks at the same time
- Useful for programs:
 - with long, mostly-independent computations
 - with a graphical interface



Process Ends

Figure: Example GUI Program.

Three threads are created within one process

Using Threads

- A multithreaded program is a program that employs threads
- Concurrent computing techniques are techniques that allow many tasks to occur at the same time [W]
- Parallel computing techniques are techniques that allow many calculations to occur at the same time [W]
- Problems can be solved or improved using neither, either, or both of these techniques at once

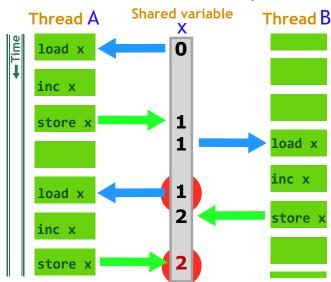
Race Conditions

One problem multithreaded programs face are called *Race Conditions*

Defined in Saltzer and Kaashoek as "A timing-dependent error in thread coordination that may result in threads computing incorrect results"

Let's see an example where two threads increment a shared variable

Race Condition Example

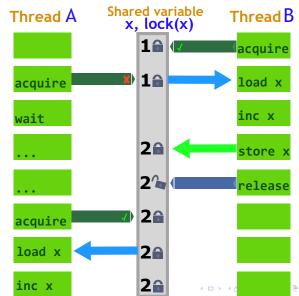


Synchronicity and Locks

- Race conditions can be fixed by controlling access to shared data.
- This control is achieved by employing locks
- Locks secure objects or data shared between threads such that only one thread can read and write to it at one time
- When a thread locks a lock, that thread acquires the lock
- When a thread unlocks a lock, that thread releases the lock

Now, let's fix the race condition in the previous example using locks

Lock Example



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Outline

Concepts

Thread Scheduling on Linux Completely Fair Scheduler Thread State and Cache

Bug fixes and two new schedulers

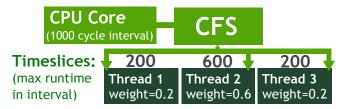
Conclusions

Completely Fair Scheduler (CFS)

- Default Linux thread scheduler (there are others)
- Handles which threads are executed at what times on this core
- Spend a fair amount of runtime on all threads

Completely Fair Scheduler (CFS)

- Like any program, runs on one core
- Makes sure all threads run at least once within an arbitrary interval of CPU cycles
- Distribute timeslices (max CPU cycles) among threads
- Threads with higher priority (weights) get larger timeslices¹
- Monitors the number of cycles that the running thread receives and switches it out when it exceeds its timeslice



¹Priority (PR) and niceness (NI) values are responsible for determining process priority on Linux. We won't get into that here.

Runqueues

- The data structure within the CFS that contains threads is called a runqueue
- A runqueue is a priority queue that sorts for threads that have received the least cycles in the current interval
- When a thread reaches its maximum time, the first thread in the runqueue is chosen to replace

Runqueues on Multiple Cores

If each core needs work to do, how are threads distributed?

Before we can answer this question, we need to know some about switching threads, cache and processor state

Context Switching

- The scheduler switches active threads on cores by saving and restoring thread and processor state information.
- These switches are called context switches
- Scheduler performance

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Process and Thread State

Process State

Consists of resources that each of the processor's threads should have access to

- Compiled code, Data
- Handles (references) for files and sockets
- Process control block (Important logistical information)

Thread State

Scheduler uses this information to pause and resume a thread's execution

- Run-time stack
- Copy of core's registers from when the thread was last active

Important note: Process state contains much more data



Cache

- Cache is the fastest form of memory
- Memory and cache exists in a hierarchy
- How cache is arranged depends on the machine:
 - A cache can exist on, built-in to, or outside a processor
 - Cache can be one per n processors or one per n cores

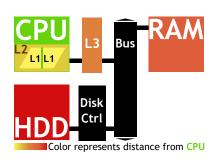


Figure: Distance of various forms of memory from CPU

Cache

- Cache is the fastest form of memory
- Locality: Speed of memory read and writes decrease as distance from CPU increases
- Cache coherence: Any changes to memory shared by two caches must propogate to the other to maintain correctness

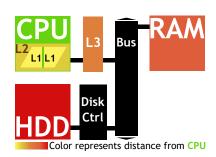


Figure: Distance of various forms of memory from CPU

Runqueues on Multiple Cores (Revisited)

Since process states are heaver than thread states, context switches between threads of different processes are more expensive

- If all cores shared one runqueue, access and changes to it would need to be synchronous and cache-coherent
- This would slow the system to a crawl
- So each core has its own runqueue and threads
- In order to best take advantage of available cores, the load on each of the core's runqueues must stay balanced.
- Most schedulers, including the CFS periodically run a load-balancing algorithm
- Explaining load-balancing depends on scheduling domains and groups. Don't have time to cover in this talk



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Bug fixes and two new schedulers CFS Bug Fixes Shuffler FLSched

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Bugs everywhere!

Four bugs from Lozi et al.[W]:

- The Overload-on-Wakeup bug
- The Missing Scheduling Domains bug



- The Scheduling Group Construction bug
- The Group Imbalance bug

These two heavily depend on definition of load balancing, scheduling domains, and scheduling groups

The Overload-on Wakeup bug

- Optimization in thread wakeup core where threads awakened by other threads are placed on the same core in hope to increase cache locality.
- However, it does this disregarding whether the thread should start on an idle core instead!
- This problem only occurs in environments where threads sleep frequently, such as database systems.

The Overload-on Wakeup bug

The fix for this bug was to modify thread wakeup code:

When a thread awakens by another, if the same core isn't busy it will go to that core, otherwise it goes to an idle core

Since this bug strongly effected database systems, Lozi et al. ran a database benchmark for a popular proprietary database called TPC-H.

Bug Fixes	TPC-H request #18	Full TPC-H benchmarl
None	55.9s	542.9s
Overload-on-Wakeup	43.5s (-22.2%)	471.1s (-13.2%)

Table: "Impact of the bug fixes for Overload-on-Wakeup on a popular commercial database (values averaged after five runs.)"

The Missing Scheduling Domains bug

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Shuffler

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FLSched

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Conclusions

- Added developmental plasticity to N-gram GP using Incremental Fitness-based Development (IFD).
- IFD consistently improved N-gram GP performance on suite of test problems.
- "Knocking out" IFD shows it's valuable in all phases, even if it wasn't used earlier in a run.
- IFD generates more complex, less converged probability tables.
- IFD generates more modules/loops & uses more low-probability paths.
- Currently exploring applications to dynamic environments.



Thanks!

Thank you for your time and attention!

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Questions?

References



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See the GECCO '09 paper for additional references.