

A

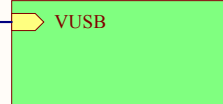
▲ Global Net:
- Vin
- 5V
- 3V3
- GND
Modules' number:
- MCU : 0
- USB : 1
- Pwr : 2
- Hed : 3
- uSD : 4
- Led&Btn : 5

B

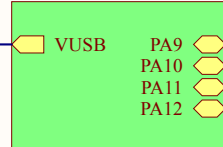
C

D

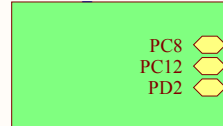
U_MB1-Pwr-r1
MB1-Pwr-r1.0.SchDoc



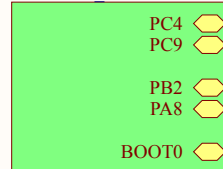
U_MB1-USB-r1
MB1-USB-r1.0.SchDoc



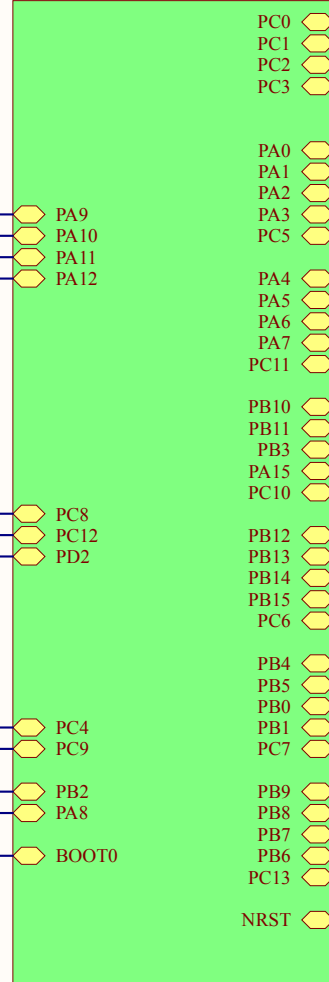
U_MB1-SD_CARD
MB1-SD_CARD.SchDoc



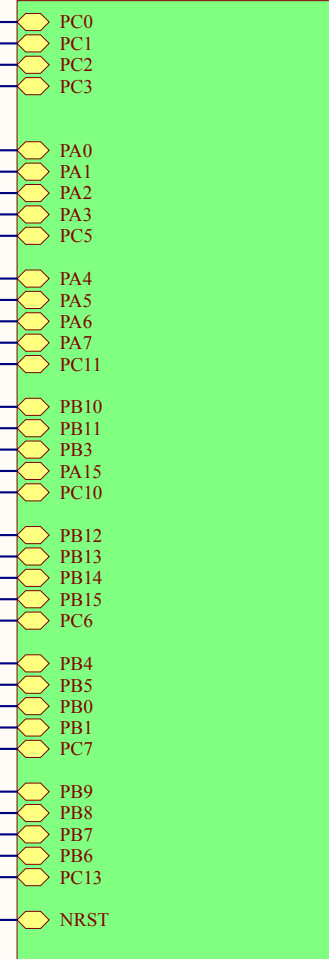
U_MB1-LED_BUTTON
MB1-LED_BUTTON.SchDoc



U_MB1-MCU-r1
MB1-MCU-r1.0.SchDoc



U_MB1-Hed-r1
MB1-Hed-r1.0.SchDoc



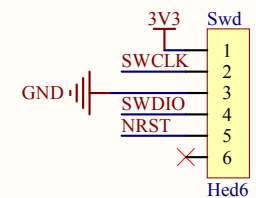
A

B

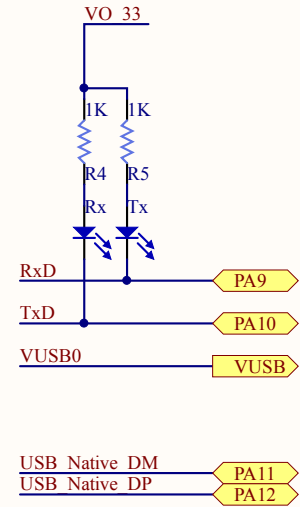
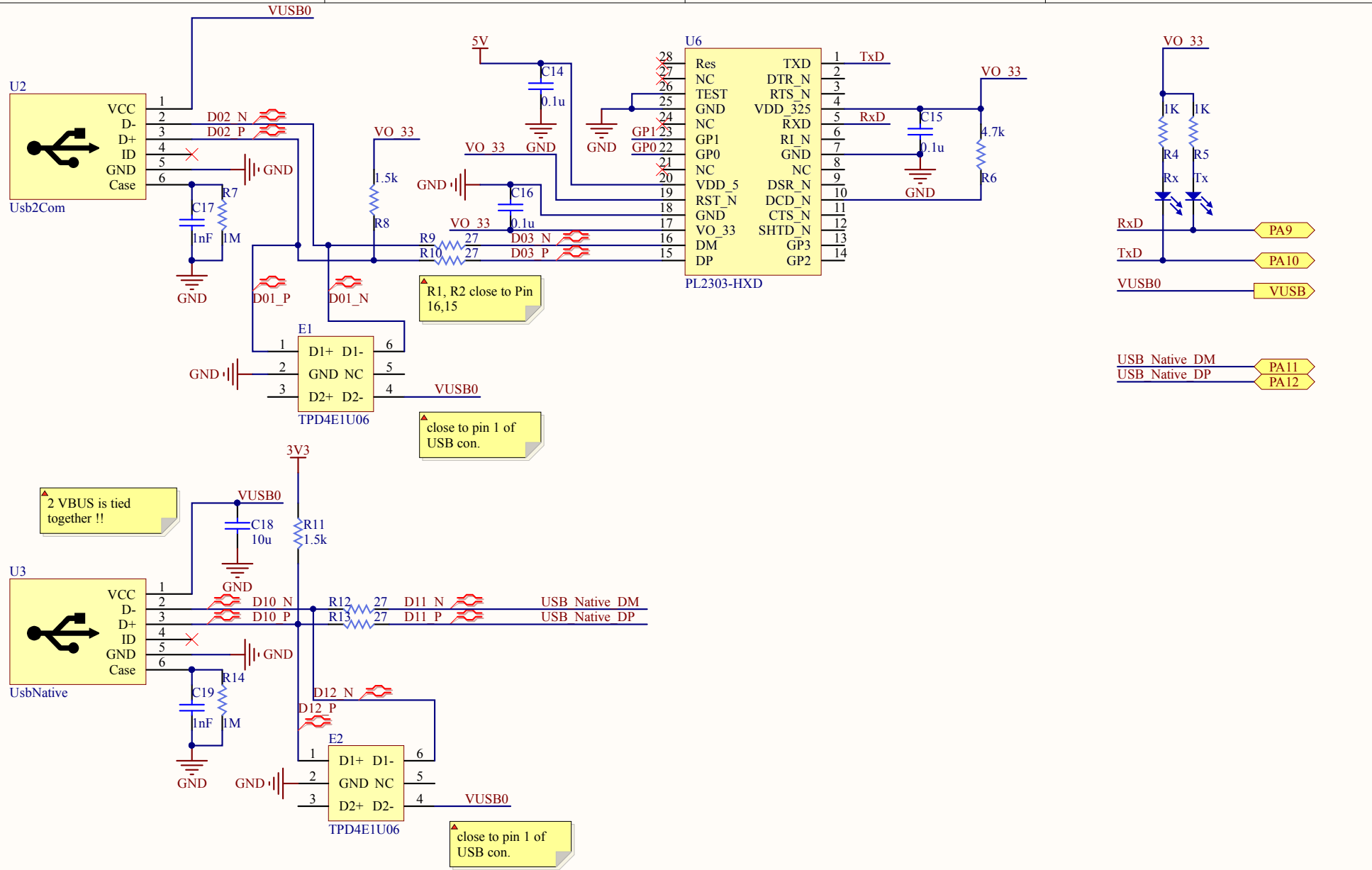
C

D

Title HLib - MBoard1		
Size A	Number	Revision 1.1
Date: 7/22/2015	Sheet 1 of 7	
File: C:\Users\...\MBoard1-R1.0.SchDoc	Drawn By: MBoard team	



Title STM32F103		
Size A	Number	Revision 1.1
Date:	7/22/2015	Sheet 2 of 7
File:	C:\Users\...MB1-MCU-r1.0.SchDoc	Drawn By: MBoard team



Title USB2Com & USB Native		
Size A	Number	Revision 1.1
Date: 7/22/2015	Sheet 3of 7	
File: C:\Users\...\MB1-USB-r1.0.SchDoc	Drawn By: MBoard team	

1

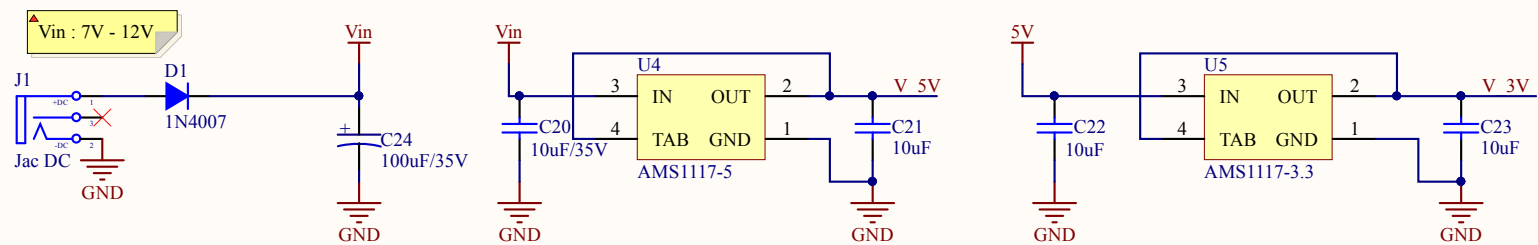
2

3

4

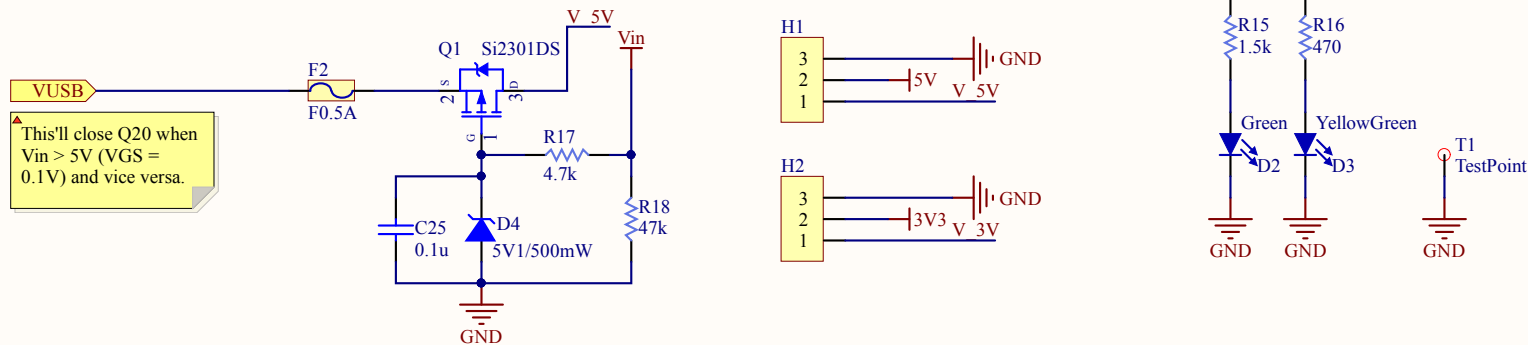
A

A



B

B



C

C

D

D

Title Power		
Size A	Number	Revision 1.1
Date:	7/22/2015	Sheet 4 of 7
File:	C:\Users\...\MB1-Pwr-r1.0.SchDoc	Drawn By: MBoard team

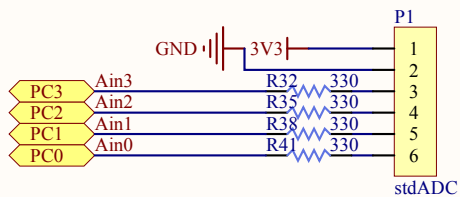
1

2

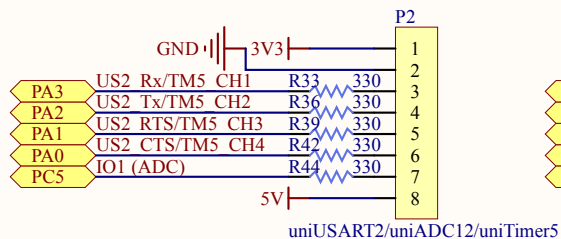
3

4

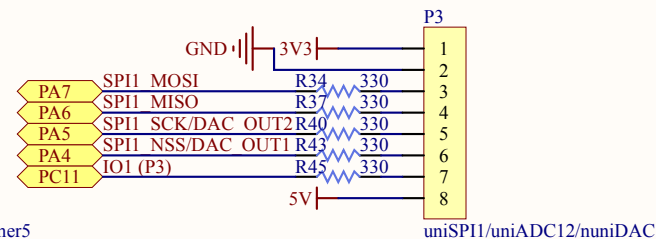
A



stdADC

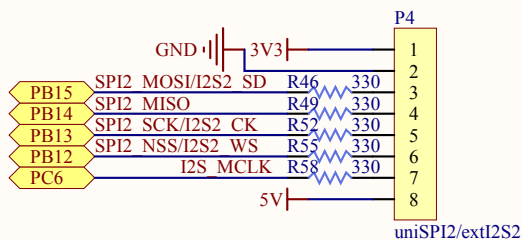


uniUSART2/uniADC12/uniTimer5

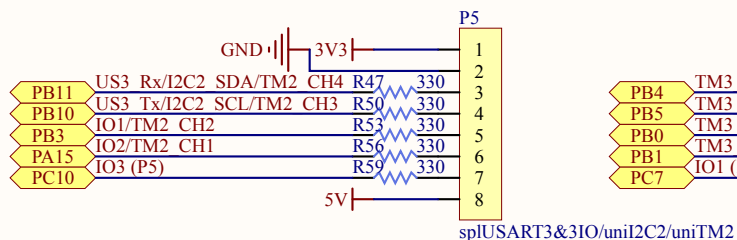


uniSPI1/uniADC12/uniDAC

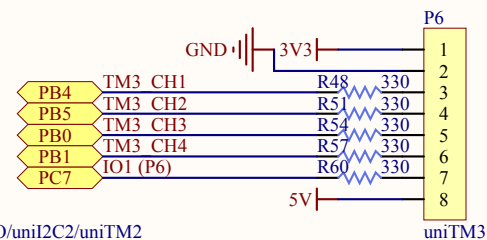
B



uniSPI2/extI2S2



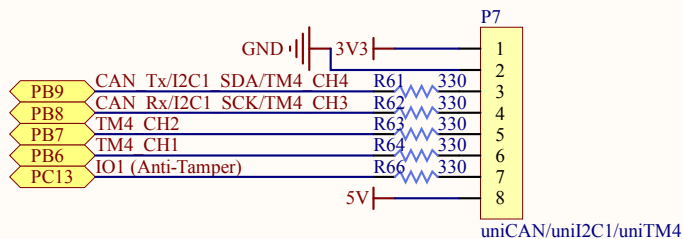
splUSART3&3IO/uniI2C2/uniTM2



uniTM3

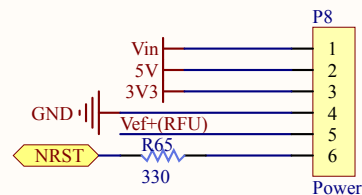
▲ TM2 has reversed order ! OK

C



uniCAN/uniI2C1/uniTM4

▲ TM4 has reversed order ! OK



Power

D

Title Headers

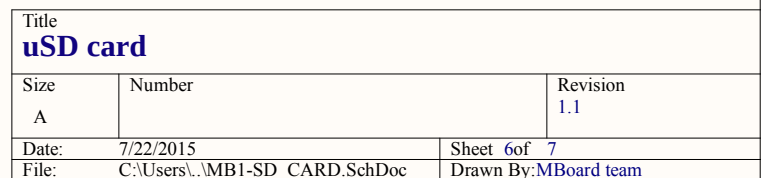
Size	Number	Revision
A		1.1
Date:	7/22/2015	Sheet 5of 7
File:	C:\Users\...\MB1-Hed-r1.0.SchDoc	Drawn By: MBoard Team

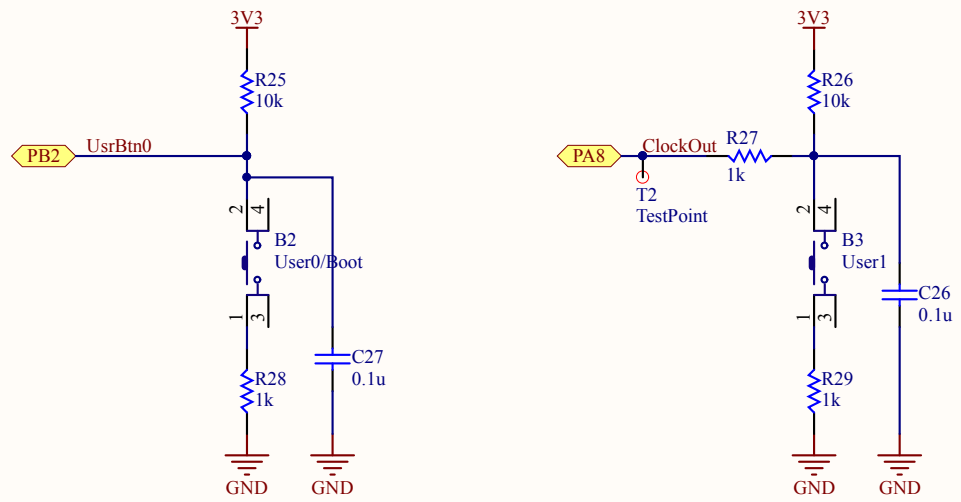
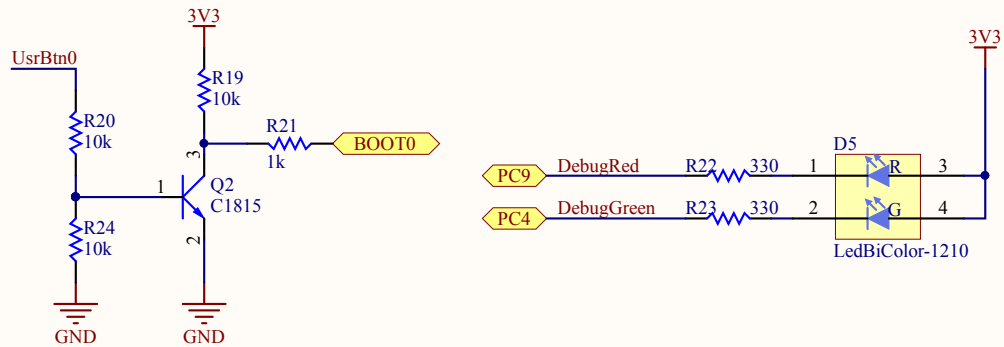
A

B

C

D





Title Leds & Buttons		
Size A	Number	Revision 1.1
Date: 7/22/2015	Sheet 7 of 7	
File: C:\Users\...MB1-LED_BUTTON.SchDoc	Drawn By: MBoard team	

