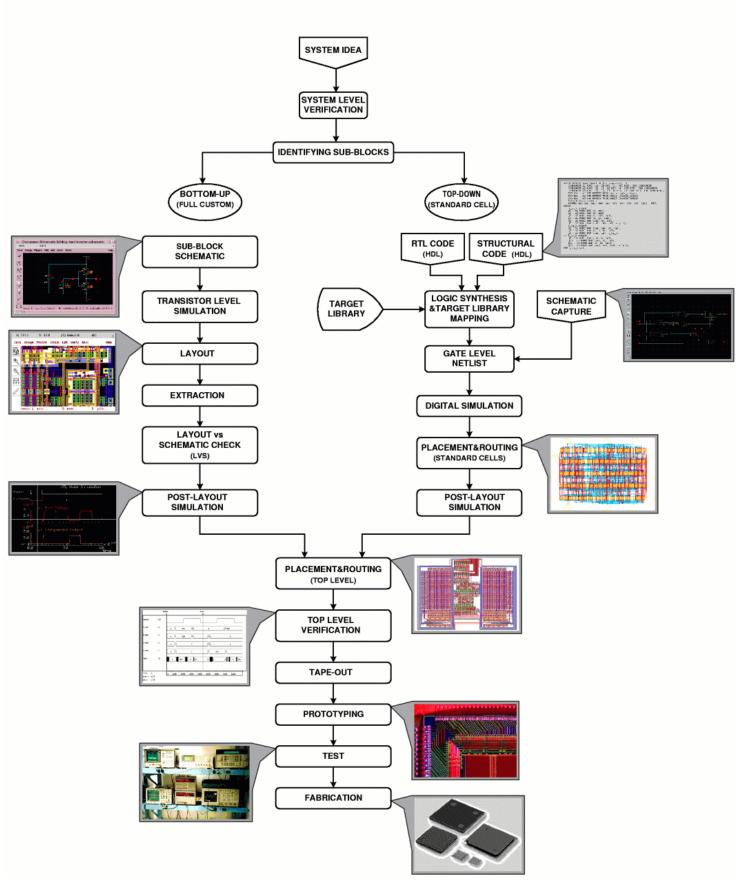
VLSI DESIGN FLOW



Ilhan HATIRNAZ 11/10/1998