	BASELINE	LOOP FUSION	LOOP INTERCHANGE
cycles	456,630,187,309	352,055,974,458	31,761,921,859
instructions	61,131,258,235	55,774,249,212	44,886,458,554
cache-misses	10,784,618,622	8,288,176,850	46,702,290
task-clock [msec]	135,997.80	104,800.99	9,491.75
clock frequency [GHz]	3.358	3.359	3.346
insn per second	0.13	0.16	1.41
CPUs utilized	1.000	1.000	1.000
time elapsed	136.03361311	104.8290996	9.49555139
seconds user	135.78948300	104.7205230	9.405289
seconds sys	0.208946	0.080978	0.086974

STRENGTH REDUCTION	DOUBLE BUFFER	CODE MOTION	STATIC BIG MATRIX	
31,955,399,767	25,975,165,024	24,366,546,148	24,882,994,103	
44,827,367,532	49,815,275,781	46,172,785,443	41,952,899,606	
45,149,626	15,171,065	11,934,386	13,949,362	
9,503.76	7,733.82	7,370.24	7,432.88	
3.362	3.359	3.306	3.348	
1.4	1.92	1.89	1.69	
1.000	1.000	1.000	1.000	
9.507661444	7.73732836	7.373770054	7.436169378	
9.446708	7.579452	7.316808	7.413418	
0.05698	0.154968	0.053998	0.019995	