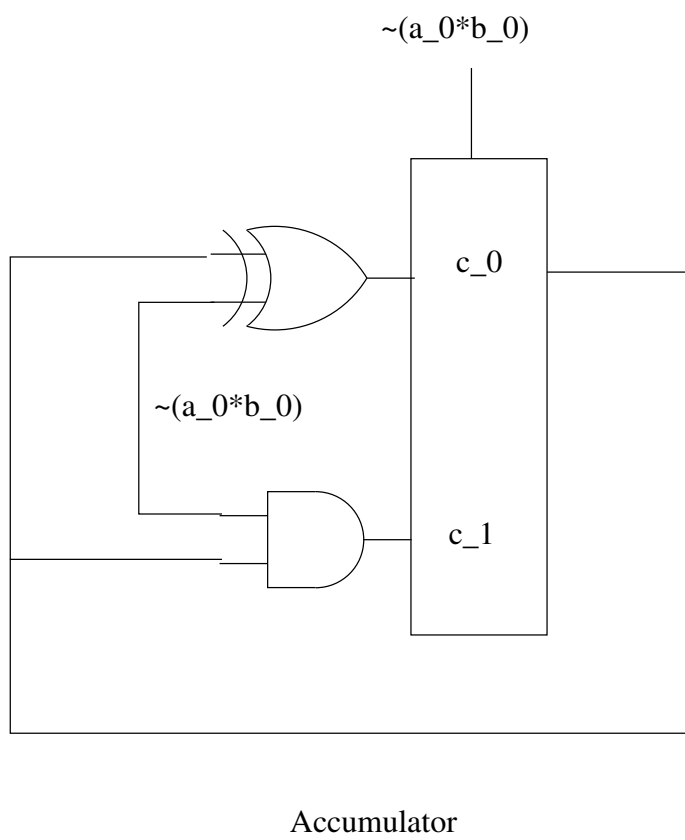
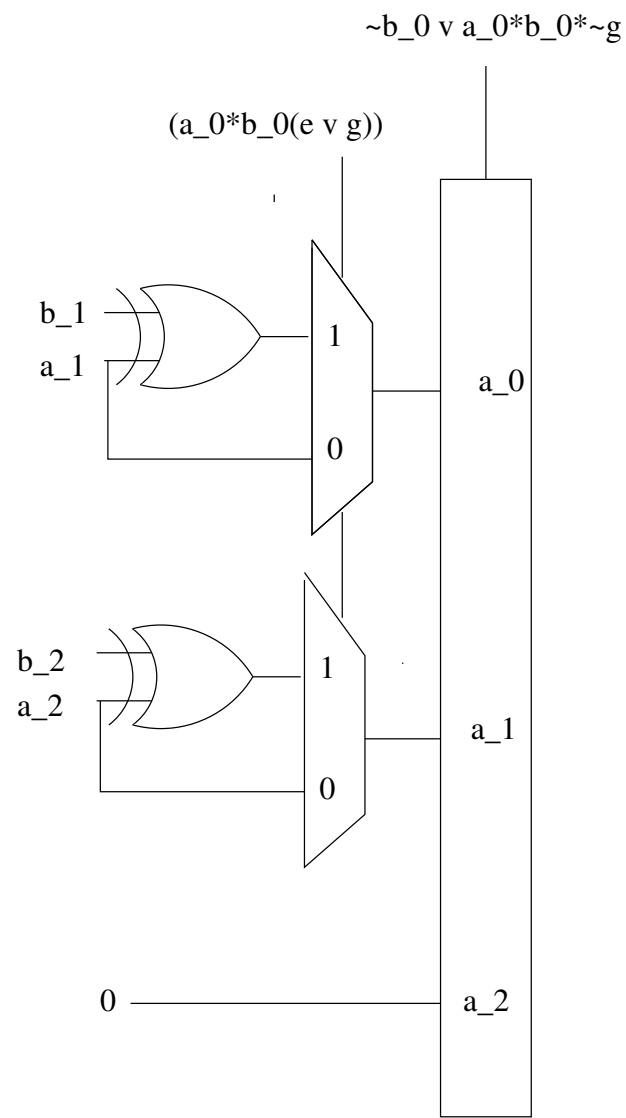
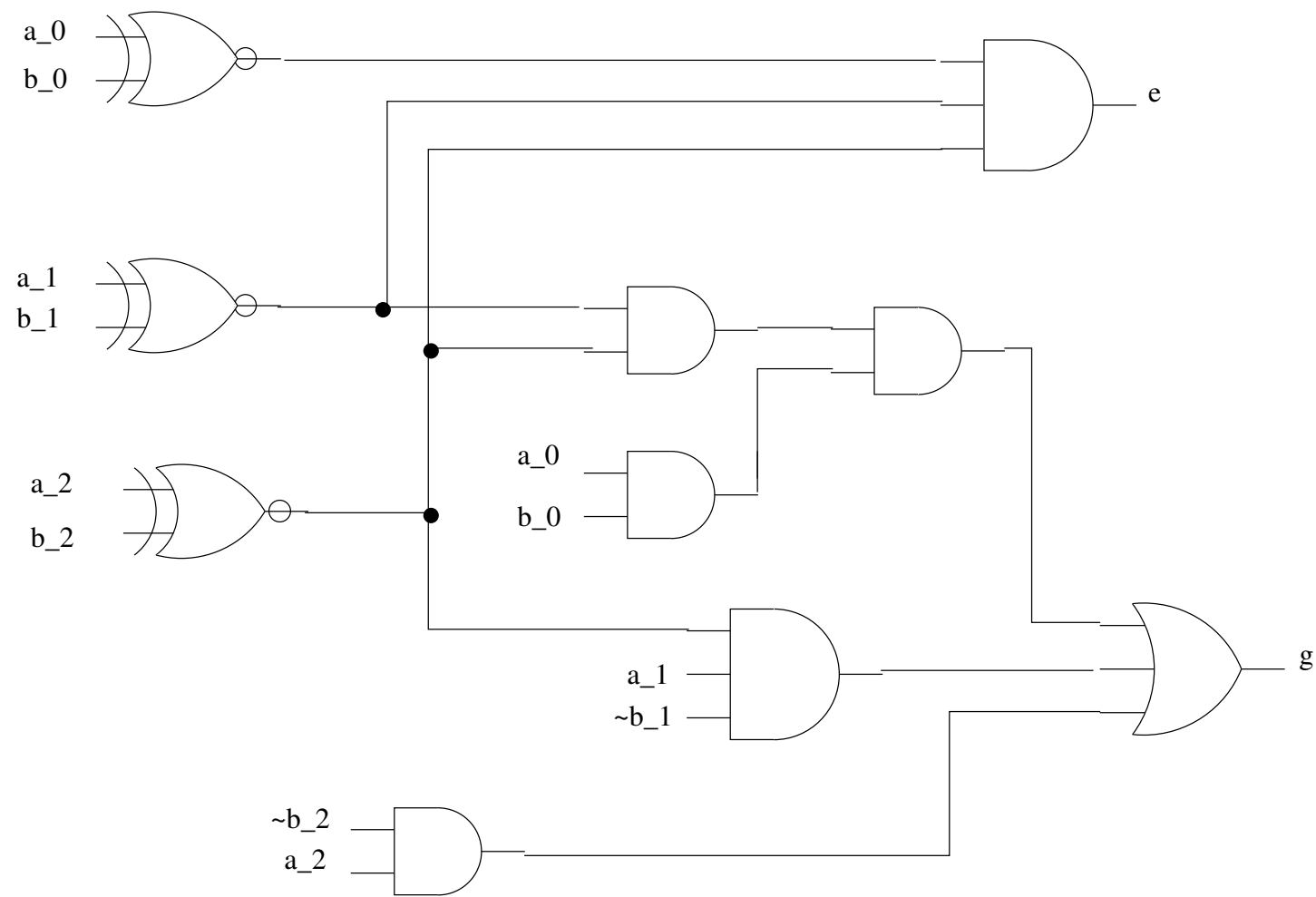


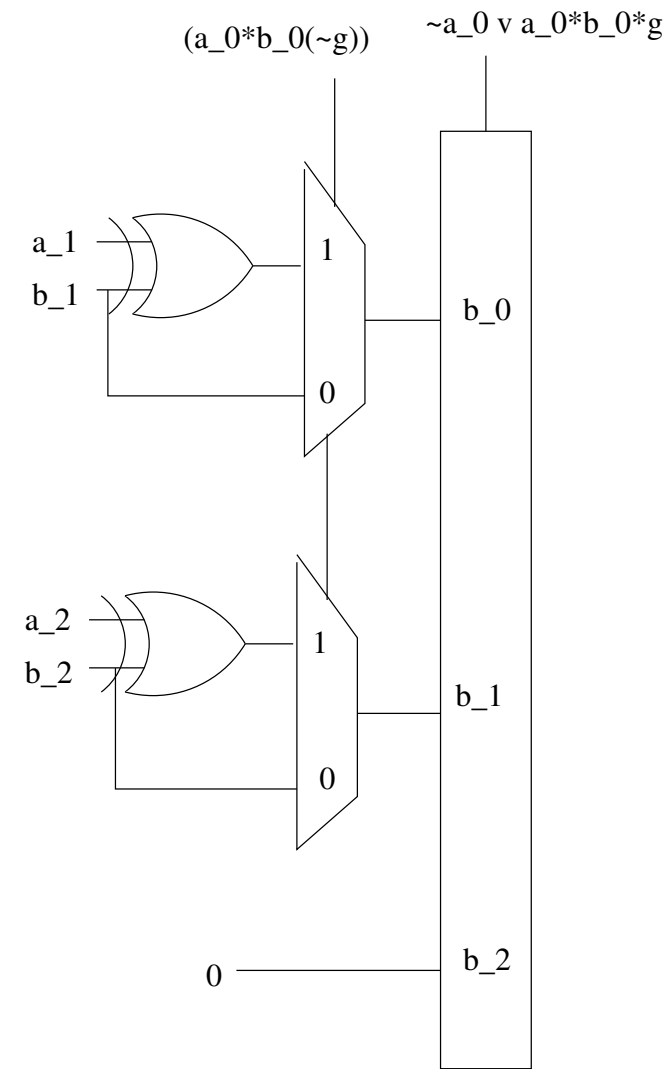
B Register Shifter



Comparator Circuit



A Register Shifter



Output GCD Register

